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// Analog LCD Example
// Jason Losh

//-----
// Hardware Target
//-----

// Target Platform: EK-TM4C123GXL with LCD/Temperature Sensor
// Target uC:   TM4C123GH6PM
// System Clock: 40 MHz

// Hardware configuration:
// Red Backlight LED:
//   PE5 drives an NPN transistor that powers the red LED
// Green Backlight LED:
//   PE5 drives an NPN transistor that powers the green LED
// Blue Backlight LED:
//   PE4 drives an NPN transistor that powers the blue LED
// LM60 Temperature Sensor:
//   AN0/PE3 is driven by the sensor (Vout = 424mV + 6.25mV / degC with +/-2degC uncalibrated error)
// ST7568R Graphics LCD Display Interface:
//   MOSI (SS12) on PB7
//   MISO (SS12Rx) is not used by the LCD display but the pin is used for GPIO for AD0
//   SCLK (SS12Clk) on PB4
//   AD0 connected to PB6
//   ~CS connected to PB1
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//-----
// Device includes, defines, and assembler directives
//-----
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```
#include <stdint.h>
#include <stdio.h>
#include <stdbool.h>
#include <string.h>
#include "tm4c123gh6pm.h"
#include "graphics_lcd.h"
#include "wait.h"

#define RED_LED (*(volatile uint32_t *) (0x42000000 + (0x400053FC-0x40000000)*32 + 5*4))
#define GREEN_LED (*(volatile uint32_t *) (0x42000000 + (0x400243FC-0x40000000)*32 + 5*4))
#define BLUE_LED (*(volatile uint32_t *) (0x42000000 + (0x400243FC-0x40000000)*32 + 4*4))
```

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//-----
// Global variables
//-----
```

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//-----
// Subroutines
//-----
```

```
// Initialize Hardware
void initHw()
{
    // Configure HW to work with 16 MHz XTAL, PLL enabled, system clock of 40 MHz
    SYSCTL_RCC_R = SYSCTL_RCC_XTAL_16MHZ | SYSCTL_RCC_OSCSRC_MAIN | SYSCTL_RCC_USESYSDIV | (4 << SYSCTL_RCC_SYSDIV_5);

    // Set GPIO ports to use APB (not needed since default configuration -- for clarity)
    // Note UART on port A must use APB
    SYSCTL_GPIOHCTL_R = 0;

    // Enable GPIO port B and E peripherals
    SYSCTL_RCGC2_R = SYSCTL_RCGC2_GPIOB | SYSCTL_RCGC2_GPIOE;

    // Configure three backlight LEDs
    GPIO_PORTB_DIR_R |= 0x20; // make bits 5 an output
    GPIO_PORTB_DR2R_R |= 0x20; // set drive strength to 2mA
```

```
setGraphicsLcdTextPosition(0, 1);
putsGraphicsLcd("Unfiltered (C)");
setGraphicsLcdTextPosition(0, 2);
putsGraphicsLcd("Filtered (C)");

// Display raw ADC value and temperatures
uint16_t raw;
float instantTemp, irTemp;
char str[10];
float alpha = 0.99;
int firstUpdate = true;
while(1)
{
    // Read sensor
    raw = readAdc(531);

    // Calculate temperature in degC as follows:
    // For the 12-bit SAR ADC with Vref+ = 3.3V and Vref- = 0V, outputing a result R:
    // Resolution is approx 0.81mV / LSB or 0.13 degC / LSB
    // R(Vin) = floor(Vin(3.3V * 4096) >= Vin(0)) ~ 3.3V * ((R-0.5) / 4096)
    // (~ = 0.5LSb offset in Vin(R) equation are introduced for mid-tread value of the SAR transfer function)
    // T(Vin) = (Vin - 0.424V) / 0.00625V
    // T(R) ~ ((3.3V * ((R-0.5) / 4096)) - 0.424V) / 0.00625V
    // T(R) ~ (0.12890625 * R) - 67.77546875 (simplified floating point equation to save cycles)
    instantTemp = ((raw / 4096.0 * 3.3) - 0.424) / 0.00625;
    // First order IIR filter
    // In the z-domain:
    // H(z) = sum(j = 0..M) {b_j * z^(-j)}
    // -----
    // sum(i = 0..N) {a_i * z^(-i)}
    // Setting a0 = 1, yields:
    // H(z) = sum(j = 0..M) {b_j * z^(-j)}
    // -----
    // 1 + sum(i = 1..N) {a_i * z^(-i)}
    // for N = 1, M = 0
    // H(z) = b0 / (1 + a1 * z^(-1))
    // Given IIR difference equation:
    // sum(i = 0..N) {a_i * y(n-i)} + sum(j = 0..M) {b_j * x(n-j)}
    // Separating y(n), rearranging and inverting signs of a(1-N), yields
    // a0 * y(n) + sum(i = 1..N) {a_i * y(n-i)} + sum(j = 0..M) {b_j * x(n-j)}
    // for N = 1, M = 0, and a0 = 1,
    // y(n) = b0 * x(n) + a1 * y(n-1)
    // Setting b0 = (1-a1), yields
    // y(n) = alpha * y(n-1) + (1-alpha) * x(n)
    // Adding an exception for the first sample, yields:
    // y(n) = x(n); n = 0
    // y(n) = alpha * y(n-1) + (1-alpha) * x(n); n > 0
    if (firstUpdate)
    {
        irTemp = instantTemp;
        firstUpdate = false;
    }
    else
        irTemp = irTemp * alpha + instantTemp * (1-alpha);

    // display raw ADC value and temperatures
    sprintf(str, "%u", raw);
    setGraphicsLcdTextPosition(100, 0);
    putsGraphicsLcd(str);
    sprintf(str, "%3.1f", instantTemp);
    setGraphicsLcdTextPosition(100, 1);
    putsGraphicsLcd(str);
    sprintf(str, "%3.1f", irTemp);
    setGraphicsLcdTextPosition(100, 2);
    putsGraphicsLcd(str);
    waitMicroseconds(500000);
}
```

```
GPIO_PORTB_DEN_R |= 0x20; // enable bits for digital
GPIO_PORTB_DIR_R |= 0x30; // make bits 4 and 5 outputs
GPIO_PORTB_DR2R_R |= 0x30; // set drive strength to 2mA
GPIO_PORTB_DEN_R |= 0x30; // enable bits 4 and 5 for digital
```

```
// Configure AD and ~CS for graphics LCD
GPIO_PORTB_DIR_R |= 0x42; // make bits 1 and 6 outputs
GPIO_PORTB_DR2R_R |= 0x42; // set drive strength to 2mA
GPIO_PORTB_DEN_R |= 0x42; // enable bits 1 and 6 for digital

// Configure SS12 pins for SPI configuration
SYSCTL_RCGCSSI_R |= SYSCTL_RCGCSSI_R2; // turn-on SS12 clocking
GPIO_PORTB_DIR_R |= 0x90; // make bits 4 and 7 outputs
GPIO_PORTB_DR2R_R |= 0x90; // set drive strength to 2mA
GPIO_PORTB_AFSEL_R |= 0x90; // select alternative functions for MOSI, SCLK pins
GPIO_PORTB_PCTL_R = GPIO_PCTL_PB7_SS12TX | GPIO_PCTL_PB4_SS12CLK; // map alt fns to SS12
GPIO_PORTB_DEN_R |= 0x90; // enable digital operation on TX, CLK pins
```

```
// Configure the SS12 as a SPI master, mode 3, 8bit operation, 1 MHz bit rate
SS12_CR1_R &= ~SSI_CR1_SSE; // turn off SS12 to allow re-configuration
SS12_CR1_R = 0; // select master mode
SS12_CC_R = 0; // select system clock as the clock source
SS12_CPSR_R = 40; // set bit rate to 1 MHz (if SR=0 in CR0)
SS12_CR0_R = SSI_CR0_SPH | SSI_CR0_SPO | SSI_CR0_FRF_MOTO | SSI_CR0_OSS_R; // set SR=0, mode 3 (SPH=1, SPO=1), 8-bit
SS12_CR1_R |= SSI_CR1_SSE; // turn on SS12
```

```
// Configure AN0 as an analog input
SYSCTL_RCGCACDC_R |= 1; // turn on ADC module 0 clocking
GPIO_PORTB_AFSEL_R |= 0x08; // select alternative functions for AN0 (PE3)
GPIO_PORTB_DIR_R &= ~0x08; // turn off digital operation on pin PE3
GPIO_PORTB_AMSEL_R |= 0x08; // turn on analog operation on pin PE3
ADDC_CC_R = ADC_CC_CS_SYSPCLK; // select PLL as the time base (not needed, since default value)
ADDC_ACTSS_R &= ~ADDC_ACTSS_ASEN3; // disable sample sequencer 3 (SS3) for programming
ADDC_EMUX_R = ADC_EMUX_EM3_PROCESSOR; // select SS3 bit in ADOPSS as trigger
ADDC_SSMUX3_R = 0; // set first sample to AN0
ADDC_SSCTL3_R = ADC_SSCTL3_END0; // mark first sample as the end
ADDC_ACTSS_R |= ADC_ACTSS_ASEN3; // enable SS3 for operation
```

```
int16_t readAdc(531)
{
    ADCD_PSSI_R |= ADCD_PSSI_SS3; // set start bit
    while (ADCD_ACTSS_R & ADCD_ACTSS_BUSY); // wait until SS3 is not busy
    return ADCD_SSIF03_R; // get single result from the FIFO
}
```

```
//-----
// Main
//-----
```

```
int main(void)
{
    // Initialize hardware
    initHw();

    // Turn-on all LEDs to create white backlight
    RED_LED = 1;
    GREEN_LED = 1;
    BLUE_LED = 1;

    // Initialize graphics LCD
    initGraphicsLcd();

    // Draw legend
    setGraphicsLcdTextPosition(0, 0);
    putsGraphicsLcd("Raw ADC");
}
```