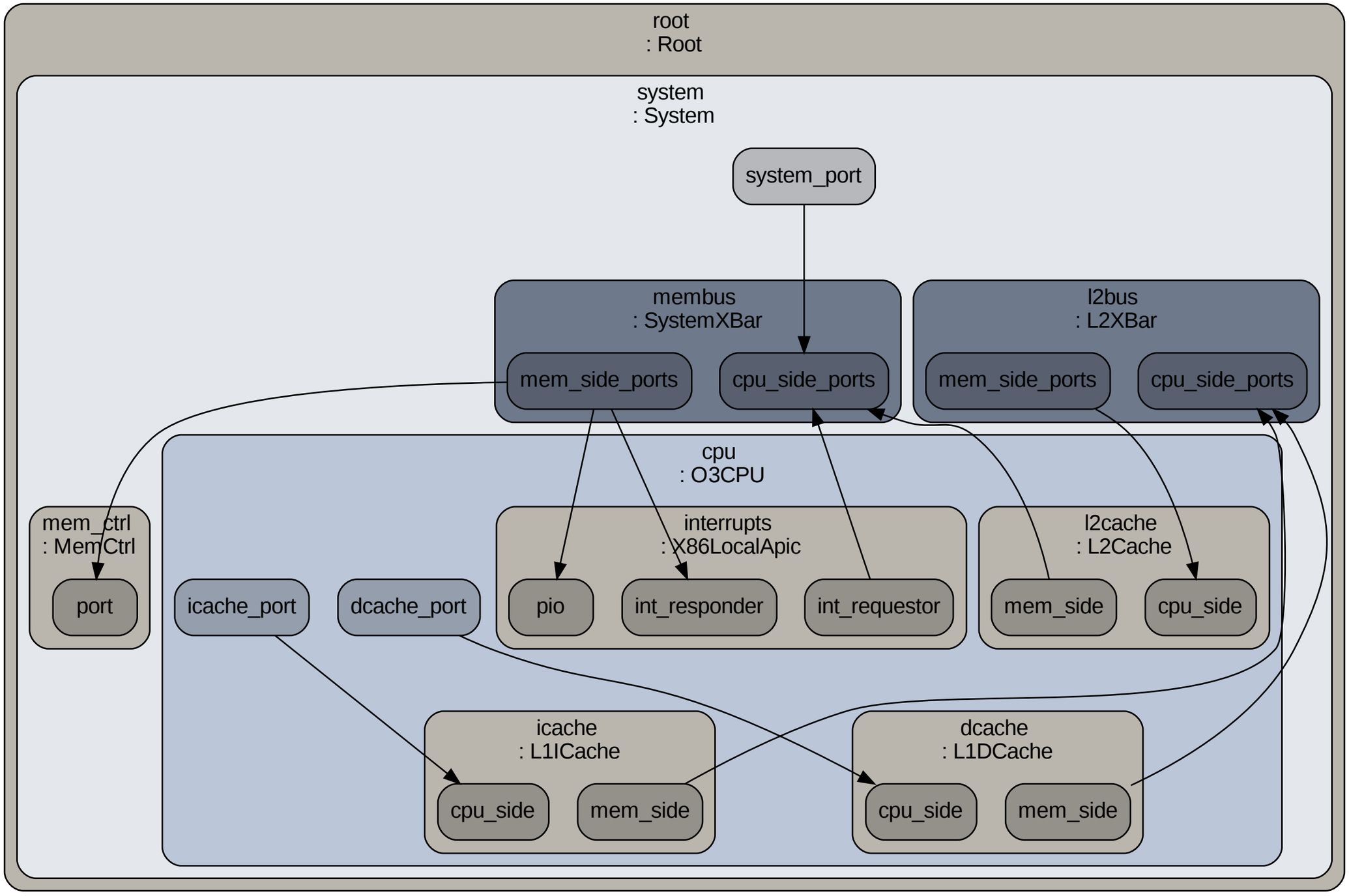
root
: Root
system
: System
system_port
membus
: SystemXBar
mem_side_ports
cpu_side_ports
l2bus
: L2XBar
mem_side_ports
cpu_side_ports
cpu
: O3CPU
mem_ctrl
: MemCtrl
port
interrupts
: X86LocalApic
pio
int_responder
int_requestor
l2cache
: L2Cache
mem_side
cpu_side
icache_port
dcache_port
icache
: L1ICache
cpu_side
mem_side
dcache
: L1DCache
cpu_side
mem_side