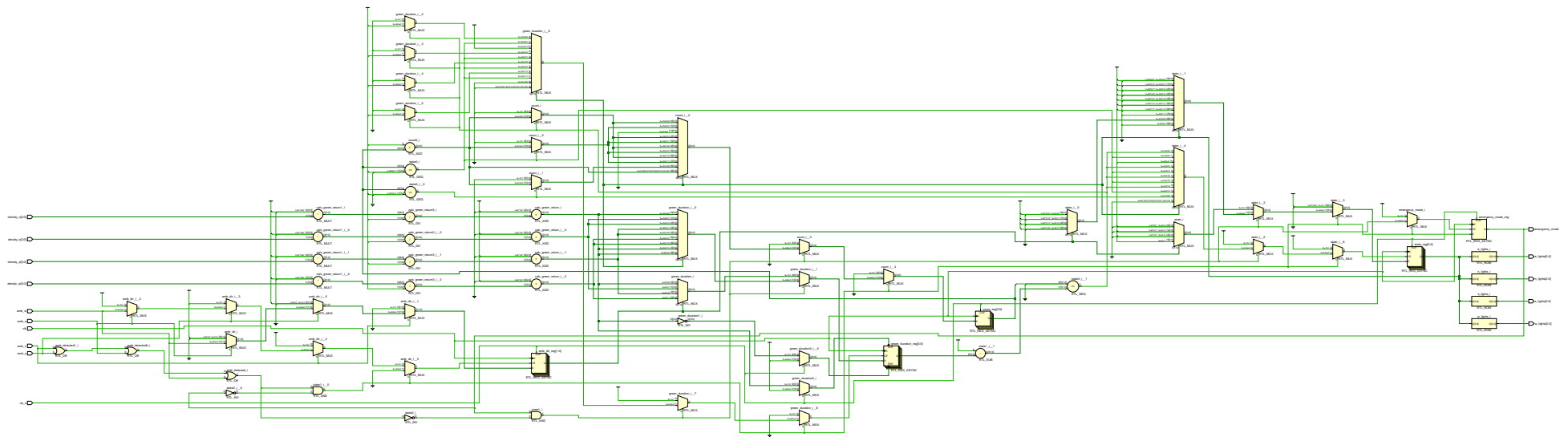


Schematic Diagram for FPGA-Based Adaptive Traffic Light Controller with Emergency Vehicle Prioritization



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