

1. As the production company I want to be able to produce the lane detection system as economically as possible
 - 1.1. The system is formally verified and implemented on an FPGA – *Maturity*
 - 1.2. Faulty systems can be fixed by only replacing the FPGA – *Replaceability*
 - 1.3. The digital logic layer is compact enough to fit on the Artix 7 XC7A35T chip family – *Resource utilization*
 - 1.4. The system interacts with the existing OV7670 camera module – *Adaptability*