

Commits on Mar 30, 2022

Write resource utilization reports in technical design



hypothermic committed on Mar 30

Verified



d9db253

<>

Commits on Mar 29, 2022

Add place&route figures to technical design



hypothermic committed on Mar 29 ✓

Verified



654e2c9

<>

Add package and address mapping diagrams



hypothermic committed on Mar 29 ✗

Verified



62d118b

<>

Write paragraph Implementation Strategy



hypothermic committed on Mar 29 ✓

Verified



cd7d0e9

<>

Commits on Mar 28, 2022

Add enlarged version of block diagram as appendix entry



hypothermic committed on Mar 28 ✓

Verified



97d871f

<>

Update design document with a beautiful figure of the block design



hypothermic committed on Mar 28 ✗

Verified



080129e

<>

Add s_axilite interface to VPU to control it via DMA



hypothermic committed on Mar 28

Verified



bd06d79

<>

Fix IP core output location



hypothermic committed on Mar 28 ✓

Verified



bf07f4f

<>

HLS IP Core export works



hypothermic committed on Mar 28

Verified



6fdeef2

<>