

Computational Flow and Complexity Analysis of Multi-Head Attention Mechanism

Understanding Operations, Dimensions, and Computational Costs

Technical Report

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Abstract

This paper provides a systematic analysis of the Multi-Head Attention (MHA) mechanism, the core component of Transformer-based large language models (LLMs), from a computational graph perspective. We begin by mathematically defining and characterizing fundamental operations—matrix multiplication, element-wise operations, softmax, layer normalization, transpose, and reshape—that are essential for understanding MHA diagrams. We then trace the complete data flow of both forward and backward passes step-by-step, quantitatively deriving the time complexity (FLOPs) and space complexity (memory) at each stage. Special attention is given to analyzing how the $O(S^2)$ complexity of self-attention affects long sequence processing, and we discuss practical optimization strategies including FlashAttention, Multi-Query Attention, and gradient checkpointing.

Keywords Transformer; Multi-Head Attention; Computational Complexity; Memory Analysis; Forward Pass; Backward Pass; Gradient Computation; LLM Optimization.

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1 Introduction

The Transformer architecture [1] has become the foundation of modern natural language processing and large language models (LLMs). State-of-the-art models such as the GPT series, BERT, and LLaMA all utilize the Multi-Head Attention (MHA) mechanism as their core component, and the performance and efficiency of these models heavily depend on the effective implementation of MHA.

1.1 Motivation

As the scale of LLMs increases dramatically (e.g., GPT-3 with 175B parameters, GPT-4 with an estimated 1.7T parameters), accurate understanding of computational and memory costs for each operation has become essential. Key considerations include:

- **Increasing sequence length S :** As context windows expand from 4K \rightarrow 32K \rightarrow 128K, self-attention with $O(S^2)$ complexity becomes the primary bottleneck
- **Growing model dimension D :** Larger embedding dimensions increase the computational cost of linear projections
- **Batch processing:** Optimizing batch size B to maximize GPU utilization
- **Gradient computation:** Memory requirements for the backward pass are 2-3x higher than the forward pass

1.2 Contributions

The contributions of this paper are as follows:

1. **Computational graph-based analysis:** Visualizing MHA data flow through graphs with nodes (operations) and edges (tensors)
2. **Detailed operation analysis:** Mathematical definitions and complexity derivations for each primitive operation (MatMul, Add, Softmax, LayerNorm, etc.)
3. **Forward & Backward pass analysis:** Step-by-step FLOPs and memory analysis for forward and backward propagation
4. **Scaling laws:** Characterizing complexity scaling with respect to B , S , D , and N_H
5. **Optimization strategies:** Discussion of practical optimization techniques including FlashAttention and KV caching

1.3 Organization

The paper is organized as follows:

- **Section 2:** Computational graph notation and visual conventions
- **Section 3:** Mathematical definitions and characteristics of basic operations
- **Section 4:** Step-by-step complexity analysis of the forward pass
- **Section 5:** Gradient computation analysis of the backward pass
- **Section 6:** Overall complexity summary and optimization strategies

2 Computational Graph Notation

2.1 Graph Structure: Nodes and Edges

Neural network computation can be represented as a Directed Acyclic Graph (DAG), where:

- **Nodes:** Represent operations
- **Edges:** Represent tensor data flow
- **Edge labels:** Indicate tensor names and shapes (e.g., $\mathbf{X} [B, S, D]$)

2.2 Node Types

The following node types are used in our diagrams:

Matrix Multiplication \odot

- **Symbol:** Circular node with \bullet symbol
- **Meaning:** Matrix multiplication of two tensors
- **Inputs:** Two tensors (one marked with double arrow)
- **Output:** Product tensor

Element-wise Addition \oplus

- **Symbol:** Circular node with $+$ symbol
- **Meaning:** Element-wise addition of two tensors (broadcasting allowed)
- **Usage:** Residual connections, bias addition

Auxiliary Operations $\boxed{\text{LN}}$, $\boxed{\text{SM}}$, $\boxed{\text{S}}$, $\boxed{\text{T}}$, $\boxed{\text{R}}$, $\boxed{\text{C}}$, $\boxed{\text{DO}}$

- **Symbol:** Rectangular node with operation abbreviation
- **Types:**
 - $\boxed{\text{LN}}$: Layer Normalization
 - $\boxed{\text{SM}}$: Scale + Mask (for attention scores)
 - $\boxed{\text{S}}$: Softmax
 - $\boxed{\text{T}}$: Transpose
 - $\boxed{\text{R}}$: Reshape (head split/merge)
 - $\boxed{\text{C}}$: Concatenate
 - $\boxed{\text{DO}}$: Dropout

Reduction Operations \oplus

- **Symbol:** Small circle with \sum symbol
- **Meaning:** Summation over specific axes (e.g., for bias gradient computation)
- **Label:** Indicates reduction dimensions (e.g., $\sum_{B,S}$)

2.3 Edge Conventions

Single Arrow \rightarrow Represents general data flow. In forward pass, carries activations; in backward pass, carries gradients.

Double Arrow \Rightarrow Explicitly marks the second operand (typically weight matrix) of matrix multiplication

⊙. This clarifies that W is the right operand in $Z = XW$.

Example:

- Forward: $\mathbf{X} \rightarrow \odot \Leftarrow \mathbf{W} \rightarrow \mathbf{Z}$
- Meaning: $\mathbf{Z} = \mathbf{XW}$

2.4 Shape Notation

All tensors are annotated with their shapes:

- B : Batch size
- S : Sequence length
- D : Model dimension
- N_H : Number of attention heads
- D_h : Head dimension ($D_h = D/N_H$)
- V : Vocabulary size

Broadcasting notation: $\text{BC}_{B,S}(\tilde{\mathbf{b}})$ indicates that a bias of shape $[D]$ is broadcast to shape $[B, S, D]$.

3 Understanding Basic Operations

3.1 Matrix Multiplication

3.1.1 Mathematical Definition

Matrix multiplication of $\mathbf{A} \in \mathbb{R}^{m \times k}$ and $\mathbf{B} \in \mathbb{R}^{k \times n}$:

$$\mathbf{C} = \mathbf{AB}, \quad C_{ij} = \sum_{p=1}^k A_{ip} B_{pj} \quad (1)$$

3.1.2 Batched Matrix Multiplication

With additional batch dimension: $\mathbf{A} \in \mathbb{R}^{[B, m, k]}$, $\mathbf{B} \in \mathbb{R}^{[B, k, n]}$

$$\mathbf{C}[b, :, :] = \mathbf{A}[b, :, :] \mathbf{B}[b, :, :] \quad \text{for each } b \in [1, B] \quad (2)$$

3.1.3 Computational Cost (FLOPs)

Single matrix multiplication $[m, k] \times [k, n] \rightarrow [m, n]$:

- Per output element: k multiplications + $(k - 1)$ additions $\approx 2k$ FLOPs
- Total output elements: $m \times n$
- **Total FLOPs:** $2mnk$

Batched matrix multiplication $[B, m, k] \times [B, k, n]$:

$$\text{FLOPs} = B \cdot 2mnk = 2Bmnk \quad (3)$$

3.1.4 Memory Requirements

- Inputs: $Bmk + Bkn$ elements
- Output: Bmn elements
- **Total:** $B(mk + kn + mn)$ (multiply by 4 for float32 bytes)

3.1.5 Backward Pass

Forward: $\mathbf{Z} = \mathbf{XW}$ where $\mathbf{X} \in \mathbb{R}^{[B, S, D_{\text{in}}]}$, $\mathbf{W} \in \mathbb{R}^{[D_{\text{in}}, D_{\text{out}}]}$

Gradients with respect to inputs:

$$\frac{\partial L}{\partial \mathbf{X}} = \frac{\partial L}{\partial \mathbf{Z}} \mathbf{W}^\top \quad [B, S, D_{\text{out}}] \times [D_{\text{out}}, D_{\text{in}}] \quad (4)$$

$$\frac{\partial L}{\partial \mathbf{W}} = \mathbf{X}^\top \frac{\partial L}{\partial \mathbf{Z}} \quad [D_{\text{in}}, B \cdot S] \times [B \cdot S, D_{\text{out}}] \quad (5)$$

Each gradient computation also involves matrix multiplication with similar FLOPs.

3.2 Element-wise Addition

3.2.1 Mathematical Definition

For two tensors $\mathbf{A}, \mathbf{B} \in \mathbb{R}^{[d_1, d_2, \dots, d_n]}$ with the same shape:

$$\mathbf{C} = \mathbf{A} + \mathbf{B}, \quad C_{i_1, i_2, \dots, i_n} = A_{i_1, i_2, \dots, i_n} + B_{i_1, i_2, \dots, i_n} \quad (6)$$

3.2.2 Broadcasting

NumPy/PyTorch broadcasting rules:

- Shape $[B, S, D] + \text{shape } [D] \rightarrow$ bias broadcast over (B, S)
- Shape $[B, S, D] + \text{shape } [B, S, D] \rightarrow$ element-wise addition

Example: Bias addition

$$\mathbf{Y} = \mathbf{X} + \text{BC}_{B, S}(\tilde{\mathbf{b}}), \quad \mathbf{Y}[b, s, :] = \mathbf{X}[b, s, :] + \tilde{\mathbf{b}} \quad (7)$$

3.2.3 Computational Cost

Element-wise addition:

$$\text{FLOPs} = \text{total number of elements} = \prod_i d_i \quad (8)$$

Example: Addition of $[B, S, D]$ tensors requires BSD FLOPs

3.2.4 Memory

- Inputs: $2 \times \text{size}$ (smaller tensor reused with broadcasting)
- Output: size

3.2.5 Backward Pass

Forward: $\mathbf{C} = \mathbf{A} + \mathbf{B}$

Gradients:

$$\frac{\partial L}{\partial \mathbf{A}} = \frac{\partial L}{\partial \mathbf{C}} \quad (9)$$

$$\frac{\partial L}{\partial \mathbf{B}} = \frac{\partial L}{\partial \mathbf{C}} \quad (10)$$

With broadcasting, sum gradient over broadcast dimensions:

$$\frac{\partial L}{\partial \tilde{\mathbf{b}}} = \sum_{b=1}^B \sum_{s=1}^S \frac{\partial L}{\partial \mathbf{C}}[b, s, :] \quad (\text{sum over } B, S) \quad (11)$$

This is represented by a \oplus node in the diagram.

3.3 Softmax

3.3.1 Mathematical Definition

For vector $\mathbf{x} = [x_1, x_2, \dots, x_n]$:

$$\text{softmax}(\mathbf{x})_i = \frac{e^{x_i}}{\sum_{j=1}^n e^{x_j}} \quad (12)$$

For multi-dimensional tensors, applied along a specific axis (typically the last axis).

Usage in MHA: For attention scores $\mathbf{A} \in \mathbb{R}^{[B, N_H, S, S]}$, apply softmax along the last axis (key dimension):

$$\mathbf{AS}[b, h, i, :] = \text{softmax}(\mathbf{A}[b, h, i, :]) \quad (13)$$

3.3.2 Numerical Stability

In practice, use the log-sum-exp trick to prevent overflow:

$$\text{softmax}(\mathbf{x})_i = \frac{e^{x_i - \max(\mathbf{x})}}{\sum_j e^{x_j - \max(\mathbf{x})}} \quad (14)$$

3.3.3 Computational Cost

Softmax over dimension of size n :

- Max computation: n comparisons
- Exp computation: n exponentials
- Sum computation: n additions
- Division: n divisions
- **Total:** $\approx 4n$ operations per vector

For tensor $[B, N_H, S, S]$ with softmax along last axis:

$$\text{FLOPs} = B \cdot N_H \cdot S \cdot (4S) = 4BN_H S^2 \quad (15)$$

3.3.4 Memory

- Input: $BN_H S^2$
- Output: $BN_H S^2$
- Intermediate values (max, sum): $BN_H S$

3.3.5 Backward Pass

Forward: $\mathbf{y} = \text{softmax}(\mathbf{x})$

Gradient:

$$\frac{\partial L}{\partial x_i} = y_i \left(\frac{\partial L}{\partial y_i} - \sum_j y_j \frac{\partial L}{\partial y_j} \right) \quad (16)$$

Vector form:

$$\frac{\partial L}{\partial \mathbf{x}} = \mathbf{y} \odot \left(\frac{\partial L}{\partial \mathbf{y}} - \langle \mathbf{y}, \frac{\partial L}{\partial \mathbf{y}} \rangle \mathbf{1} \right) \quad (17)$$

where \odot is element-wise multiplication and $\langle \cdot, \cdot \rangle$ is inner product.

3.4 Layer Normalization

3.4.1 Mathematical Definition

Normalization over feature dimension (D) for each sample and token:

Input: $\mathbf{x} \in \mathbb{R}^D$ (single token)

$$\mu = \frac{1}{D} \sum_{i=1}^D x_i \quad (18)$$

$$\sigma^2 = \frac{1}{D} \sum_{i=1}^D (x_i - \mu)^2 \quad (19)$$

$$\hat{x}_i = \frac{x_i - \mu}{\sqrt{\sigma^2 + \epsilon}} \quad (20)$$

$$y_i = \gamma_i \hat{x}_i + \beta_i \quad (21)$$

where $\gamma, \beta \in \mathbb{R}^D$ are learnable parameters and ϵ is a small constant for numerical stability (e.g., 10^{-5}).

3.4.2 Batch Processing

For tensor $\mathbf{X} \in \mathbb{R}^{[B, S, D]}$, apply LayerNorm independently at each (b, s) position:

$$\mathbf{Y}[b, s, :] = \text{LayerNorm}(\mathbf{X}[b, s, :]) \quad (22)$$

3.4.3 Computational Cost

Single vector $\mathbf{x} \in \mathbb{R}^D$:

- Mean computation: D additions
- Variance computation: D subtractions, D squares, D additions
- Normalization: D subtractions, D divisions, D square roots (amortized)
- Scale/shift: D multiplications, D additions
- **Total:** $\approx 6D$ operations

Tensor $[B, S, D]$:

$$\text{FLOPs} = B \cdot S \cdot 6D = 6BSD \quad (23)$$

3.4.4 Memory

- Input: BSD
- Output: BSD
- Parameters: $2D (\gamma, \beta)$
- Cache for backward: $2BS (\mu, \sigma^2) + BSD (\hat{\mathbf{X}})$

3.4.5 Backward Pass

Values saved from forward: $\mu, \sigma^2, \hat{\mathbf{x}}$

Gradient w.r.t. parameters:

$$\frac{\partial L}{\partial \beta} = \sum_{b,s} \frac{\partial L}{\partial \mathbf{Y}}[b, s, :] \quad (24)$$

$$\frac{\partial L}{\partial \gamma} = \sum_{b,s} \frac{\partial L}{\partial \mathbf{Y}}[b, s, :] \odot \hat{\mathbf{X}}[b, s, :] \quad (25)$$

Gradient w.r.t. input (per token):

$$\frac{\partial L}{\partial \mathbf{x}} = \frac{\gamma}{\sqrt{\sigma^2 + \epsilon}} \left[\frac{\partial L}{\partial \mathbf{y}} - \frac{1}{D} \sum \frac{\partial L}{\partial \mathbf{y}} - \hat{\mathbf{x}} \frac{1}{D} \sum \left(\frac{\partial L}{\partial \mathbf{y}} \odot \hat{\mathbf{x}} \right) \right] \quad (26)$$

3.5 Transpose

3.5.1 Mathematical Definition

Matrix transpose $\mathbf{A} \in \mathbb{R}^{[m,n]}$:

$$\mathbf{A}_{ij}^\top = \mathbf{A}_{ji} \quad (27)$$

For higher-dimensional tensors, exchange (permute) specific axes:

$$\mathbf{B} = \text{permute}(\mathbf{A}, \text{dims} = (0, 2, 1, 3)) \quad (28)$$

Usage in MHA: Transform $\mathbf{K} \in \mathbb{R}^{[B, N_H, S, D_h]}$ to $\mathbf{K}^\top \in \mathbb{R}^{[B, N_H, D_h, S]}$.

3.5.2 Computational Cost

Transpose only rearranges data:

$$\text{FLOPs} = 0 \quad (\text{no arithmetic operations}) \quad (29)$$

However, changes in memory access patterns can affect cache efficiency.

3.5.3 Memory

- If not in-place: requires memory for both input and output
- Contiguous memory requirement: may need reordering for subsequent operation efficiency

3.5.4 Backward Pass

Forward: $\mathbf{B} = \text{permute}(\mathbf{A}, \text{dims})$

Backward: Permute gradient in reverse order

$$\frac{\partial L}{\partial \mathbf{A}} = \text{permute} \left(\frac{\partial L}{\partial \mathbf{B}}, \text{inverse_dims} \right) \quad (30)$$

3.6 Reshape

3.6.1 Mathematical Definition

Change tensor shape while preserving element order:

$$\mathbf{B} = \text{reshape}(\mathbf{A}, \text{new_shape}) \quad (31)$$

Usage in MHA:

- Head split: $[B, S, D] \rightarrow [B, N_H, S, D_h]$ where $D = N_H \times D_h$
- Head merge: $[B, N_H, S, D_h] \rightarrow [B, S, D]$

3.6.2 Computational Cost

Reshape only changes metadata (shape information):

$$\text{FLOPs} = 0 \quad (\text{no arithmetic operations}) \quad (32)$$

3.6.3 Memory

- Typically a view operation: no additional memory required
- May trigger copy if contiguous memory is required

3.6.4 Backward Pass

Forward: $\mathbf{B} = \text{reshape}(\mathbf{A}, \text{shape}_B)$

Backward: Reshape gradient back to original shape

$$\frac{\partial L}{\partial \mathbf{A}} = \text{reshape}\left(\frac{\partial L}{\partial \mathbf{B}}, \text{shape}_A\right) \quad (33)$$

3.7 Concatenate

3.7.1 Mathematical Definition

Combine multiple tensors along a specific axis:

$$\mathbf{C} = \text{concat}([\mathbf{A}_1, \mathbf{A}_2, \dots, \mathbf{A}_k], \text{dim} = d) \quad (34)$$

Usage in MHA: Combine outputs from each head

$$[B, S, N_H, D_h] \xrightarrow{\text{reshape}} [B, S, N_H \times D_h] = [B, S, D] \quad (35)$$

(Actually implemented as reshape, but conceptually concatenation)

3.7.2 Computational Cost

Concatenate only performs memory copying:

$$\text{FLOPs} = 0 \quad (\text{no arithmetic operations}) \quad (36)$$

3.7.3 Memory

- Inputs: $\sum_i \text{size}(\mathbf{A}_i)$
- Output: $\text{size}(\mathbf{C}) = \sum_i \text{size}(\mathbf{A}_i)$

3.7.4 Backward Pass

Forward: $\mathbf{C} = \text{concat}([\mathbf{A}_1, \dots, \mathbf{A}_k], \text{dim} = d)$

Backward: Split gradient back to original tensors

$$\frac{\partial L}{\partial \mathbf{A}_i} = \text{split} \left(\frac{\partial L}{\partial \mathbf{C}}, \text{dim} = d \right)_i \quad (37)$$

3.8 Dropout

3.8.1 Mathematical Definition

During training, randomly set activations to zero:

$$\mathbf{Y} = \frac{\mathbf{M} \odot \mathbf{X}}{1 - p} \quad (38)$$

where $\mathbf{M} \sim \text{Bernoulli}(1 - p)$ is a binary mask and p is the dropout rate.

During inference: identity operation ($\mathbf{Y} = \mathbf{X}$)

3.8.2 Computational Cost

Training:

- Mask generation: $\text{size}(\mathbf{X})$ random samples
- Element-wise multiplication: $\text{size}(\mathbf{X})$
- Scaling: $\text{size}(\mathbf{X})$ divisions
- **Total:** $\approx 2 \times \text{size}(\mathbf{X})$ operations

Inference: 0 FLOPs (identity)

3.8.3 Memory

- Mask: $\text{size}(\mathbf{X})$ bits (can be optimized)
- Cache for backward: mask must be stored

3.8.4 Backward Pass

Forward (training): $\mathbf{Y} = \mathbf{M} \odot \mathbf{X} / (1 - p)$

Backward:

$$\frac{\partial L}{\partial \mathbf{X}} = \frac{\mathbf{M} \odot \frac{\partial L}{\partial \mathbf{Y}}}{1 - p} \quad (39)$$

Gradient is zero at positions where mask is zero.

4 Forward Pass Analysis

Having understood the basic operations, we now analyze the forward pass of Multi-Head Attention step-by-step. For each step, we specify input/output shapes, computational cost (FLOPs), and memory usage.

4.1 Notation

- $\mathbf{X} \in \mathbb{R}^{[B,S,D]}$: Input hidden states
- $\widetilde{\mathbf{W}}_Q, \widetilde{\mathbf{W}}_K, \widetilde{\mathbf{W}}_V \in \mathbb{R}^{[D,D]}$: Query, Key, Value projection weights
- $\widetilde{\mathbf{W}}_O \in \mathbb{R}^{[D,D]}$: Output projection weight
- $\tilde{\mathbf{b}}_O \in \mathbb{R}^D$: Output bias
- N_H : Number of attention heads
- $D_h = D/N_H$: Dimension per head

4.2 Step 0: Layer Normalization

Operation: $\boxed{\text{LN}}$

Input: $\mathbf{X} [B, S, D]$

Output: $\mathbf{X}_{\text{norm}} [B, S, D]$

Computation:

$$\mathbf{X}_{\text{norm}}[b, s, :] = \text{LayerNorm}(\mathbf{X}[b, s, :]) \quad (40)$$

FLOPs: $6BSD = O(BSD)$

Memory:

- Activations: $2BSD$;
- Cache: $2BS + BSD$;
- Parameters: $2D$

4.3 Step 1: Q, K, V Projections

Operation: $\odot \times 3$

Input: $\mathbf{X}_{\text{norm}} [B, S, D]$

Output: $\mathbf{Q}_{\text{flat}}, \mathbf{K}_{\text{flat}}, \mathbf{V}_{\text{flat}}$ each $[B, S, D]$

FLOPs: $3 \times 2BSD^2 = 6BSD^2 = O(BSD^2)$

Memory: Weights: $3D^2$; Outputs: $3BSD$

4.4 Step 2: Reshape to Multi-Head

Operation: $\boxed{\mathbf{R}} \times 3$

Input/Output: $[B, S, D] \rightarrow [B, N_H, S, D_h]$

FLOPs: 0 (metadata operation)

4.5 Step 3: Attention Scores ($\mathbf{Q} \cdot \mathbf{K}^\top$)

Operation: $\boxed{\mathbf{T}} + \odot$

Input: \mathbf{Q}, \mathbf{K} each $[B, N_H, S, D_h]$

Output: \mathbf{A} $[B, N_H, S, S]$

FLOPs: $2BS^2D = O(BS^2D)$ (Primary $O(S^2)$ bottleneck)

Memory: BN_HS^2 (Memory bottleneck for long sequences)

4.6 Step 4: Scale and Mask

Operation: $\boxed{\mathbf{SM}}$

FLOPs: $2BN_HS^2 = O(BN_HS^2)$

4.7 Step 5: Softmax

Operation: $\boxed{\mathbf{S}}$

FLOPs: $4BN_HS^2 = O(BN_HS^2)$

4.8 Step 6: Attention \times Value

Operation: \odot

FLOPs: $2BS^2D = O(BS^2D)$ (Second $O(S^2)$ bottleneck)

4.9 Steps 7-9: Concatenate, Output Projection, Dropout

FLOPs: $2BSD^2$ (output projection)

4.10 Forward Pass Total Complexity

Table 1: Forward Pass FLOPs Summary

Component	FLOPs
Linear Projections	$8BSD^2$
Attention Core	$4BS^2D$
Other Operations	$O(BN_HS^2)$
Total	$8BSD^2 + 4BS^2D + O(BN_HS^2)$

Complexity Analysis:

- **Short sequences** ($S \ll D$): $O(BSD^2)$ dominates (linear projections)
- **Long sequences** ($S \gg D$): $O(BS^2D)$ dominates (attention)
- **Crossover**: $S \approx 2D$

5 Backward Pass Analysis

The backward pass receives gradient $\frac{\partial L}{\partial \mathbf{A}_{\text{out}}}$ from the loss function and computes gradients for all parameters and inputs. Generally, backward pass computation is approximately 2x that of forward pass.

5.1 Key Observations

1. Each forward operation requires computing gradients for both inputs and weights
2. Total backward FLOPs: $16BSD^2 + 8BS^2D + O(BN_H S^2)$
3. **Ratio**: Backward is approximately 2x forward

5.2 Memory Considerations

- Must cache intermediate activations from forward pass
- Alternative: recompute activations during backward (gradient checkpointing)
- Trade-off: Memory vs computation time

6 Complexity Summary and Optimization

6.1 Overall Complexity

Table 2: Multi-Head Attention Total Complexity		
Component	FLOPs	Memory
Forward		
Linear Projections	$8BSD^2$	$4D^2 + 4BSD$
Attention Core	$4BS^2D$	$BN_H S^2$
Backward		
Linear Projections	$16BSD^2$	$4D^2$
Attention Core	$8BS^2D$	$BN_H S^2$
Total (Fwd+Bwd)	$24BSD^2 + 12BS^2D$	$8D^2 + BN_H S^2$

6.2 Scaling Characteristics

6.2.1 Sequence Length S

Critical observation: Attention has $O(S^2)$ complexity in both FLOPs and memory.

Example: $B = 32, N_H = 32, S = 8192$, float32

$$\text{Memory}(\mathbf{A}) = 32 \times 32 \times 8192^2 \times 4 \text{ bytes} = 256 \text{ GB} \quad (41)$$

This exceeds single GPU memory ($\sim 80\text{GB}$)!

6.3 Optimization Strategies

6.3.1 FlashAttention

Problem: Standard attention requires $O(S^2)$ memory.

Solution: IO-aware attention using tiling and recomputation.

Benefits:

- Memory: $O(BS \cdot D)$ instead of $O(BN_H S^2)$ ($S^2 \rightarrow S$ reduction)
- Speed: 2-4x faster wall-clock time
- Exact: Same results as standard attention

6.3.2 Multi-Query / Grouped-Query Attention

Problem: KV cache dominates inference memory.

Solution:

- **MQA:** All heads share single K, V (N_H heads, 1 KV pair)
- **GQA:** Groups of heads share K, V (N_H heads, G KV pairs)

Benefits:

- MQA: $32\times$ KV cache reduction
- GQA: $4\times$ reduction (with $G = 8$)
- Quality: GQA \approx standard $>$ MQA

6.3.3 Gradient Checkpointing

Problem: Must store all activations for backward pass.

Solution: Store only layer boundaries, recompute during backward.

Trade-off:

- Memory: $O(L \cdot BSD) \rightarrow O(\sqrt{L} \cdot BSD)$
- Time: $\sim 33\%$ increase

6.3.4 Mixed Precision Training

FP16/BF16 Benefits:

- Memory: 2x reduction
- Speed: 2-3x faster (with tensor cores)
- Stability: BF16 $>$ FP16

6.4 Practical Recommendations

6.4.1 Training

1. Use gradient checkpointing for $L > 24$ layers
2. Enable FlashAttention (always)
3. Use BF16 mixed precision
4. Maximize batch size to 90-95% GPU memory

6.4.2 Inference

1. Use GQA or MQA for KV cache efficiency
2. Apply INT8 quantization (2-4x speedup)
3. Consider speculative decoding for latency
4. Use sliding window for very long contexts

7 Conclusion

This paper provided a systematic analysis of the Multi-Head Attention mechanism from a computational graph perspective. We explained the mathematical definitions and characteristics of each primitive operation, and quantitatively derived the computational and memory costs at each stage of both forward and backward passes.

Key Findings :

1. **Dual bottleneck:** Linear projections for short sequences, attention for long sequences
2. **Memory bottleneck:** $O(S^2)$ attention scores, cumulative KV cache
3. **Backward is 2x forward:** Due to computing both input and weight gradients

Optimization Direction :

- FlashAttention (essential): $O(S^2) \rightarrow O(S)$ memory
- GQA/MQA: 4-32x KV cache reduction
- Mixed precision + fused kernels: 2-3x speedup
- Gradient checkpointing: \sqrt{L} memory reduction

This analysis provides LLM researchers and engineers with the understanding needed to design efficient Transformer implementations.

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