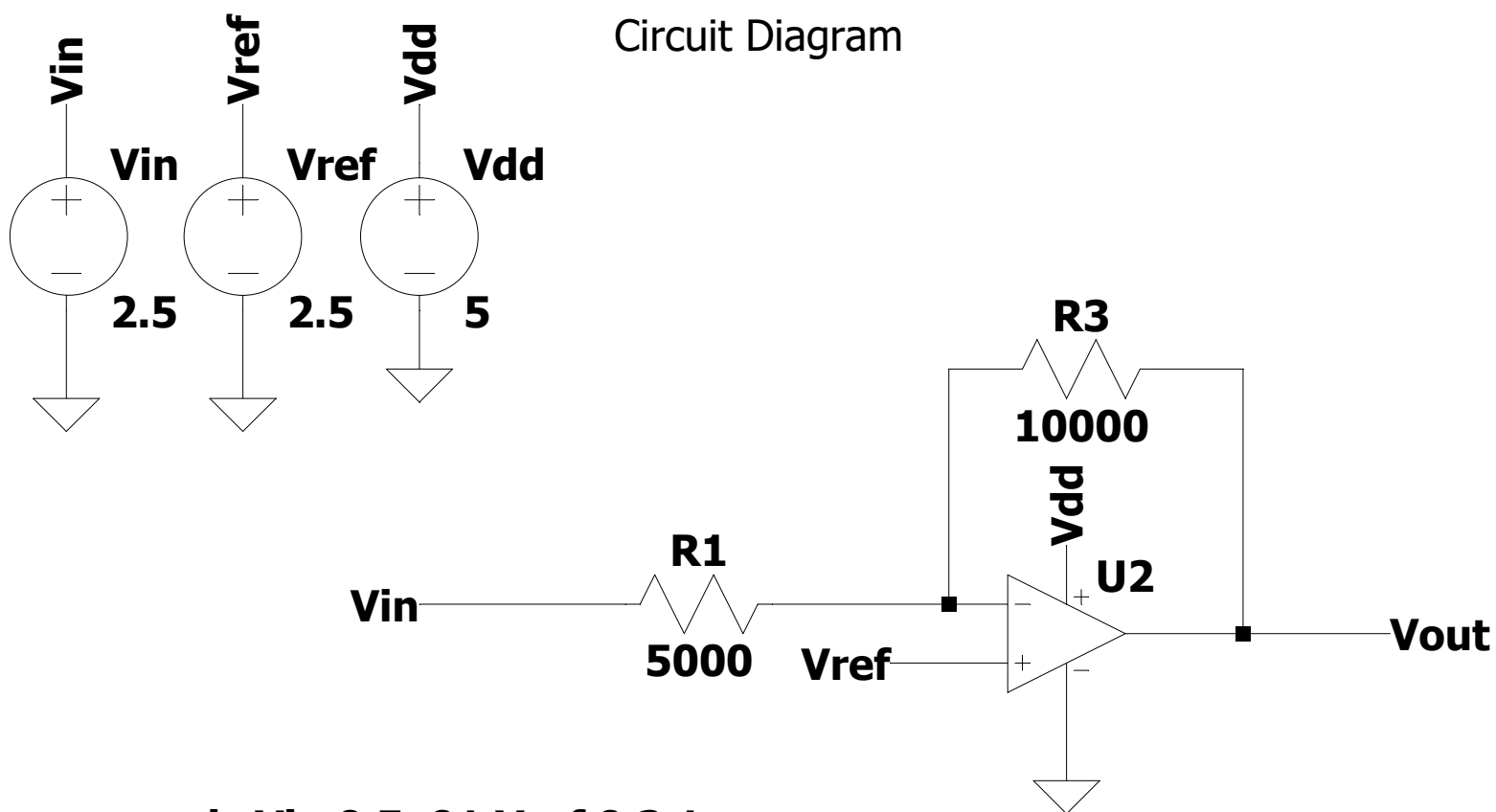


Circuit Diagram



.dc Vin 0 5 .01 Vref 0 2 1

