

Cfg1/PAL Cfg0/PAO Bootting from	USB.	eMMC Flash.	NAND Flash.	SPI Flash.	
. Cfg0/PA0	NO	OFF	NO	OFF	
Cfg1/PA	NO	NO	OFF	OFF	
		al.			
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System Clock from	12MHz crystal.	UPLL output.	
Cfg2	NO	OFF	

R105

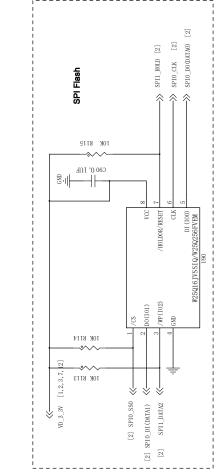
R104 10K

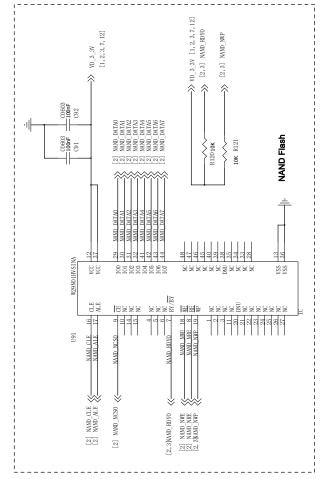
Setting PJ[4:0]	As GPI0 pin.	As JTAG interface.	
Cfg4/PA4	NO	OFF	

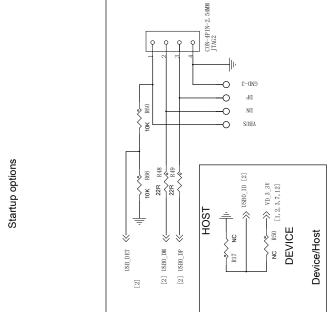
UART Debug Message	Output turn ON.	Output turn OFF.
Cfg5/PA5	NO	OFF

fg6   NAND Flash Page Size	N 2KB	JFF 4KB	ON 8KB	None None
Cfg6 N	NO	0FF	NO	OFF
Cfg7	No	NO	OFF	OFF

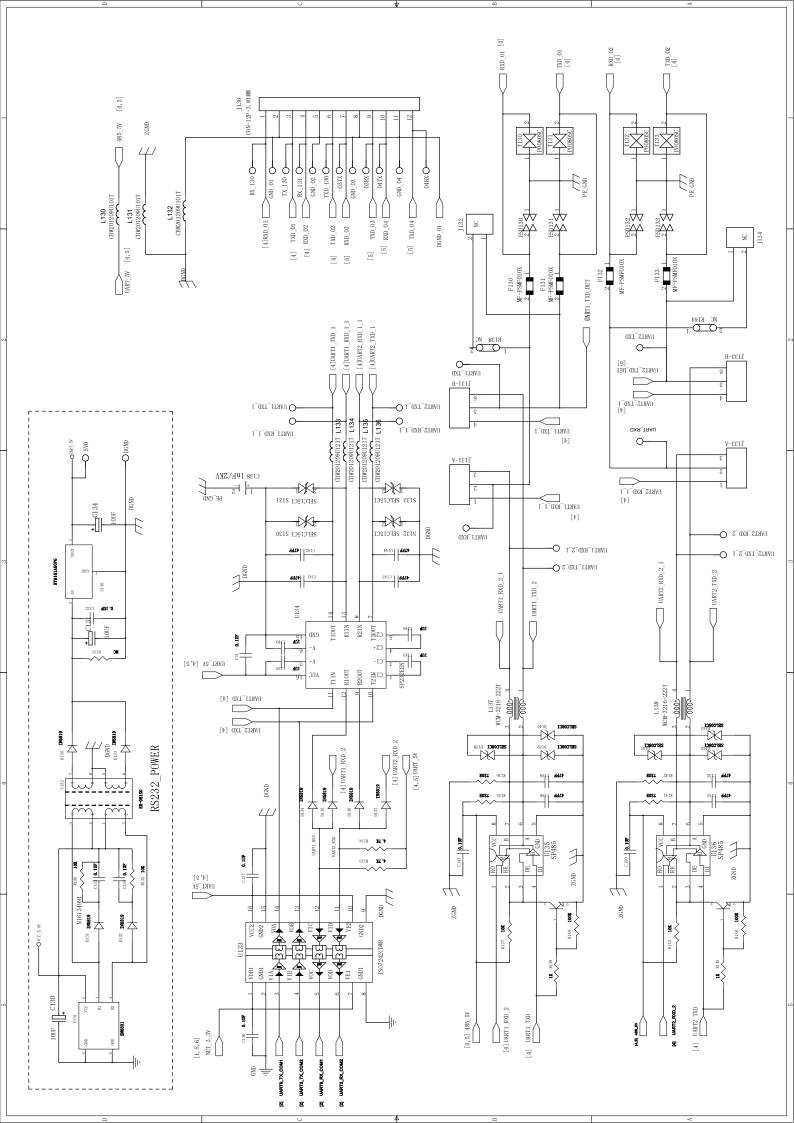
NAND Flash ECC type	BCH T12.	BCH T15.	BCH T24.	None
9 Cfg8	NO	OFF	NO	OFF
Cfg9	No	NO	OFF	OFF

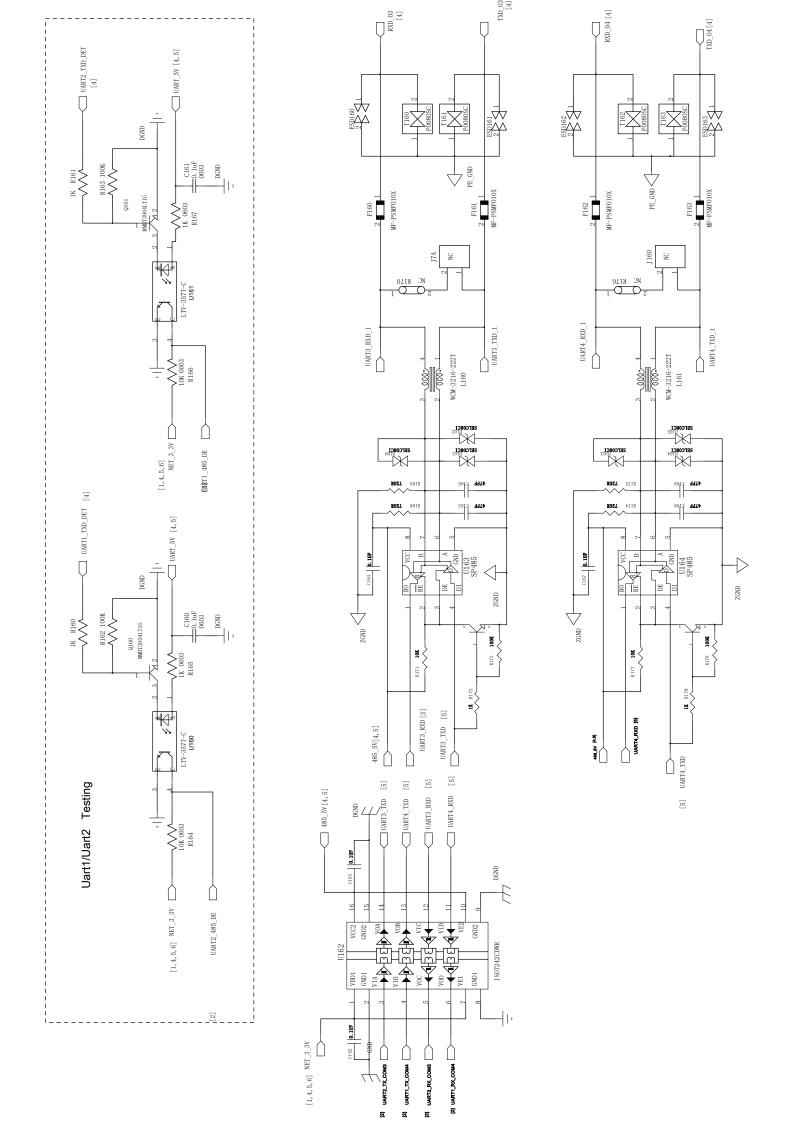


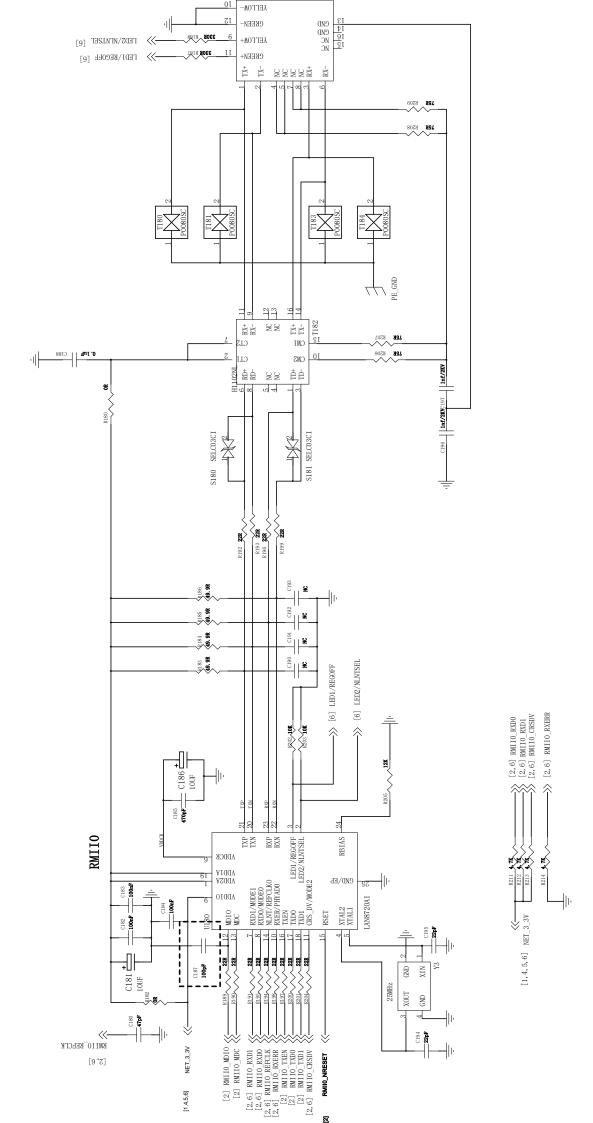


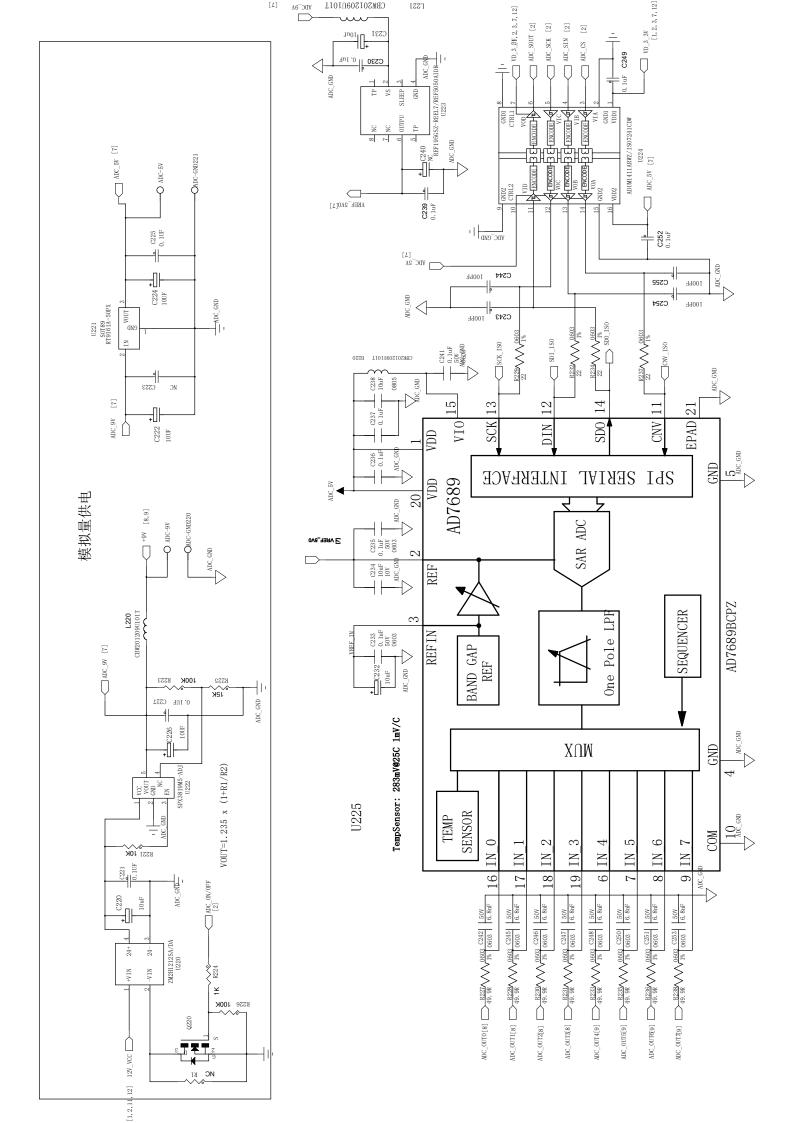


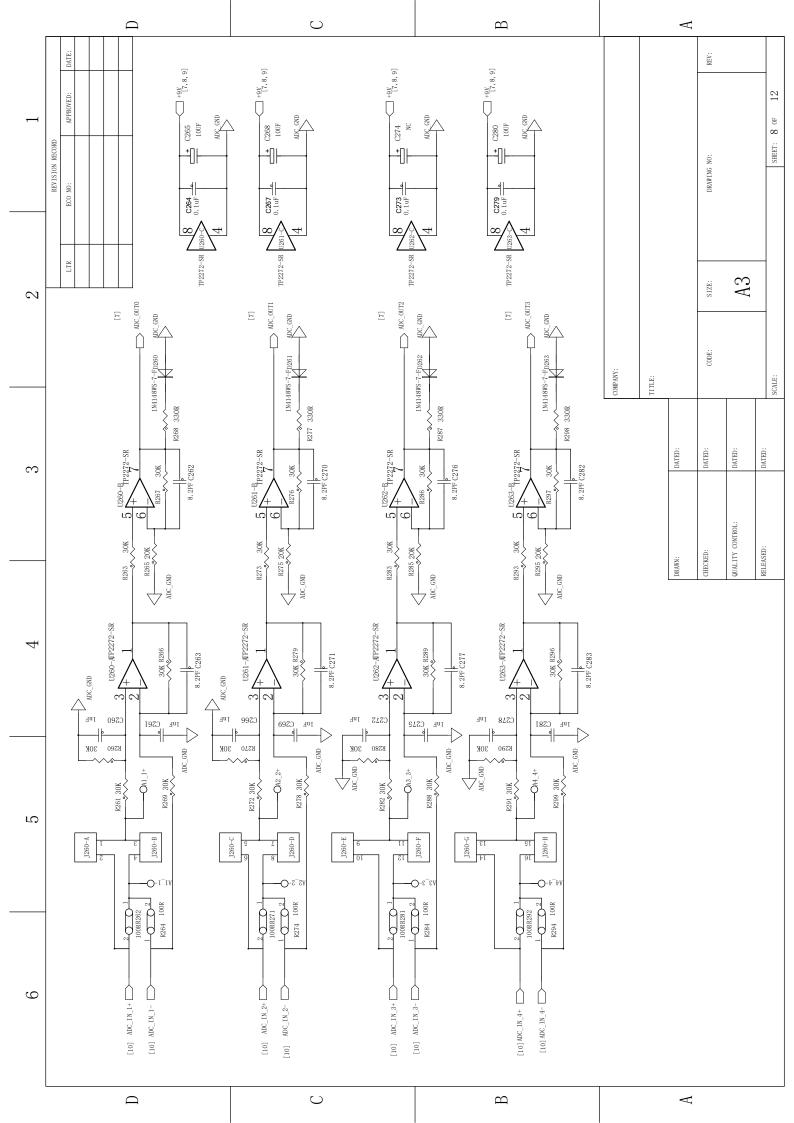
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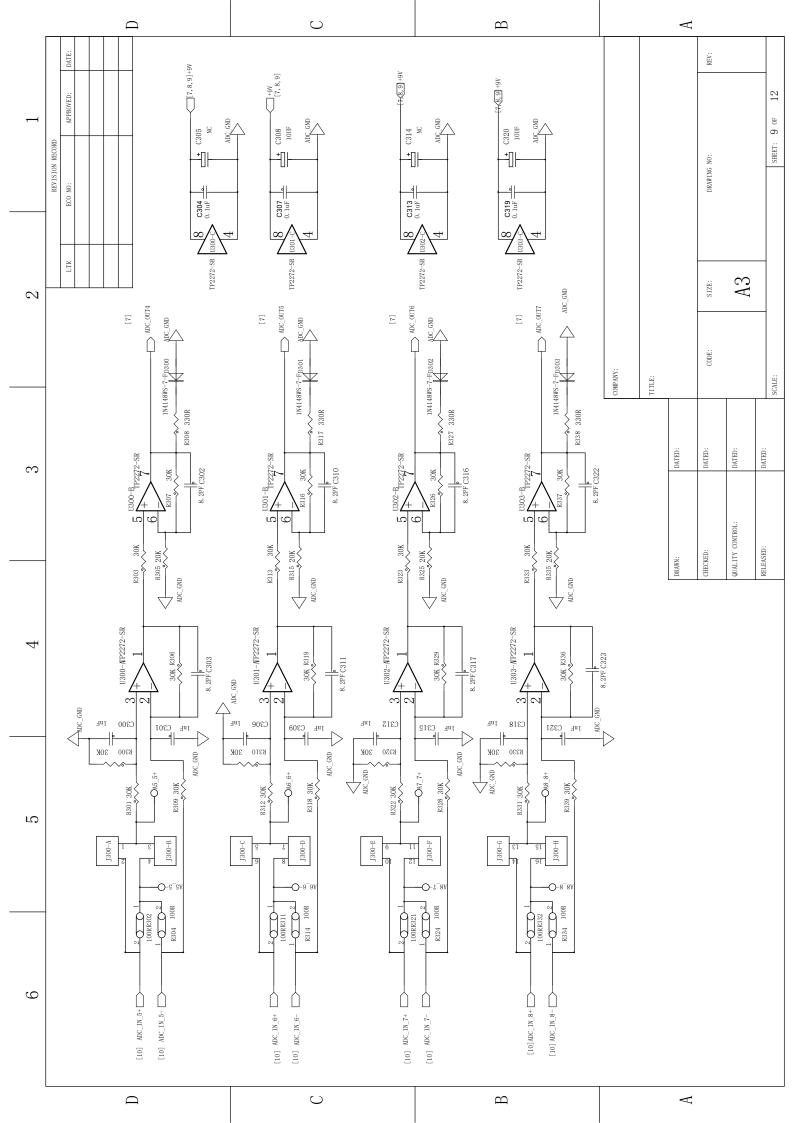


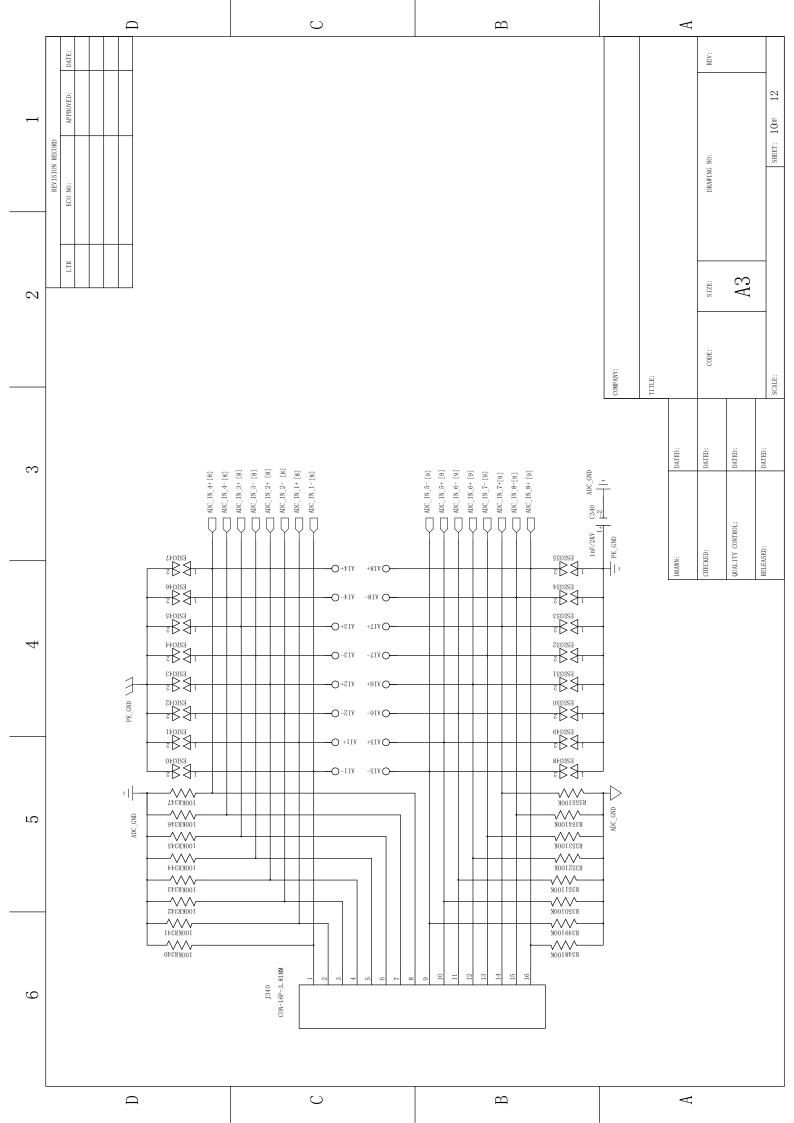


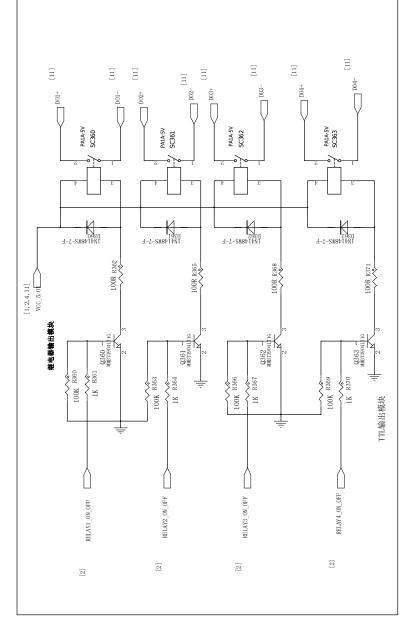


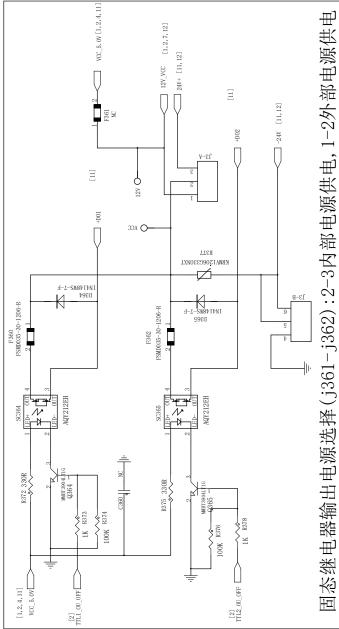


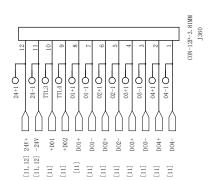




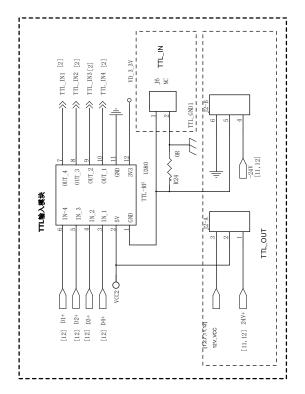


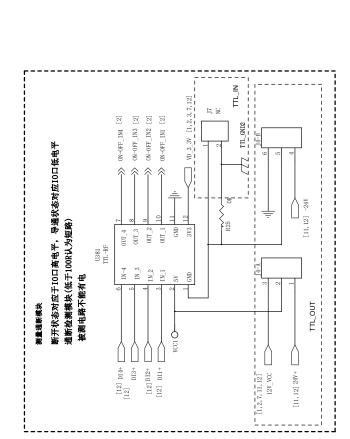


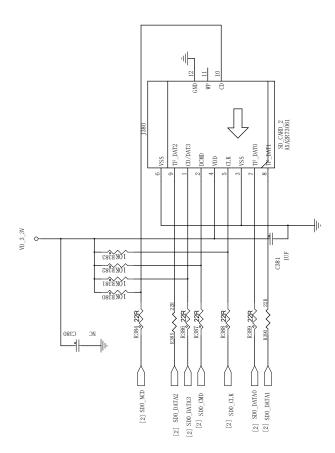


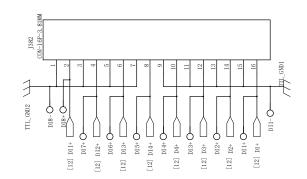


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