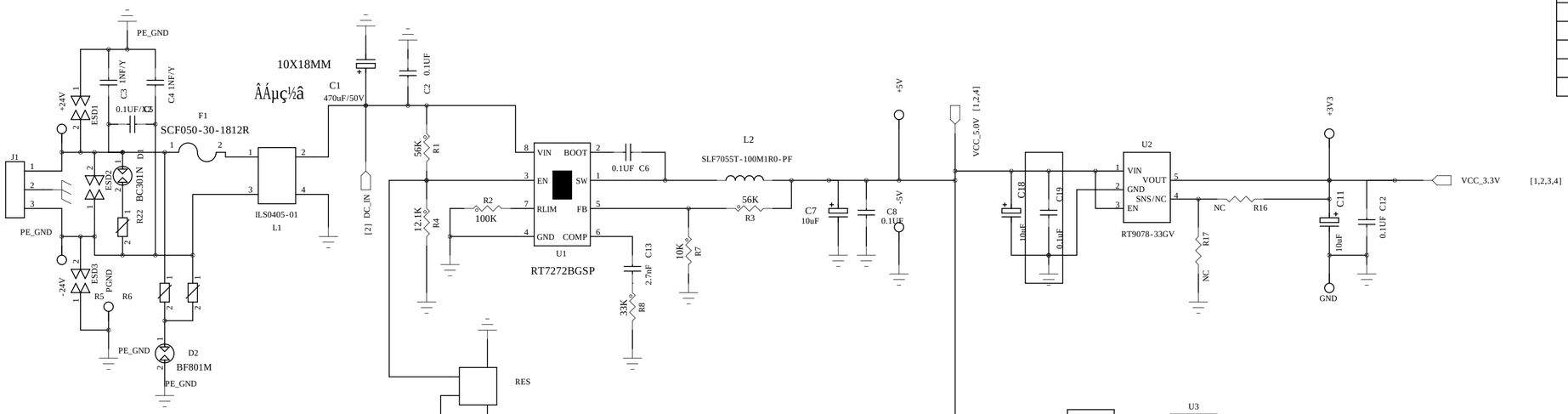
REVISION RECORD			
LTR	ECO NO:	APPROVED:	DATE:



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CHECKED: <Checked By>	DATED: <Checked Date>
QUALITY CONTROL: <QC By>	DATED: <QC Date>
RELEASED: <Released By>	DATED: <Release Date>

COMPANY: <Company Name>

TITLE: <Title>

CODE:	SIZE:	DRAWING NO:	REV:
<Code>	A2	<Drawing Number>	<Revision>

SCALE: <Scale> SHEET: 1 OF 4

REVISION RECORD			
LTR	ECO NO:	APPROVED:	DATE:

Name \ Module	EC20	NB	EMTC	M26
DETECTION_01	1	0	1	0
DETECTION_02	0	1	1	0

Cfg1/PA1	Cfg0/PA0	Bootting from
ON	ON	USB.
ON	OFF	eMMC Flash.
OFF	ON	NAND Flash.
OFF	OFF	SPI Flash.

Cfg2	System Clock from
ON	12MHz crystal.
OFF	UPLL output.

Cfg3	Watch Dog Setting
ON	OFF after power on.
OFF	ON after power on.

Startup options

Cfg4/PA4	Setting PJ[4:0]
ON	As GPIO pin.
OFF	As JTAG interface.

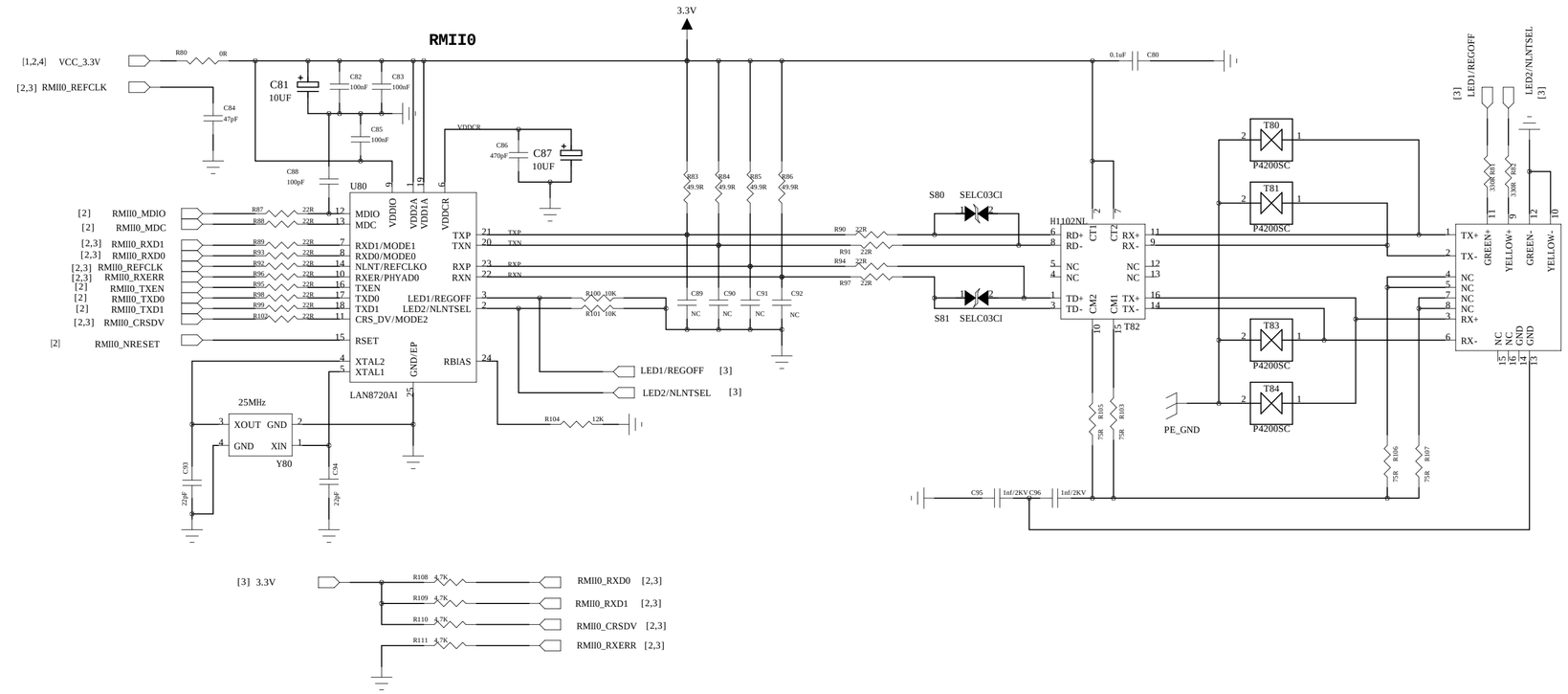
Cfg5/PA5	UART Debug Message
ON	Output turn ON.
OFF	Output turn OFF.

Cfg7	Cfg6	NAND Flash Page Size
ON	ON	2KB
ON	OFF	4KB
OFF	ON	8KB
OFF	OFF	None

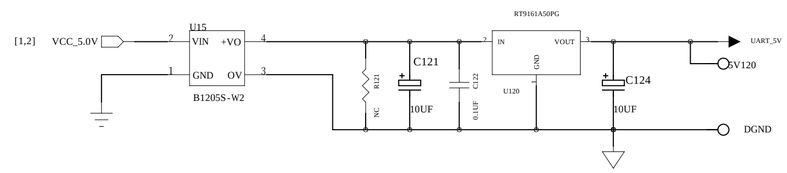
Cfg9	Cfg8	NAND Flash ECC type
ON	ON	BCH T12.
ON	OFF	BCH T15.
OFF	ON	BCH T24.
OFF	OFF	None

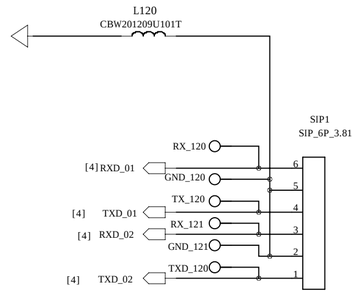
COMPANY:	<Company Name>		
TITLE:	<Title>		
DRAWN: <Drawn By>	DATED: <Drawn Date>		
CHECKED: <Checked By>	DATED: <Checked Date>	CODE: <Code>	SIZE: A2
QUALITY CONTROL: <QC By>	DATED: <QC Date>	DRAWING NO: <Drawing Number>	REV: <Revision>
RELEASED: <Released By>	DATED: <Release Date>	SCALE: <Scale>	SHEET: 2 OF 4

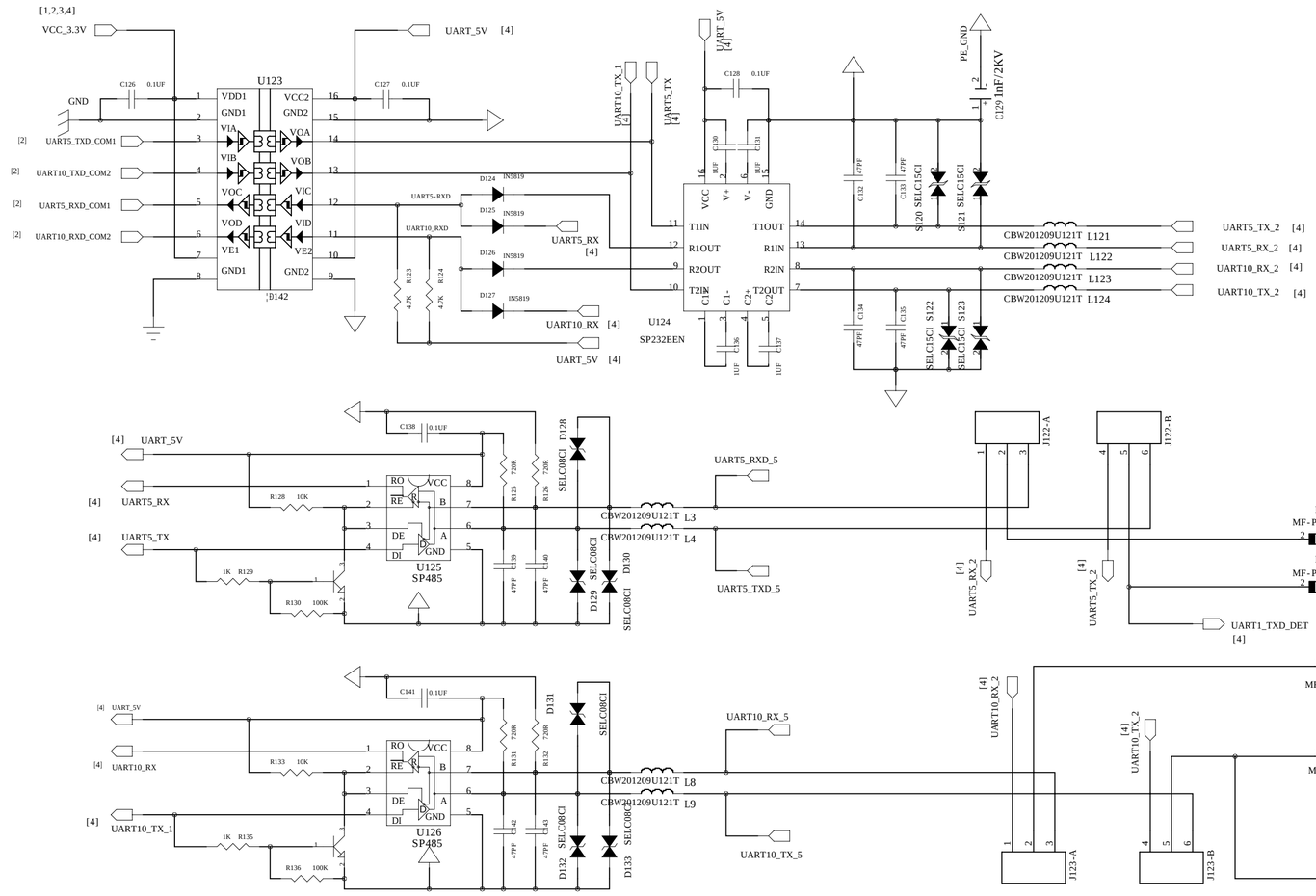
REVISION RECORD			
LTR	ECO NO:	APPROVED:	DATE:

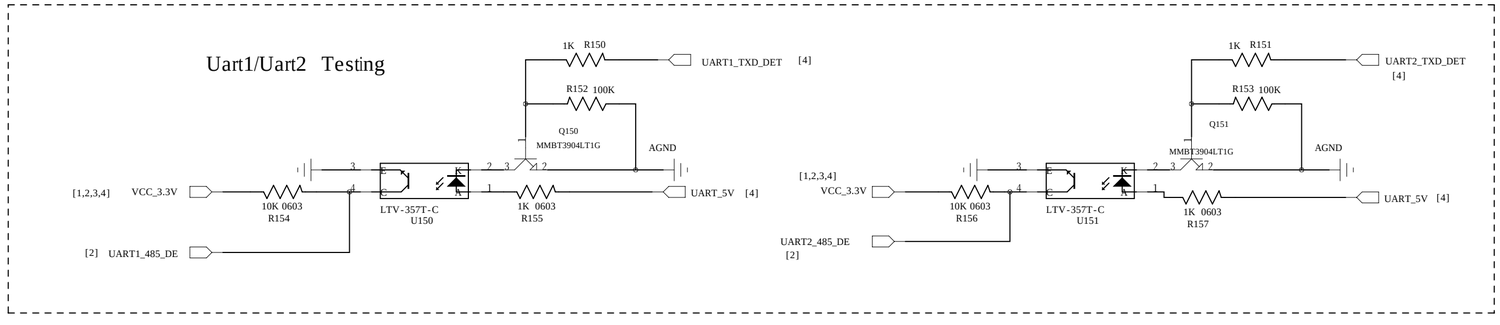


DRAWN: <Drawn By>	DATED: <Drawn Date>	COMPANY: <Company Name>	
CHECKED: <Checked By>	DATED: <Checked Date>	TITLE: <Title>	
QUALITY CONTROL: <QC By>	DATED: <QC Date>	CODE: <Code>	SIZE: A2
RELEASED: <Released By>	DATED: <Release Date>	DRAWING NO: <Drawing Number>	REV: <Revision>
		SCALE: <Scale>	SHEET: 3 OF 4


U15
VIN +VO
GND OV
B1205S-W2
[1,2] VCC_5.0V
C121
10UF
C122
0.1UF
R121
NC
RT9161A50PG
IN VOUT
GND
U120
C124
10UF
UART_5V
5V120
DGND


L120
CBW201209U101T
SIP1
SIP_6P_3.81
RX_120
[4] RXD_01
GND_120
TX_120
[4] TXD_01
RX_121
[4] RXD_02
GND_121
TXD_120
[4] TXD_02


[1,2,3,4]
VCC_3.3V
GND
U123
VDD1 VCC2
GND1 GND2
VIA VOA
VIB VOB
VOC VIC
VOD VID
VE1 VE2
GND1 GND2
C126 0.1UF
C127 0.1UF
D142
UART5_TXD_COM1
UART10_TXD_COM2
UART5_RXD_COM1
UART10_RXD_COM2
UART5_5V [4]
UART5-RXD
UART10_RXD
D124 IN5819
D125 IN5819
D126 IN5819
D127 IN5819
R123 4.7K
R124 4.7K
UART5_RX [4]
UART10_RX [4]
UART_5V [4]
UART10_TX_1 [4]
UART5_TX [4]
C128 0.1UF
U124
SP232EEN
T1IN R1OUT R2OUT T2IN
T1OUT R1IN R2IN T2OUT
VCC V+ V- GND
C129 1nF/2KV
PE_GND
S120 SELC15CI
S121 SELC15CI
S122 SELC15CI
S123 SELC15CI
CBW201209U121T L121
CBW201209U121T L122
CBW201209U121T L123
CBW201209U121T L124
UART5_TX_2 [4]
UART5_RX_2 [4]
UART10_RX_2 [4]
UART10_TX_2 [4]
C138 0.1UF
U125
SP485
R128 10K
R129 1K
R130 100K
R125 7.20R
R126 7.20R
D128 SELC08CI
D129 SELC08CI
D130 SELC08CI
CBW201209U121T L3
CBW201209U121T L4
UART5_RXD_5
UART5_TXD_5
J122-A
J122-B
F120 MF-PSMF010X
F121 MF-PSMF010X
ESD120
ESD121
T120 P0080SC
T121 P0080SC
RXD_01 [4]
TXD_01 [4]
UART1_TXD_DET [4]
C141 0.1UF
U126
SP485
R133 10K
R135 1K
R136 100K
R131 7.20R
R132 7.20R
D131 SELC08CI
D132 SELC08CI
D133 SELC08CI
CBW201209U121T L8
CBW201209U121T L9
UART10_RX_5
UART10_TX_5
J123-A
J123-B
F122 MF-PSMF010X
F123 MF-PSMF010X
ESD122
ESD123
T122 P0080SC
T123 P0080SC
RXD_02 [4]
TXD_02 [4]
UART2_TXD_DET [4]


Uart1/Uart2 Testing
1K R150
UART1_TXD_DET [4]
R152 100K
Q150
MMBT3904LT1G
AGND
[1,2,3,4] VCC_3.3V
10K 0603
R154
LTV-357T-C
U150
1K 0603
R155
UART_5V [4]
[2] UART1_485_DE
1K R151
UART2_TXD_DET [4]
R153 100K
Q151
MMBT3904LT1G
AGND
[1,2,3,4] VCC_3.3V
10K 0603
R156
LTV-357T-C
U151
1K 0603
R157
UART_5V [4]
UART2_485_DE [2]