1. Description

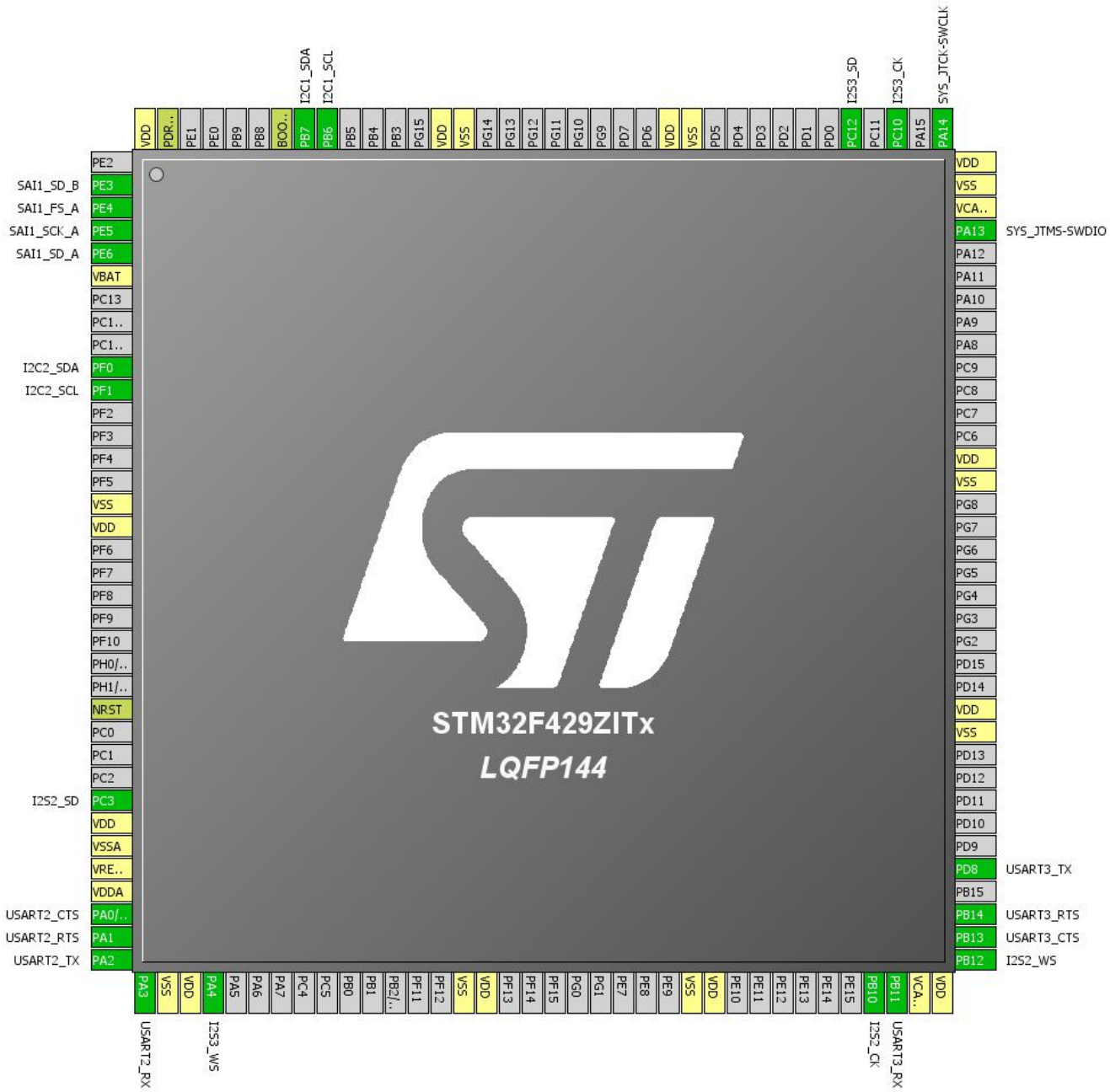
1.1. Project

Project Name	Putz01_STCubeGenerated
Generated with:	STM32CubeMX 4.8.0
Date	08/03/2015

1.2. MCU

MCU Serie	STM32F4
MCU Line	STM32F429/439
MCU name	STM32F429ZITx
MCU Package	LQFP144
MCU Pin number	144

2. Pinout Configuration



3. IPs and Middlewares Configuration

IP	Mode	Fonction	Pin
I2C1	I2C: I2C	I2C1_SCL	PB6
		I2C1_SDA	PB7
I2C2	I2C: I2C	I2C2_SCL	PF1
		I2C2_SDA	PF0
I2S2	Mode: Half-Duplex Slave	I2S2_CK	PB10
		I2S2_SD	PC3
		I2S2_WS	PB12
I2S3	Mode: Half-Duplex Slave	I2S3_CK	PC10
		I2S3_SD	PC12
		I2S3_WS	PA4
SAI1	SAI_A Mode: Asynchronous Slave	SAI1_SD_A	PE6
		SAI1_SCK_A	PE5
		SAI1_FS_A	PE4
	SAI_B Mode: Synchronous Slave	SAI1_SD_B	PE3
SYS	Debug: Serial Wire Debug (SWD)	SYS_JTCK-SWCLK	PA14
		SYS_JTMS-SWDIO	PA13
USART2	Mode: Asynchronous	USART2_RX	PA3
		USART2_TX	PA2
	Flow Control (RS232): CTS/RTS	USART2_CTS	PA0/WKUP
		USART2_RTS	PA1
USART3	Mode: Asynchronous	USART3_RX	PB11
		USART3_TX	PD8
	Flow Control (RS232): CTS/RTS	USART3_CTS	PB13
		USART3_RTS	PB14

4. Pins Configuration

Pin	Pos	Function(s)	Label
PE3	2	SAI1_SD_B	
PE4	3	SAI1_FS_A	
PE5	4	SAI1_SCK_A	
PE6	5	SAI1_SD_A	
PF0	10	I2C2_SDA	
PF1	11	I2C2_SCL	
PC3	29	I2S2_SD	
PA0/WKUP	34	USART2_CTS	
PA1	35	USART2_RTS	
PA2	36	USART2_TX	
PA3	37	USART2_RX	
PA4	40	I2S3_WS	
PB10	69	I2S2_CK	
PB11	70	USART3_RX	
PB12	73	I2S2_WS	
PB13	74	USART3_CTS	
PB14	75	USART3_RTS	
PD8	77	USART3_TX	
PA13	105	SYS_JTMS-SWDIO	
PA14	109	SYS_JTCK-SWCLK	
PC10	111	I2S3_CK	
PC12	113	I2S3_SD	
PB6	136	I2C1_SCL	
PB7	137	I2C1_SDA	

5. Power Plugin report

5.1. Microcontroller Selection

Serie	STM32F4
Line	STM32F429/439
MCU	STM32F429ZITx
Datasheet	024030_Rev4

5.2. Parameter Selection

Temperature	25
Vdd	null

6. Software Project

6.1. Project Settings

Name	Value
Project Name	Putz01_STCubeGenerated
Project Folder	D:\Dev\HsDesign\StretchTech\SysWkBench01\Putz01_STM32CubeGenerated\P
Toolchain / IDE	SW4STM32
Firmware Package Name and Version	STM32Cube FW_F4 V1.6.0

6.2. Code Generation Settings

Name	Value
STM32Cube Firmware Library Package	Copy all used libraries into the project folder
Generate peripheral initialization as a pair of '.c/.h' files	No
Backup previously generated files when re-generating	Yes
Delete previously generated files when not re-generated	Yes
Set all free pins as analog (to optimize the power consumption)	No

6.3. Toolchains Settings

Name	Value
Compiler Optimizations	Balanced Size/Speed