D Y PATIL
RAMRAO ADIK
INSTITUTE OF
TECHNOLOGY
NAVI MUMBAI

RAIT/Ph.D./416   24.03.2015

To

The Dy. Registrar
Ph.D. Thesis Section,
University of Mumbai
Fort Campus, Mumbai – 400 032.

Sub: Justification for the Ph.D. admission.

Dear Sir / Madam,

Following students were selected for Ph.D. in Computer Engineering and Electronics Engineering at our Research Center.

1. Jadhav Vaishali Satish – Electronics
2. Chitra Suresh – Electronics
3. Mendgudle Sujata D. – Electronics

4. Mane Vanita Manikrao – Computer
5. Shedge Rajashree – Computer
6. Jagtap Shital Vishwanath – Computer
7. Jadhav Dipti Shailendra – Computer
8. Marathe Nilesh Ratnakar – Computer

1) The interviews were held in the month of November – 2013 (18th & 19th) and students were informed about their selection in the same month.

2) Students took 3-4 months (January – 2014 to May – 2014) to submit the documents and complete the formalities at Research Centre & University.

3) The course work for Ph.D. started in Academic Year 2014-15 from July 2014 to December – 2014.

4) Due to constraints of Ph.D. superiors, the Ph.D course work seminars were delayed and got completed by the end of February – 2015.

5) The result is declared in March – 2015.

This is for your information.

Thanking you,

Yours faithfully,

Dr. Ramesh Vasappanavara, Ph.D.
Principal

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