

CIRCUIT ELEMENTS

Search..

Input ▾

Output ▾

X

Gates ▾

TESTBENCH

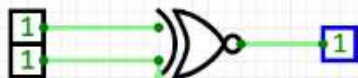
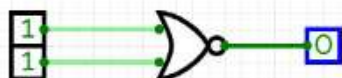
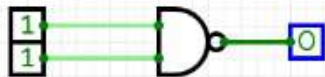
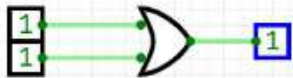
Decoders & Multiplexers ▾

Sequential Elements ▾

Annotation ▾

Misc ▾

TIMING DIAGRAM



PROPERTIES

PROJECT PROPERTIES

Project:

gates implementation

Circuit:

Main

Clock Time (ms):

- 500 +

Clock Enabled:

☒

Lite Mode:

☐

Edit Layout

Delete Circuit

