## CprE 381, Computer Organization and Assembly-Level Programming, Fall 2012

## **Lab Evaluation Form – Project A**

Lab Partners		
Section/Lab T		
Completed	Description	Points
	Prelab: A list of best practices/tips	/ 5
	Part 1: 1-bit ALU with support to add/sub, slt, and, or, xor, nand, and nor instructions	/ 15
	Part 2: 32-bit ALU	/ 15
	Part 3: Barrel shifter	/ 15
	VHDL Coding Quality	/ 10
	Lab Report	/ 20
TA Signatur	e Date	
	Subtotal	
	Late Penalty (20% next week, 100% thereafter)	
	Score	/ 80