CprE 381, Computer Organization and Assembly-Level Programming, Fall 2013 Lab Evaluation Form – Lab 3

Lab Partners		
Section/Lab	Γime	
Completed	Description	Points
	Prelab: None	
	Parts (a) and (b): N-Bit Register	/ 10
	Parts (c) and (d): Register Address Decoder	/ 10
	Parts (e) and (f): 32-bit 32:1 multiplexor	/ 10
	Parts (g) and (h): The MIPS Register File	/15
	VHDL Coding Quality	/5
	Lab Report	/ 10
TA Signatur	e Date	
	Subtotal	
	Late Penalty (20% next week, 100% thereafter)	
	Score	/ 60