

# Instruction-Level Data Isolation for the Kernel on ARM

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May 22nd, 2021

## 1 Paper Summary

As the increasing demand for the security of the OS kernel data, a more efficient mechanism should be implemented to replace the current MMU-based mechanism or Software-based mechanism. *Instruction-Level Data Isolation* is proposed to address this issue. This paper is organized in a clear structure as follows:

This is the example of code section: `java is good`

This is the example of quote section:

Quote: winter is coming!

This is the example of reference<sup>1</sup>

This is the example of math equations:

$$1 \div 3 = \frac{1}{3} \tag{1}$$

This is (1)

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1. [www.baidu.com](http://www.baidu.com) ↩