PRABHDEEP KAUR

(Electronics and Communication Engineer)

1. ASIC design(Application-specific integrated circuit) : ASIC is microchips designed for special applications which we use in our daily basics . it’s includes various steps like design conceptualization, chip optimization, logical implementation, physical implementation and design validation and verification. All those steps I learned in my engineering.

Experience:

1. Ct group of institute : (2013-2017)

* Completed my electronic engineering with 148 credits where I learned basic concepts of ASIC designings and I made some basic projects using RT tools and embedded system like full/half adder, multiplier and subtractor.
* Provided first-rate support to team in overseeing all areas of plant, while maintaining and monitoring the current and voltage reading through ammeter and voltmeter
* Handled the operation, installation, troubleshooting and maintenance of different electrical equipment, including router, direct current/alternating current (DC/AC) motor, resistance, capacitor, and microcontroller integrated circuit (IC) for both computer and printer circuit board (PCB)

Projectes:

1. calculator.
2. Traffic light system.
3. Toll tax automation.
4. Wall detector robert.
5. Signal testing.
6. Stepper motor.
7. up/down counter.

B.Sofcon: (2015-2016)

* I did my 6 week internship in a Sofcon company and after that I did a part time job for one year in this company and also got a professional certificate from this company.
* Displayed expertise in handling embedded system programming and practical electronics as well as in utilizing computers in operating equipment
* Conducted server testing process to ensure efficiency of operation, configuration, and remote management
* Work on an 8051 microcontroller too.

Projectes:

1. ATM machine programming.
2. Human /wall/metal detection robert.
3. Water level control.
4. Temperature control.
5. Bluetooth control electronic home applications.
6. GSM control robot car.
7. Fingerprint based biometric attendance machine.

2. Systemverilog and UVM:systemverilog basically a language which is a hardware description and hardware verification used to model ,design,testing and implementation.

1. Ct group of institution: (2015-2017)

* Last two year of our college we had a lab for a verilog system where I learned how to coding ,testing and troubleshooting our model .
* Here I learned the basic and most important concept of verilog system.

Projects:

1. flip -flop simulator.
2. Washing machine.
3. 32-bit booth multiplier.
4. 8:3 encoder with priority.
5. 32-bit binary to gray counter converter.
6. Gray counter.
7. First in first out design.

B. Telcocrats: (2016-2017)

* Demonstrated broad knowledge of using various electrical and engineer tools and software, such MapInfo, mobile communications network application (MCOM), ATOLL, OMT, and RL Tool
* Performed outdoor services for setting up new antennas
* Contributed key insights in the attainment of overall systems with network efficiency with organized group of individuals with tight deadlines
* Oversaw the development and management of long- and short-term projects based on individual priorities
* Also work on RL coding amd troubleshooting them
* I learned how to create/randomize, implementat functional coverage, check for functional correctness and gather/verify protocol execution coverage.