

JAWAHARLAL NEHRU TECHNOLOGICAL UNIVERSITY HYDERABAD
HYDERABAD - 500 085, ANDHRA PRADESH, INDIA

CONSOLIDATED MARKS MEMO / CREDIT SHEET

BACHELOR OF TECHNOLOGY- ELECTRONICS & COMMUNICATION ENGINEERING

CMM. No.: C 0492027

Serial No.: 21101054315

Name : BABY DEEPA ANNIE

Hall Ticket No.: 10P81A0466

Year of Admission : 2010-2011

Name of the College : P8-JIET, CHINTHAPALLIGUDA

Month & Year of Final Exam : April, 2014

Class Awarded : FIRST CLASS WITH DISTINCTION

S.No.	SUBJECT TITLE	WT MARKS	EXT MARKS	TOTAL	CREDITS	S.No.	SUBJECT TITLE	WT MARKS	EXT MARKS	TOTAL	CREDITS
I YEAR											
1	ENGLISH	21	56	77	4	2	MATHEMATICS-I	20	35	55	5
3	MATHEMATICAL METHODS	22	45	67	6	4	ENGINEERING PHYSICS	21	35	56	4
5	ENGINEERING CHEMISTRY	19	26	45	4	6	COMPUTER PROGRAMMING & DATA STRUCTURES	24	41	65	6
7	ENGINEERING DRAWING	23	32	55	4	8	COMPUTER PROGRAMMING LAB	23	45	68	4
9	ENGG. PHYSICS & ENGG. CHEMISTRY LAB	22	50	72	4	10	ENGLISH LANGUAGE COMMUNICATION SKILLS LAB	24	46	70	4
11	IT WORKSHOP & ENGINEERING WORKSHOP	24	44	68	4						
I SEMESTER			II YEAR			II SEMESTER					
1	MATHEMATICS-III	23	28	51	3	1	PRINCIPLES OF ELECTRICAL ENGINEERING	25	42	67	3
2	PROBABILITY THEORY & STOCHASTIC PROCESSES	23	38	61	3	2	ELECTRONIC CIRCUIT ANALYSIS	25	44	69	4
3	ENVIRONMENTAL STUDIES	24	54	78	3	3	PULSE & DIGITAL CIRCUITS	24	26	50	4
4	ELECTRIC CIRCUITS	24	37	61	4	4	SWITCHING THEORY & LOGIC DESIGN	24	30	54	4
5	ELECTRONIC DEVICES & CIRCUITS	24	49	73	4	5	ELECTROMAGNETIC THEORY & TRANSMISSION LINES	25	35	60	4
6	SIGNALS & SYSTEMS	24	42	66	4	6	ELECTRICAL ENGINEERING LAB	25	50	75	2
7	ELECTRONIC DEVICES & CIRCUITS LAB	24	44	68	2	7	ELECTRONIC CIRCUIT ANALYSIS LAB	25	49	74	2
8	BASIC SIMULATION LAB	24	41	65	2	8	PULSE & DIGITAL CIRCUITS LAB	20	48	68	2
I SEMESTER			III YEAR			II SEMESTER					
1	CONTROL SYSTEMS	24	26	50	4	1	MANAGERIAL ECONOMICS & FINANCIAL ANALYSIS	23	38	61	4
2	COMPUTER ORGANIZATION	25	47	72	4	2	NANO TECHNOLOGY	24	37	61	4
3	ANTENNAS & WAVE PROPAGATION	24	31	55	3	3	DIGITAL COMMUNICATIONS	24	62	86	3
4	ELECTRONIC MEASUREMENTS & INSTRUMENTATION	25	60	85	4	4	MICROPROCESSORS & MICROCONTROLLERS	25	30	55	4
5	ANALOG COMMUNICATIONS	25	43	68	3	5	DIGITAL SIGNAL PROCESSING	24	46	70	4
6	IC APPLICATIONS	24	39	63	3	6	MICROPROCESSORS & MICROCONTROLLERS (LAB)	25	48	73	2
7	ANALOG COMMUNICATIONS (LAB)	24	50	74	2	7	DIGITAL SIGNAL PROCESSING (LAB)	24	45	69	2
8	IC APPLICATIONS (LAB)	25	49	74	2	8	ADVANCED ENGLISH COMMUNICATION SKILLS (LAB)	24	46	70	2
I SEMESTER			IV YEAR			II SEMESTER					
1	MANAGEMENT SCIENCE	24	27	51	3	1	SATELLITE COMMUNICATIONS	22	38	60	3
2	VLSI DESIGN	24	33	57	4	2	RADAR SYSTEMS	22	30	52	3
3	MICROWAVE ENGINEERING	24	32	56	3	3	DIGITAL DESIGN THROUGH VERILOG HDL	23	28	49	3
4	COMPUTER NETWORKS	24	31	55	4	4	INDUSTRY ORIENTED MINI PROJECT	-	46	46	2
5	DSP PROCESSORS AND ARCHITECTURES	23	33	56	3	5	SEMINAR	49	-	49	2
6	EMBEDDED SYSTEMS	23	26	49	4	6	MAJOR PROJECT	48	145	193	10
7	E-CAD AND VLSI LAB	24	48	72	2	7	COMPREHENSIVE VIVA	-	96	96	2
8	MICROWAVE ENGG. AND DIGITAL COMMNS. LAB	24	48	72	2						

(# Project Internal=50 External=150)

Number of Credits registered for : 200 Aggregate Marks Secured for BOB : 193

Aggregate Marks Secured : 3743 OUT OF 5200 (71.98%)

Date of Issue : June 13, 2014
(See overleaf for Rules concerned to award of class)

A indicates 'ABSENT'

(*Courses registered but not counted for CGPA/CGPA=0.00)

Ans 5 S. G. S.
CONTROLLER OF EXAMINATIONS