**Errata for ECE 4723/6723 Lab Development Board**

This document details the errata for the custom-developed ECE 4723/6723 lab development board. Details include affected areas and possible solutions. Please see the device’s datasheet for a full functional description.

**ECE 4723/6723 Lab Development Board Errata Summary**

1. **RD8 pin unconnected**
   * **PROBLEM DEFINITION**Pin number 42 on the PIC33EP512GP806 microcontroller (RD8) should connect to pad RD8 on H2 but does not.
   * **SCOPE OF IMPACT**PinOut of RD8 on H2 will be disconnected.
   * **WORKAROUND**  
     Connect pin 42 on the microcontroller to the RD8 pad on H2 using a wire.
2. **SCL pin unconnected**
   * **PROBLEM DEFINITION**Pin number 37 on the PIC33EP512GP806 microcontroller (SCL) should connect to pad SCL on H2 but does not.
   * **SCOPE OF IMPACT**PinOut of SCL on H2 will be disconnected.
   * **WORKAROUND**  
     Connect pin 37 on the microcontroller to the SCL pad on H2 using a wire.
3. **SDA pin unconnected**
   * **PROBLEM DEFINITION**Pin number 38 on the PIC33EP512GP806 microcontroller (SDA) should connect to pad SDA on H2 but does not.
   * **SCOPE OF IMPACT**PinOut of SDA H2 will be disconnected.
   * **WORKAROUND**  
     Connect pin 38 on the microcontroller to the SDA pad on H2 using a wire.
4. **RF0 pin unconnected on FTDI – UART operation impossible**
   * **PROBLEM DEFINITION**Pin number 58 (RF0) on the PIC33EP512GP806 microcontroller should connect to pad RXD on CAN2 but does not.
   * **SCOPE OF IMPACT**PinOut RXD of the FTDI will be disconnected. This pin is necessary for UART operation.
   * **WORKAROUND**  
     Connect pinout RF0 the RXD pad on the H1 pinout the FTDI pad using a wire.
5. **MCLR pin unconnected on FTDI - UART operation impossible**
   * **PROBLEM DEFINITION**Pin number 7 on the PIC33EP512GP806 microcontroller (MCLR) should connect to pad RTS on CAN2 but does not.
   * **SCOPE OF IMPACT**Pin RST of the FTDI will be disconnected. This pin is necessary for UART operation, functioning as the FTDI equivalent of #MCLR.
   * **WORKAROUND**  
     Connect the MCLR# on ICSP pin 7 to the RTS pad of FTDI using a wire.
6. **R6 and R7 unconnected - LCD operation impossible**
   * **PROBLEM DEFINITION**Resister number 6 and 7 should connect but does not.
   * **SCOPE OF IMPACT**The resister R6 will be disconnected to VO pin of the LCD. This connection is necessary for creating voltage divider circuit and better performance by the LCD.
   * **WORKAROUND**  
     Connect the resister R6 and R7 using a wire.