**Errata for ECE 4723/6723 Lab Development Board**

This document details the errata for the custom-developed ECE 4723/6723 lab development board. Details include affected areas and possible solutions. Please see the device’s datasheet for a full functional description.

**ECE 4723/6723 Lab Development Board Errata Summary**

1. **Resistor R6 is unconnected from R7 and VO**
   * **PROBLEM DEFINITION**Resistor R6 connects the 3.3 V source and one end of capacitor C2000 to pin VO on the LCD Char Module and resistor R7. This connection does not exist.
   * **SCOPE OF IMPACT**R6 brakes the voltage divider which provides the contrast of the LCD display.
   * **WORKAROUND**  
     We would add a wire from R6 to Vo to make the Contrast of the LCD display right.
2. **RD8 pin unconnected**
   * **PROBLEM DEFINITION**Pin number 42 on the PIC33EP512GP806 microcontroller (RD8) should connect to pad RD8 on H2 but does not.
   * **SCOPE OF IMPACT**PinOut of RD8 on H2 will be disconnected.
   * **WORKAROUND**  
     Connect pin 42 on the microcontroller to the RD8 pad on H2 using a wire.
3. **SCL pin unconnected**
   * **PROBLEM DEFINITION**Pin number 37 on the PIC33EP512GP806 microcontroller (SCL) should connect to pad SCL on H2 but does not.
   * **SCOPE OF IMPACT**PinOut of SCL on H2 will be disconnected.
   * **WORKAROUND**  
     Connect pin 37 on the microcontroller to the SCL pad on H2 using a wire.
4. **SDA pin unconnected**
   * **PROBLEM DEFINITION**Pin number 38 on the PIC33EP512GP806 microcontroller (SDA) should connect to pad SDA on H2 but does not.
   * **SCOPE OF IMPACT**PinOut of SDA H2 will be disconnected.
   * **WORKAROUND**  
     Connect pin 38 on the microcontroller to the SDA pad on H2 using a wire.