# (+91) 757-924-9119

# DEEPAK RAWAT

India. PIN - 246149 deepak\_rawat@bel.co.in

# **Employment**

### Senior Engineer in Design (E-III)

#### **Bharat Electronics Limited**

Jan 2021 – Present

- As part of National Common Mobility Card NCMC (One nation one card) ecosystem development initiative led by Ministry of Housing And Urban Affairs (MoHUA) with NPCI, DMRC, CDAC, SBI, BMRCL, BEST Mumbai, STQC.
- Designed and developed embedded hardware for controlling, validation and power supply unit etc. used in Automatic fare collection gates to serve mass transit requirement for Delhi Metro and Bangalore Metro.
- . Currently working on development of RFID token reader/ writers and token collection systems for public transport operators (PTO's).

#### Senior Engineer in Design (E-II)

#### **Bharat Electronics Limited**

Oct 2018 - Dec 2020

- Designed microcontroller based hardware for Geo tagging system for SPIC DRDO Delhi to lay and trace underground Optical fiber cables with accuracy to less than 10 cm.
- Involved in study, kick off meetings, requirement gathering, specs finalization to develop indigenous Driverless Train Operation (DTO) as part of Communication Based Train Control (CBTC) system for DMRC.

#### Senior Engineer in Testing (E-II)

#### **Bharat Electronics Limited**

Oct 2016 - Sep 2018

 My responsibilities included testing and debugging of cooled and uncooled thermal imagers, laser range finders, CCD cameras, holographic sight for riffles, Night vision devices, LCD displays for T-90 and T-72 tanks etc.

# Languages and Technologies

- Programming Language C, PLC Ladder Logics, VHDL, Python, Java
- Operating System Linux(Ubuntu, RHEL), Windows
- Tools and Technologies OrCAD, Allegro, Eclipse, Jupyter Notebook, Netbeans, Docklight, Putty, Code studio (CS+), Quartus etc.
- Technical Electives Embedded systems, Microcontrollers (Renesas, ATMEL), PLC, FPGA's, CPLD's, Arm Cortex Processors, GPS Receivers etc.

#### Education

# G.B. Pant Engineering College, Pauri Garhwal, U.K.

2012 - 2016

B.Tech. in Electronics and Communications Engineering

# G.B. Pant Engineering College, Pauri Garhwal, U.K.

2012 - 2016

Uttarakhand State Board (12th class)

#### Saraswati Vidya Mandir Srikot, Srinagar U.K.

2009 - 2010

Uttarakhand State Board (10th class)

# **Worked on Projects**

- Automatic Fare Collection System for BMRCL and DMRC
- Hand Held Terminals and Electronic Ticketing Machines for BEST Mumbai
- Indigenous Communication Based Train Control for DMRC
- Commander Sight for T72 Tanks for Indian Army
- Mine Field Recording Systme for Indian Army
- Geo Tagging System for SPIC DRDO