

Simulate a direct access cache and 2 way Set-Associative cache using assembly programming.

The size of main memory is 1024 words. The memory is divided into 256 blocks, each having size of 4 words. The cache is of 32 blocks. Block size is same as of main memory i.e. 4 words. For memory read/write accessing queries, file containing random strings of hexadecimal addresses is provided to you. Calculate hit rate and miss rate for the memory accesses defined by the addresses in the file.