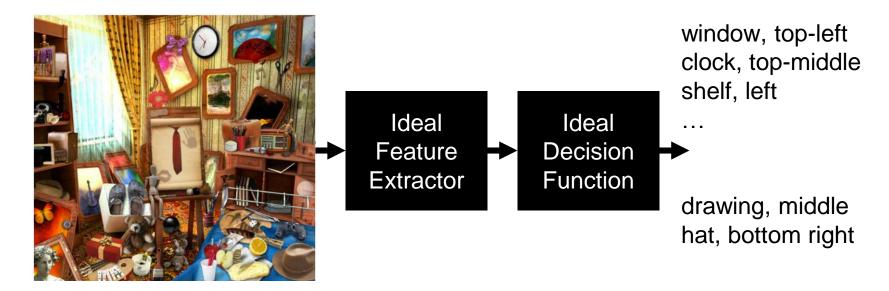
# Intro to Deep Learning Systems TensorFlow/PyTorch Tutorial

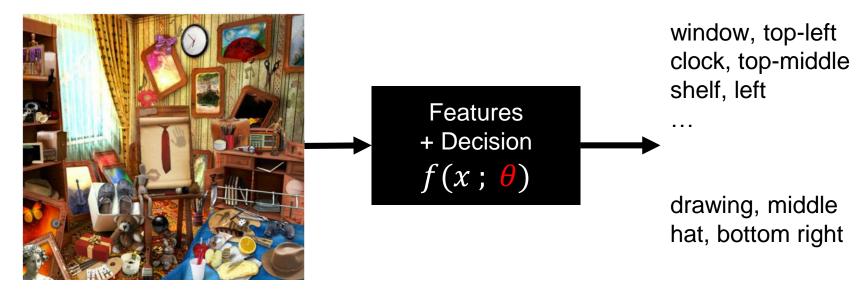
#### Parametric model



Q: What types of features shall we consider?

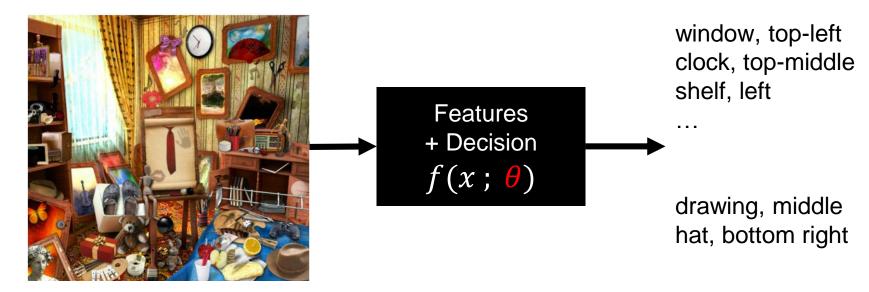
Q: What types of decision functions shall we consider?

#### Representation learning



Q: Which class of functions shall we consider for *f*?

#### Representation learning



Proposal: Composing a set of nonlinear functions g

$$f(x;\theta) = g_1(g_2(...g_n(x;\theta_n)...;\theta_2);\theta_1)$$

#### Deep learning (brief intro)

Deep Learning: Composing a set of nonlinear functions g

$$f(x; \theta) = g_1(g_2(...g_n(x; \theta_n)...; \theta_2); \theta_1)$$

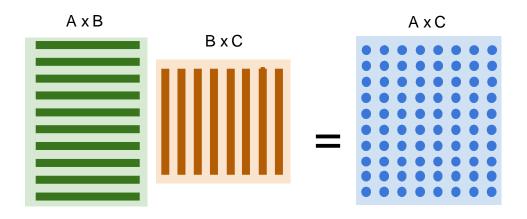
- Make predictions by using a sequence of non-linear processing stages
- The resulting intermediate representations can be interpreted as feature hierarchies
- The whole system is jointly learned from data

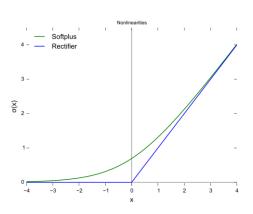
#### Deep learning (brief intro)

Deep Learning: Composing a set of nonlinear functions g

$$f(x;\theta) = g_1(g_2(...g_n(x;\theta_n)...;\theta_2);\theta_1)$$

Key Elements: Linear operations + Nonlinear activations

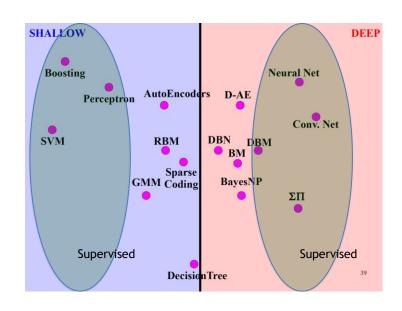




#### Deep learning (brief intro)

- A large family of methods!
  - supervised / unsupervised
  - probabilistic / deterministic

Computational demanding



Require new hardware and software

# Deep Learning Software

#### Generations of frameworks!

Caffe (UC Berkeley)

Layers + Blobs

TensorFlow (Google)

Static Computational Graph

PyTorch (Facebook)

Dynamic Computational Graph

What is the difference?

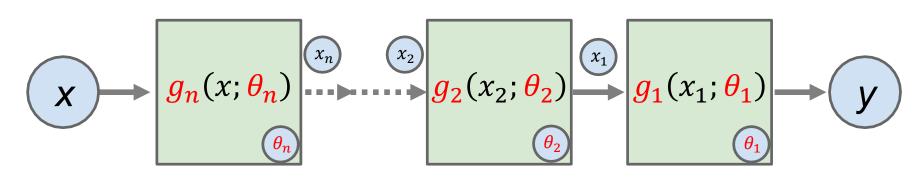
#### Designing a deep learning system

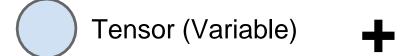
Key Principle: How to present the composition of nonlinear functions & make the computation efficient?

$$f(x; \theta) = g_1(g_2(...g_n(x; \theta_n)...; \theta_2); \theta_1)$$

#### Stack of nonlinear functions

$$f(x; \theta) = g_1(g_2(...g_n(x; \theta_n)...; \theta_2); \theta_1)$$





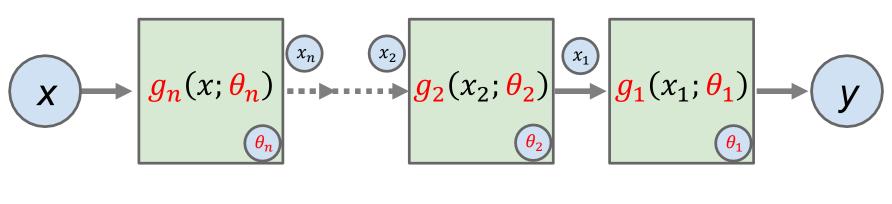


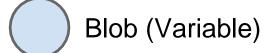


**Function** 

#### Caffe: Model = Blobs + Layers

$$f(x; \theta) = g_1(g_2(...g_n(x; \theta_n)...; \theta_2); \theta_1)$$









Layer

#### Tensors + Operations

$$f(x;\theta) = g_1(g_2(\dots g_n(x;\theta_n)\dots;\theta_2);\theta_1)$$

$$x \longrightarrow g_n(x;\theta_n) \xrightarrow{x_n} g_2(x_2;\theta_2) \xrightarrow{x_1} g_1(x_1;\theta_1)$$

$$g_2(x_2;\theta_2) \xrightarrow{\theta_2} g_1(x_1;\theta_1)$$

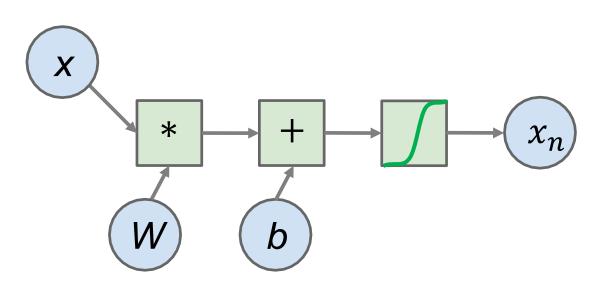
$$g_1(x_1;\theta_1) \longrightarrow g_1(x_1;\theta_1)$$

$$g_2(x_2;\theta_2) \longrightarrow g_1(x_1;\theta_1)$$

$$g_1(x_1;\theta_1) \longrightarrow g_1(x_1;\theta_1)$$

## Tensor + Operations = Computational graph

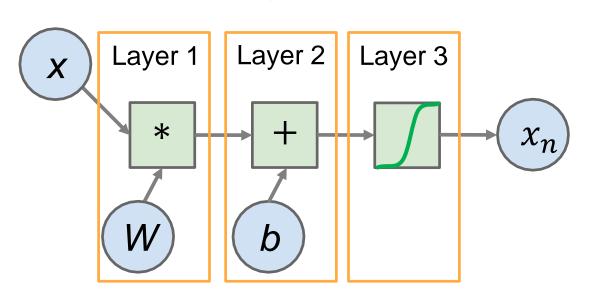
$$g_n(x; W, b) = \sigma(Wx + b)$$



- Decompose functions into atomic operations
- Separate data (tensors) and computing (ops)
- Highly modular

### Static computational graph: Caffe

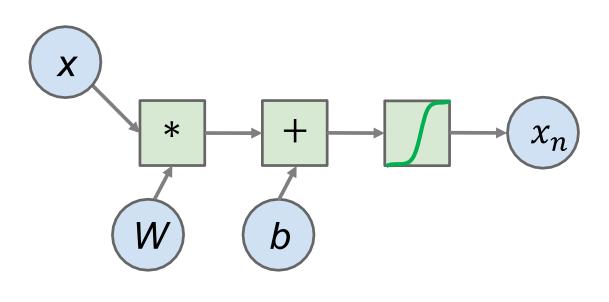
$$g_n(x; W, b) = \sigma(Wx + b)$$



- Intuitive (layer 1, 2, 3)
- Hard to optimize the graph
- Hard to reconfigure the graph (once built)

### Static computational graph: TensorFlow

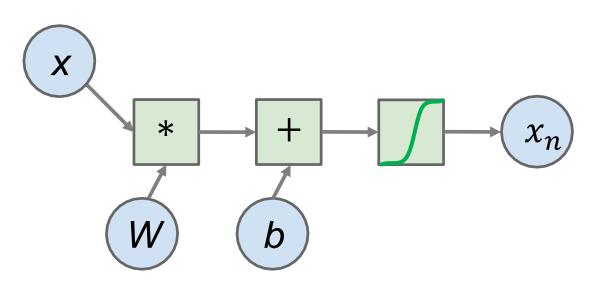
$$g_n(x; W, b) = \sigma(Wx + b)$$



- Build once and run many times
- Easy to optimize the graph
- Hard to reconfigure the graph (once built)

### Dynamic computational graph: PyTorch

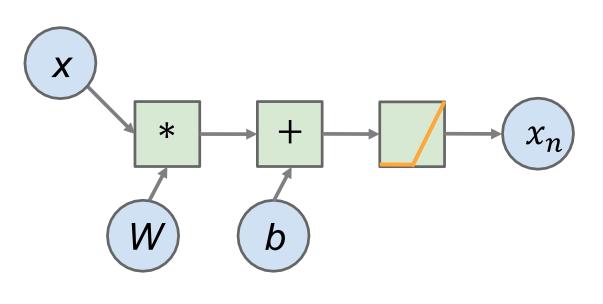
$$g_n(x; W, b) = \sigma(Wx + b)$$



Rebuild the graph at each run

### Dynamic computational graph: PyTorch

$$g_n(x; W, b) = \sigma(Wx + b)$$

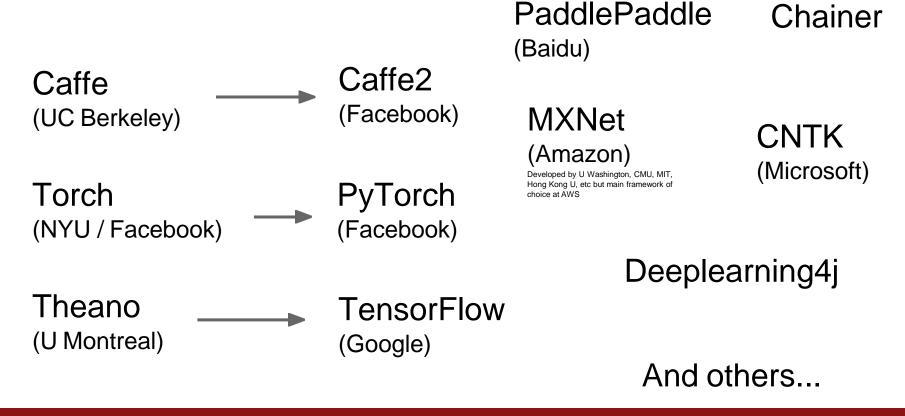


- Rebuild the graph at each run
- Easy to reconfigure the graph
- Hard to optimize the graph

#### The point of deep learning frameworks

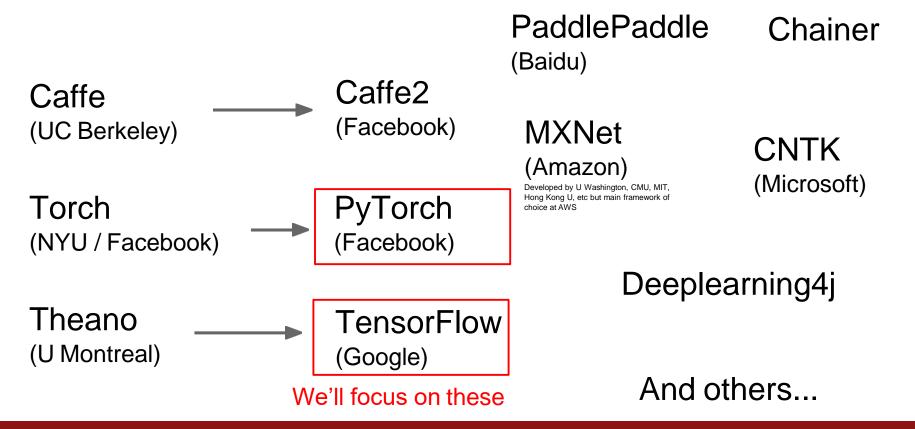
- Quick to develop and test new ideas
- Flexible for building complicated models
- Automatically compute gradients
- Run it all efficiently on GPU (wrap cuDNN, cuBLAS, etc)

#### A zoo of frameworks!



April 26, 2018

#### A zoo of frameworks!

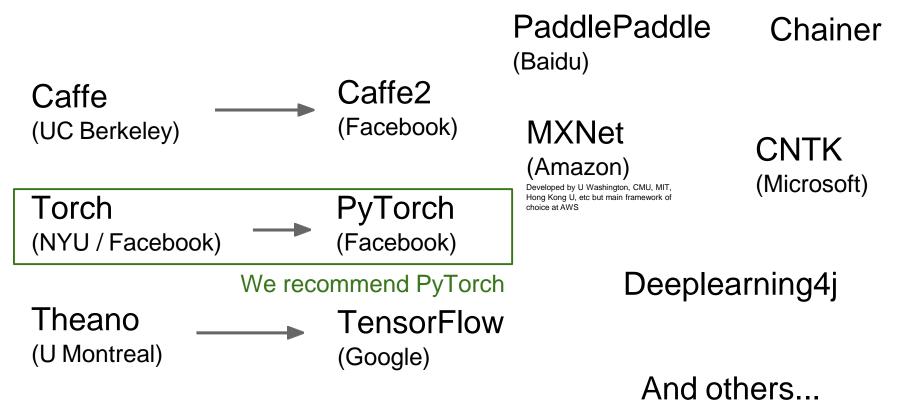


Fei-Fei Li & Justin Johnson & Serena Yeung

Lecture 8 -

April 26, 2018

#### A zoo of frameworks!



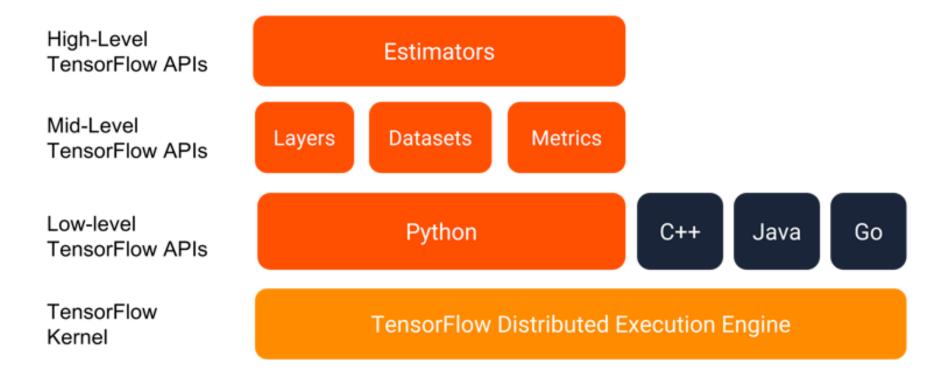
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#### TensorFlow

#### TensorFlow Stack

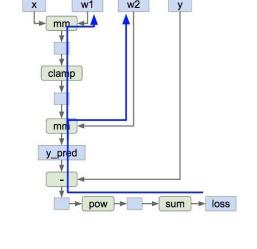


#### Static Computation Graphs

Step 1: Build computational graph describing our computation (including finding paths for backprop)

Step 2: Reuse the same graph on every iteration

\* Can be a bit tricky to implement!



```
graph = build graph()
for x batch, y batch in loader:
    run graph(graph, x=x batch, y=y batch)
```

#### TensorFlow: Neural Net

First **define** computational graph

$$y = w_2^T \max(w_1^T x, 0)$$

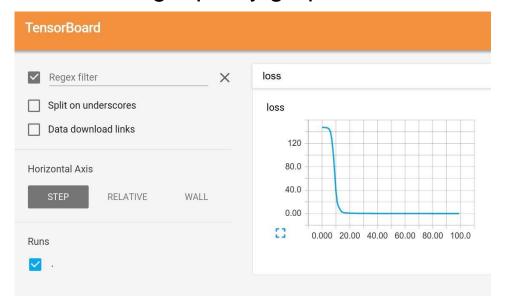
Then **run** the graph many times

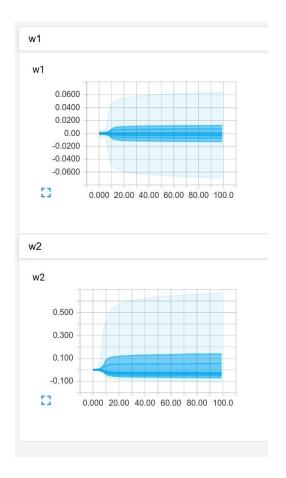
```
N, D, H = 64, 1000, 100
x = tf.placeholder(tf.float32, shape=(N, D))
y = tf.placeholder(tf.float32, shape=(N, D))
w1 = tf.placeholder(tf.float32, shape=(D, H))
w2 = tf.placeholder(tf.float32, shape=(H, D))
h = tf.maximum(tf.matmul(x, w1), 0)
y pred = tf.matmul(h, w2)
diff = y pred - y
loss = tf.reduce mean(tf.reduce_sum(diff ** 2, axis=1))
grad_w1, grad_w2 = tf.gradients(loss, [w1, w2])
```

```
with tf.Session() as sess:
    values = {x: np.random.randn(N, D),
              wl: np.random.randn(D, H),
              w2: np.random.randn(H, D),
              y: np.random.randn(N, D),}
   out = sess.run([loss, grad w1, grad w2],
                   feed dict=values)
    loss val, grad wl val, grad w2 val = out
```

#### TensorFlow: Tensorboard

Add logging to code to record loss, stats, etc Run server and get pretty graphs!





### PyTorch

#### PyTorch: Fundamental Concepts

Tensor: Like a numpy array, but can run on GPU

**Autograd**: Package for building computational graphs out of Tensors, and automatically computing gradients

**Module**: A neural network layer; may store state or learnable weights

#### PyTorch: Versions

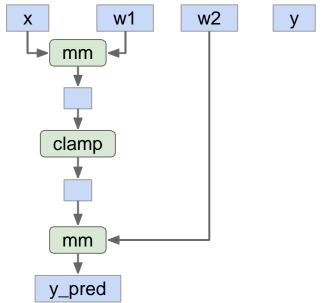
For this class we are using **PyTorch version 1.0.1** 

Be careful if you are looking at older PyTorch code (< 0.4)!

x w1 w2 y

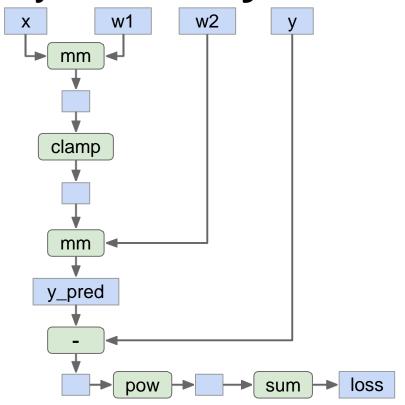
```
import torch
N, D in, H, D out = 64, 1000, 100, 10
x = torch.randn(N, D in)
y = torch.randn(N, D out)
w1 = torch.randn(D in, H, requires_grad=True)
w2 = torch.randn(H, D out, requires grad=True)
learning rate = 1e-6
for t in range(500):
    y pred = x.mm(w1).clamp(min=0).mm(w2)
    loss = (y pred - y).pow(2).sum()
    loss.backward()
```

Create Tensor objects



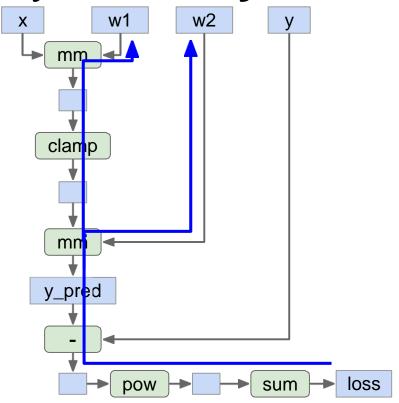
```
import torch
N, D_in, H, D_out = 64, 1000, 100, 10
x = torch.randn(N, D in)
y = torch.randn(N, D out)
w1 = torch.randn(D_in, H, requires_grad=True)
w2 = torch.randn(H, D out, requires grad=True)
learning_rate = 1e-6 y = w_2^T max(w_1^T x, 0)
for t in range(500):
    y pred = x.mm(w1).clamp(min=0).mm(w2)
    loss = (y pred - y).pow(2).sum()
    loss.backward()
```

Build graph data structure AND perform computation



```
import torch
N, D_in, H, D_out = 64, 1000, 100, 10
x = torch.randn(N, D in)
y = torch.randn(N, D_out)
w1 = torch.randn(D_in, H, requires_grad=True)
w2 = torch.randn(H, D out, requires grad=True)
learning rate = 1e-6
for t in range(500):
    y pred = x.mm(w1).clamp(min=0).mm(w2)
    loss = (y pred - y).pow(2).sum()
    loss.backward()
```

Build graph data structure AND perform computation



```
import torch
N, D_in, H, D_out = 64, 1000, 100, 10
x = torch.randn(N, D in)
y = torch.randn(N, D_out)
w1 = torch.randn(D_in, H, requires_grad=True)
w2 = torch.randn(H, D out, requires grad=True)
learning rate = 1e-6
for t in range(500):
    y pred = x.mm(w1).clamp(min=0).mm(w2)
    loss = (y pred - y).pow(2).sum()
    loss.backward()
```

Search for path between loss and w1, w2 (for backprop) AND perform computation

x w1 w2 y

```
import torch
N, D in, H, D out = 64, 1000, 100, 10
x = torch.randn(N, D in)
y = torch.randn(N, D out)
w1 = torch.randn(D_in, H, requires_grad=True)
w2 = torch.randn(H, D out, requires grad=True)
learning rate = 1e-6
for t in range(500):
    y pred = x.mm(w1).clamp(min=0).mm(w2)
    loss = (y pred - y).pow(2).sum()
    loss.backward()
```

Throw away the graph, backprop path, and rebuild it from scratch on every iteration

## PyTorch: **Dynamic** Computation Graphs

**Building** the graph and **computing** the graph happen at the same time.

Seems inefficient, especially if we are building the same graph over and over again...

```
import torch
N, D_{in}, H, D_{out} = 64, 1000, 100, 10
x = torch.randn(N, D in)
y = torch.randn(N, D out)
w1 = torch.randn(D_in, H, requires_grad=True)
w2 = torch.randn(H, D out, requires grad=True)
learning rate = 1e-6
for t in range(500):
    y pred = x.mm(w1).clamp(min=0).mm(w2)
    loss = (y pred - y).pow(2).sum()
    loss.backward()
```

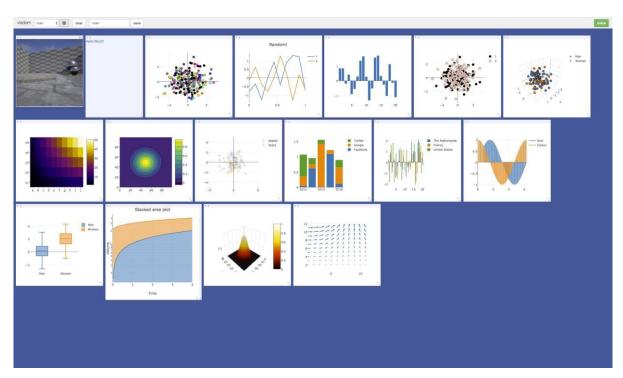
## PyTorch: Demos

https://pytorch.org/tutorials/beginner/blitz/cifar10\_tutorial.html#sphx-glr-beginner-blitz-cifar10-tutorial-py

### PyTorch: Visdom

Visualization tool: add logging to your code, then visualize in a browser

Can't visualize computational graph structure (yet?)



https://github.com/facebookresearch/visdom

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# **PyTorch**Dynamic Graphs

VS TensorFlow Static Graphs

#### Static vs Dynamic Graphs

**TensorFlow**: Build graph once, then run many times (**static**)

```
N, D, H = 64, 1000, 100
x = tf.placeholder(tf.float32, shape=(N, D))
y = tf.placeholder(tf.float32, shape=(N, D))
w1 = tf.Variable(tf.random normal((D, H)))
w2 = tf.Variable(tf.random normal((H, D)))
h = tf.maximum(tf.matmul(x, w1), 0)
y pred = tf.matmul(h, w2)
diff = y pred - y
loss = tf.reduce mean(tf.reduce sum(diff ** 2, axis=1))
grad w1, grad w2 = tf.gradients(loss, [w1, w2])
learning rate = 1e-5
new w1 = w1.assign(w1 - learning rate * grad w1)
new w2 = w2.assign(w2 - learning rate * grad w2)
updates = tf.group(new wl, new w2)
with tf.Session() as sess:
    sess.run(tf.global variables initializer())
    values = {x: np.random.randn(N, D),
              y: np.random.randn(N, D),}
    losses = []
    for t in range(50):
        loss val, = sess.run([loss, updates],
                               feed dict=values)
```

**PyTorch**: Each forward pass defines a new graph (**dynamic**)

```
Build
graph

N, D_in, H, D_out = 64, 1000, 100, 10
x = torch.randn(N, D_in)
y = torch.randn(D_in, H, requires_grad=True)
w1 = torch.randn(H, D_out, requires_grad=True)
w2 = torch.randn(H, D_out, requires_grad=True)

learning_rate = 1e-6
for t in range(500):
    y_pred = x.mm(w1).clamp(min=0).mm(w2)
    loss = (y_pred - y).pow(2).sum()

loss.backward()

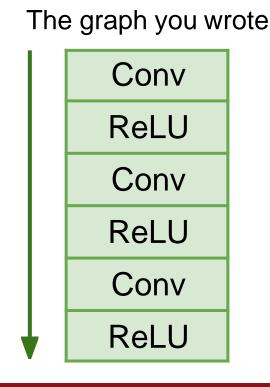
New graph each iteration
```

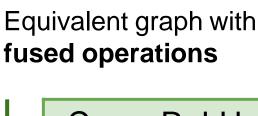
Run each

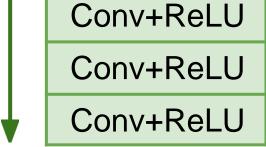
iteration

## Static vs Dynamic: Optimization

With static graphs, framework can **optimize** the graph for you before it runs!







## Static vs Dynamic: Serialization

#### **Static**

Once graph is built, can serialize it and run it without the code that built the graph!

#### **Dynamic**

Graph building and execution are intertwined, so always need to keep code around

# Static vs **Dynamic**: Conditional

$$y = \begin{cases} w1 * x & \text{if } z > 0 \\ w2 * x & \text{otherwise} \end{cases}$$

## Static vs <u>Dynamic</u>: Conditional

$$y = \begin{cases} w1 * x & \text{if } z > 0 \\ w2 * x & \text{otherwise} \end{cases}$$

#### **PyTorch**: Normal Python

```
N, D, H = 3, 4, 5
x = torch.randn(N, D, requires grad=True)
w1 = torch.randn(D, H)
w2 = torch.randn(D, H)
z = 10
if z > 0:
    y = x.mm(w1)
else:
    y = x.mm(w2)
```

## Static vs **Dynamic**: Conditional

$$y = \begin{cases} w1 * x & \text{if } z > 0 \\ w2 * x & \text{otherwise} \end{cases}$$

#### **PyTorch**: Normal Python

```
N, D, H = 3, 4, 5

x = torch.randn(N, D, requires_grad=True)
w1 = torch.randn(D, H)
w2 = torch.randn(D, H)

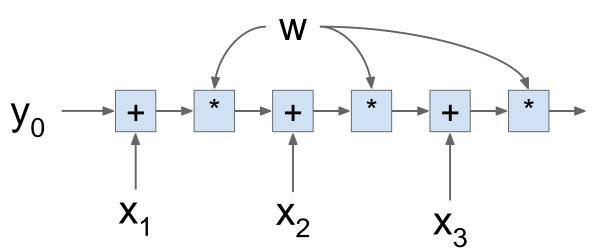
z = 10
if z > 0:
    y = x.mm(w1)
else:
    y = x.mm(w2)
```

# **TensorFlow:** Special TF control flow operator!

```
N, D, H = 3, 4, 5
x = tf.placeholder(tf.float32, shape=(N, D))
z = tf.placeholder(tf.float32, shape=None)
w1 = tf.placeholder(tf.float32, shape=(D, H))
w2 = tf.placeholder(tf.float32, shape=(D, H))
def f1(): return tf.matmul(x, w1)
def f2(): return tf.matmul(x, w2)
y = tf.cond(tf.less(z, 0), f1, f2)
with tf.Session() as sess:
    values = {
        x: np.random.randn(N, D),
        z: 10,
        w1: np.random.randn(D, H),
        w2: np.random.randn(D, H),
    y val = sess.run(y, feed dict=values)
```

# Static vs **Dynamic**: Loops

$$y_t = (y_{t-1} + x_t) * w$$



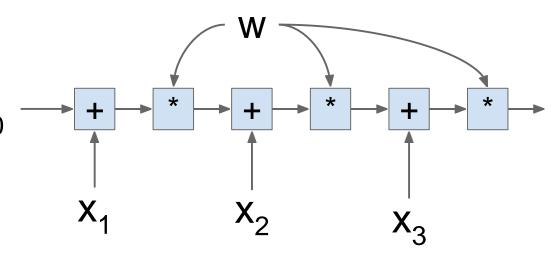
## Static vs **Dynamic**: Loops

$$y_t = (y_{t-1} + x_t) * w$$

**PyTorch**: Normal Python

```
T, D = 3, 4
y0 = torch.randn(D, requires_grad=True)
x = torch.randn(T, D)
w = torch.randn(D)

y = [y0]
for t in range(T):
    prev_y = y[-1]
    next_y = (prev_y + x[t]) * w
    y.append(next_y)
```



## Static vs <u>Dynamic</u>: Loops

$$y_t = (y_{t-1} + x_t) * w$$

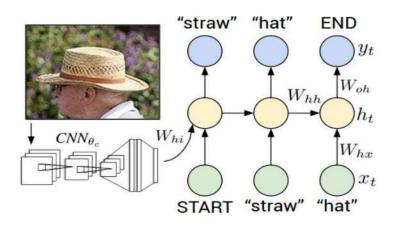
#### **PyTorch**: Normal Python

```
T, D = 3, 4
y0 = torch.randn(D, requires grad=True)
x = torch.randn(T, D)
w = torch.randn(D)
y = [y0]
for t in range(T):
    prev y = y[-1]
    next y = (prev_y + x[t]) * w
    y.append(next y)
```

#### **TensorFlow**: Special TF control flow

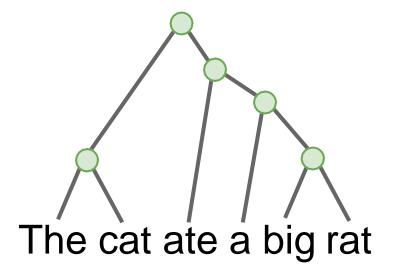
```
T, N, D = 3, 4, 5
x = tf.placeholder(tf.float32, shape=(T, D))
y0 = tf.placeholder(tf.float32, shape=(D,))
w = tf.placeholder(tf.float32, shape=(D,))
def f(prev y, cur x):
     return (prev y + cur x) * w
y = tf.foldl(f, x, y0)
with tf.Session() as sess:
    values = {
         x: np.random.randn(T, D),
         y0: np.random.randn(D),
         w: np.random.randn(D),
    y_val = sess.run(y, feed dict=values)
```

Recurrent networks



Karpathy and Fei-Fei, "Deep Visual-Semantic Alignments for Generating Image Descriptions", CVPR 2015 Figure copyright IEEE, 2015. Reproduced for educational purposes.

- Recurrent networks
- Recursive networks



- Recurrent networks
- Recursive networks
- Modular Networks

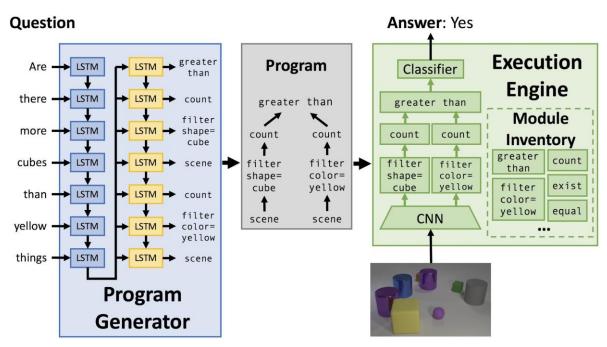


Figure copyright Justin Johnson, 2017. Reproduced with permission.

Andreas et al. "Neural Module Networks". CVPR 2016 Andreas et al, "Learning to Compose Neural Networks for Question Answering", NAACL 2016 Johnson et al. "Inferring and Executing Programs for Visual Reasoning". ICCV 2017

- Recurrent networks
- Recursive networks
- Modular Networks
- (Your creative idea here)

# PyTorch vs TensorFlow, Static vs Dynamic

**PyTorch** 

Dynamic Graphs
Static: ONNY

Static: ONNX

**TensorFlow** 

Static Graphs

Dynamic: Eager

# Deep Learning Hardware

#### My computer



#### Spot the CPU!

(central processing unit)



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#### Spot the GPUs!

(graphics processing unit)



This image is in the public domain





VS

**AMD** 

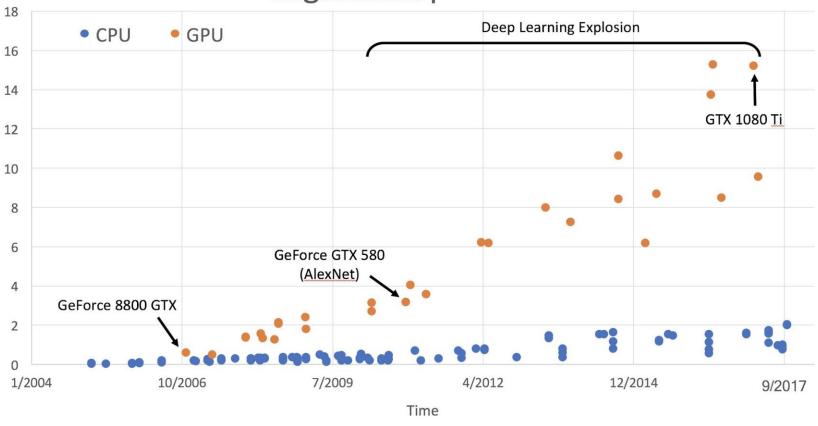
#### CPU vs GPU

	Cores	Clock Speed	Memory	Price	Speed
CPU (Intel Core i7-7700k)	4 (8 threads with hyperthreading)	4.2 GHz	System RAM	\$339	~540 GFLOPs FP32
GPU (NVIDIA GTX 1080 Ti)	3584	1.6 GHz	11 GB GDDR5 X	\$699	~11.4 TFLOPs FP32

CPU: Fewer cores, but each core is much faster and much more capable; great at sequential tasks

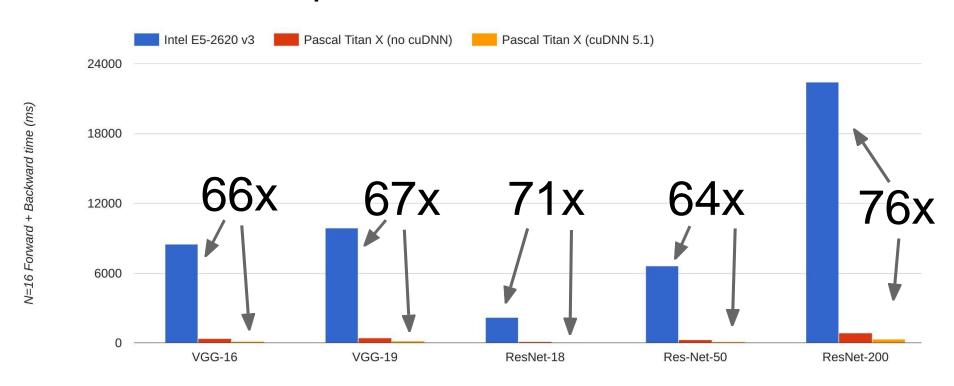
**GPU**: More cores, but each core is much slower and "dumber"; great for parallel tasks

#### GigaFLOPs per Dollar



#### CPU vs GPU in practice

(CPU performance not well-optimized, a little unfair)



Data from https://github.com/jcjohnson/cnn-benchmarks

#### CPU vs GPU

	Cores	Clock Speed	Memory	Price	Speed
CPU (Intel Core i7-7700k)	4 (8 threads with hyperthreading)	4.2 GHz	System RAM	\$339	~540 GFLOPs FP32
GPU (NVIDIA GTX 1080 Ti)	3584	1.6 GHz	11 GB GDDR5 X	\$699	~11.4 TFLOPs FP32
TPU NVIDIA TITAN V	5120 CUDA, 640 Tensor	1.5 GHz	12GB HBM2	\$2999	~14 TFLOPs FP32 ~112 TFLOP FP16
<b>TPU</b> Google Cloud TPU	?	?	64 GB HBM	\$6.50 per hour	~180 TFLOP

**CPU**: Fewer cores, but each core is much faster and much more capable; great at sequential tasks

**GPU**: More cores, but each core is much slower and "dumber"; great for parallel tasks

**TPU**: Specialized hardware for deep learning

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Lecture 8 -

April 26, 2018

#### Programming GPUs

- CUDA (NVIDIA only)
  - Write C-like code that runs directly on the GPU
  - Optimized APIs: cuBLAS, cuFFT, cuDNN, etc
- OpenCL
  - Similar to CUDA, but runs on anything
  - Usually slower on NVIDIA hardware
- HIP <a href="https://github.com/ROCm-Developer-Tools/HIP">https://github.com/ROCm-Developer-Tools/HIP</a>
  - New project that automatically converts CUDA code to something that can run on AMD GPUs
- Udacity: Intro to Parallel Programming https://www.udacity.com/course/cs344
  - For deep learning just use existing libraries

#### **CPU / GPU Communication**

Model is here



Data is here

#### **CPU / GPU Communication**

Model is here



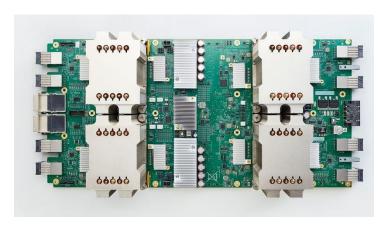
#### Data is here

If you aren't careful, training can bottleneck on reading data and transferring to GPU!

#### Solutions:

- Read all data into RAM
- Use SSD instead of HDD
- Use multiple CPU threads to prefetch data

## **Tensor Processing Units**



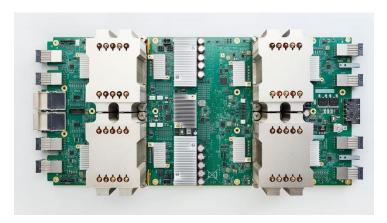
Google Cloud TPU = 180 TFLOPs of compute!



NVIDIA Tesla V100 = 125 TFLOPs of compute

NVIDIA Tesla P100 = 11 TFLOPs of compute GTX 580 = 0.2 TFLOPs

### **Tensor Processing Units**



Google Cloud TPU = 180 TFLOPs of compute!



Google Cloud TPU Pod

= 64 Cloud TPUs

= 11.5 PFLOPs of compute!

https://www.tensorflow.org/versions/master/programmers\_guide/using\_tpu