

Marketing Presentation







Continuing the STM32 Success Story

Leader in Arm Cortex-M 32-bit General Purpose MCU

Cortex-M0+ MCUs
Efficiency at its best!

1st Mixed Signal
DSP + Analog
STM32F3 Cortex-M4

STM32F3

1st High Perf.
Cortex-M4
168 MHz

STM32 F0

STM32F0

Cortex-M0

Entry Cost
Ultra-low-power
Entry Cost



STM32 **F7**



273 ULPBench™



Excellence

#1

Performance





Mainstream

Dual-core, multiprotocol and open radio

2007 2009 2010 2011 2012 2013 2014 2015 2016 2017 2018 2019



World 1st

STM32 F1

World 1st

Cortex-M

Cortex-M MCU Ultra-low-power 120 MHz, 90nm

1st High Perf.

STM32 **F2**



STM32 Rolling Longevity Commitment

Longevity commitment is renewed every year



Starting January 1st 2019

→ Until at least 2029

•	STM32F1	(launched	in	2007)			22 years of comm	itment
•	STM32L1	(launched	in	2009)			20 years of comm	itment
•	STM32F2	(launched	in	2010)			19 years of comm	itment
•	STM32F4	(launched	in	2011)			18 years of comm	itment
•	STM32F0	(launched	in	2012)			17 years of comm	itment
•	STM32F3	(launched	in	2012)			17 years of comm	itment
•	STM32L0	(launched	in	2013)			16 years of comm	itment
•	STM32F7	(launched	in	2014)			15 years of comm	itment
•	STM32L4	(launched	in	2015)			14 years of comm	itment
•	STM32L4+	(launched	in	2016)			13 years of comm	itment
•	STM32H7	(launched	in	2016)			13 years of comm	itment
•	STM32WB	(launched	in	2018)			11 years of comm	itment
•	STM32G0	(launched	in	2018)			11 years of comm	itment





STM32MP1 Product Positioning

STM32MP1





STM32MP1: A General Purpose MPU

Suitable for all Developer Types and Multiple Applications

STM32 **MP1**

Developer profile

MCU users new to MPU

STM32 **MP1**



Mixed MCU and MPU users



Pure MPU users



Possible applications







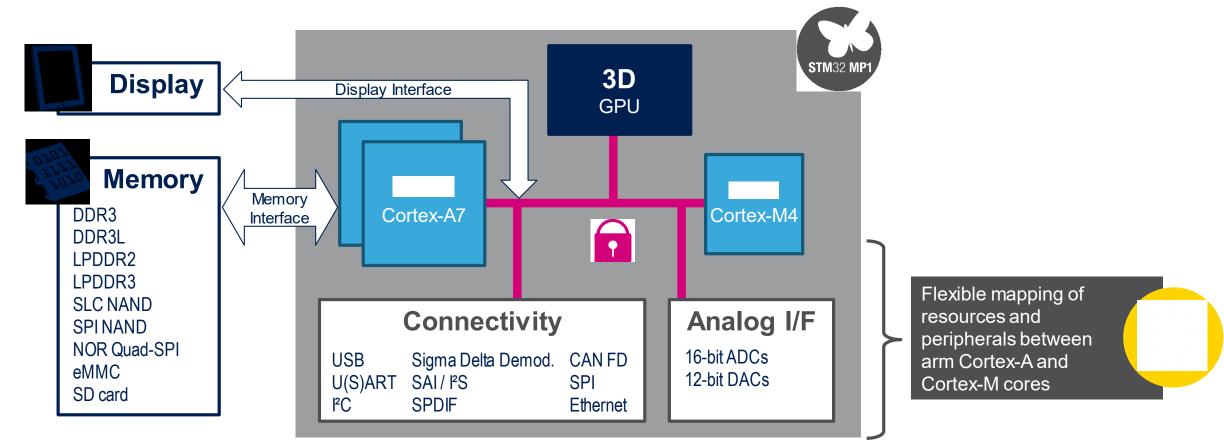






Rich Feature Set ____

Advanced & Flexible Architecture with 3D GPU







STPMIC1 Power Management IC

Simplify your design and optimize power consumption

BOM savings for typical applications

Integrated USB power switches

Small PCB footprint vs. full discrete solution





Graphics 9

- 3D GPU Vivante® GC-Nano
 - OpenGL ES 2.0 / OpenVG 1.1
 - Native support in Linux
 - HMI UI composition with 3D effects
 - Alpha Blending
- Display controller
 - Up to WXGA (1366x768) @ 60fps 24-bits RGB888
 - 2 display layers with programmable colour LUT
 - Support for RAM less display
 - MIPI DSI Tx 2 lanes @ 1Gbps







Supported by the STM32 Ecosystem

All the Tools for Successful MPU Development

Software

Hardware

Customer support











Discovery boards



Evaluation boards











STM32MP1 Hardware Solutions 11

Speed-up evaluation, prototyping and design



















Evaluation Board

Full feature STM32MP1 evaluation

- STM32MP157A-EV1
- STM32MP157C-EV1

Discovery Board

Flexible prototyping & demo

- STM32MP157A-DK1
- STM32MP157C-DK2
 - + MIPI DSI WVGA display
 - + Wi-Fi/BT combo module

Boards & SoM*s

3rd Parties Boards for prototyping and production

- Board Specification from Linaro (96boards.org)
- Commercial SoM w/ different forms





STM32MP157C-DK2 Special Offer 12



- Discounted offer for workshop attendees
- Purchase via Avnet online shop
- Part Number STM32MP157C-DK2WSHOP





STM32MP1 Software Tools 13

Complete support of Arm Cortex-A + Cortex-M architecture





All-in-one STM32 programming tool Multi-mode, user-friendly



STM32CubeMX

STM32CubeMX enhanced for MPU

- Configure and generate Code
- DRAM interface tuning tool
- Device Tree generation

IDEs Compile and Debug

Multi-Core Solutions

- Partners IDE
- Free IDE based on Eclipse
- Multi-core debugging

STM32 Programming Tool

STM32CubeProgrammer

- Flash, DRAM and/or system memory
- **OTP** programming
- Signing tool & Keys generation

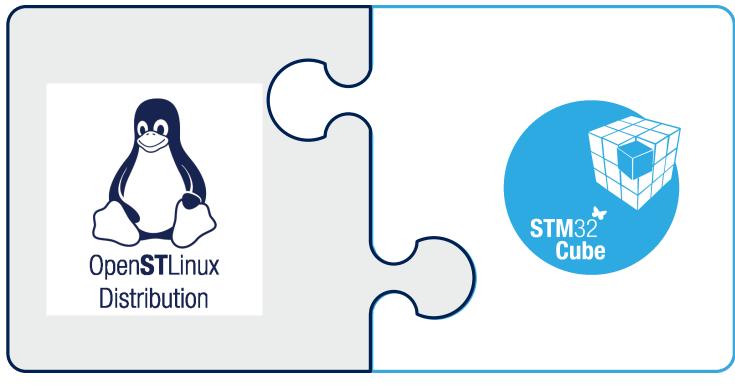




A Fully Integrated Design Suite

Leveraging the STM32Cube Environment







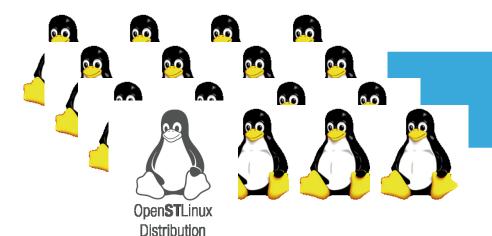
STM32MP1 Embedded Software Distribution





Simplify your Linux Development 15

Fully mainlined open source Linux distribution for Arm Cortex-A7



STM32MP1 SoC drivers already adopted by the Linux community

STM32MP1 supported in Linux 4.19 LTS

Fully compliant with open-source standards





Pre-integrated Secure OS







Benefit from Field-Proven RTOS Tools 161

Full re-use of STM32 MCU Cube firmware on Arm Cortex-M





Several APIs to access peripherals



Collection of Middleware components for Cortex-M



Hundreds of Examples



Production-ready Quality



Business-friendly license terms



smallest

package for dual Cortex-A

GP MPU



STM32MP1 Product Lines

24 Sales Type in Production Now





3 Product Lines

Optional Security — 4 Packages



STM32MP1 Series Partners List 19

- STM32MP1 Partners List evangelizing the STM32MP1 Series Solution
- Highly skilled recognized leaders in their specific domain to help customers
- Early access of the technology to benefit customers program in order to make success
- On-going ST Partner Program to enrich STM32MP1 Series Ecosystem





































SOM Makers suppliers - Overview 20































































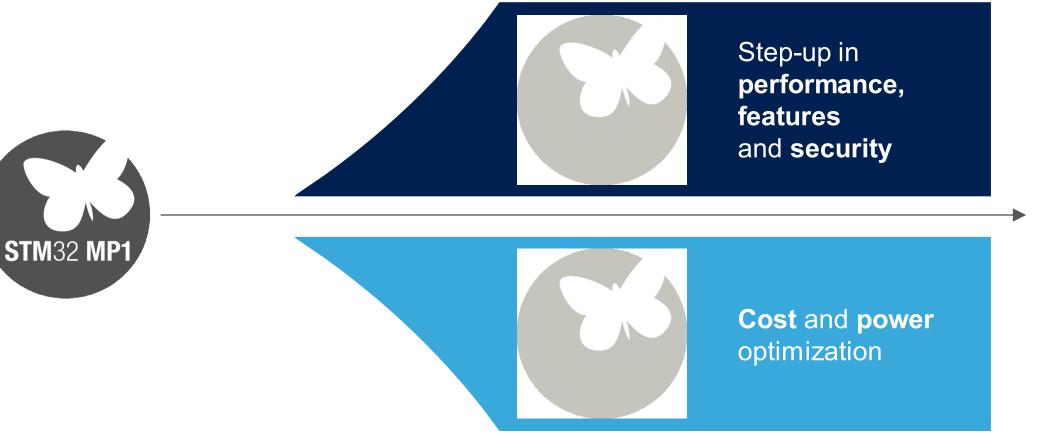






Building the Future

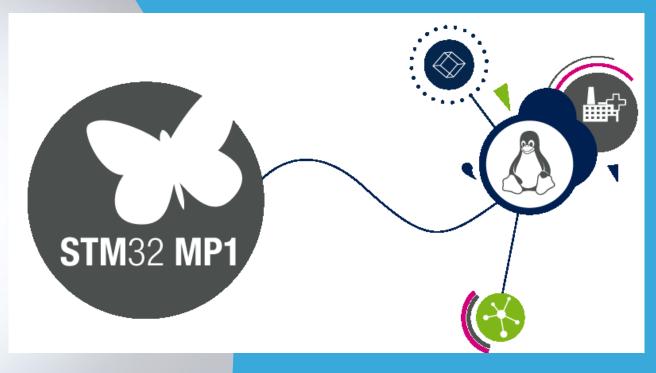
STM32 MPU Portfolio Expansion





Releasing Your Creativity 22











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