

Bhumika K.R

Bangalore |bhumikakr3030@gmail.com| 9035756998| [linkedin](#)

OBJECTIVE

An Enthusiastic engineering student eager to apply academic knowledge and hands-on experience to contribute to innovative projects and gain practical skills in [specific engineering field, e.g.,IT software and electronics] engineering.

Committed to continuous learning, teamwork, and problem solving skills , thinking abilities, analytical abilities to solve complex engineering challenges. Seeking an internship/entry-level position to further develop technical expertise and make a meaningful impact in the engineering industry.

EDUCATION

B-Tech |Electronics and communication | 6.7 GPA

NMIT, Visvesvaraya Technology University

Bangalore, India

Aug 2022 -June 2026

PU | PCMC | 60 % Percentage

Chethana PU College, State Board

Bangalore, India

04, 2020- MM,2022

Class X | 88 % Percentage

Soundarya Central School, CBSE

Bangalore, India

04, 2006 – MM,2020

SKILLS

- Technical Skills:

- CAD (Computer-Aided Design) Software (e.g., AutoCAD, SolidWorks)
- programming languages (C,Python, C++) Web Langauge (HTML,CSS)
- Circuit Design and Analysis LTSpice,Logisim
- LTSpice tools
- Basics of Matlab
- Logisim
- VLSI Design system -Verilog/VHDL using Xinlix,Cadence

- Soft Skills:

- Problem-solving and Critical Thinking
- Communication Skills (Written and Verbal)
- Confidence
- Patience
- Adaptability and Flexibility
- Listening skill
- Honesty
- Creativity
- Interpersonal Skills

WORK HISTORY

VLSI Design Project	Bangalore, India
Project Lead	08, 2023– 11, 2023
<ul style="list-style-type: none">TO get practical knowledge about the circuits of VLSI Identify and apply appropriate Electronic Design Automation (EDA) to solve real world problems in VLSI domain to create innovative products and systems pursue career in research in VLSI design domain through self-learning and self-directed on cutting edge technologies Design, implement, analyse and interpretation of VLSI projects using CAD& EDA tools:Cadence,XilinxTO learn using cadence virtuoso, to this step the schematic is converted to Layout. To the Layout is representation of an IC in terms of geometric shapes which correspond to the patterns of metal, oxide or semiconductor layers that make up the components of the Integrated circuit. Layout is designed by EDA tool such as cadence virtuoso.To the Physical design is a very complex step therefore it is divided into sub steps such as floor planning, placement, clock tree synthesis, routing etc and timing analysis checks are formed in each and every step during physical design.Conducted tests and experiments to validate and refine the project functionality, ensuring accurate and reliable performance.Verilog coding Verilog is a hardware description language (HDL).It is a language used for describing a digital system like a network switch or a microprocessor or a flip-flop. It means, by using a HDL we can describe any digital hardware at any level.To learn code of FPGA using Verilog and To the software called Xilinx and To Simulation and Synthesis: To verify whether the synthesis tool has correctly generated the gate-level netlist a verification should be done.RTL description is used for simulation to test the functionality with the help of EDA tools.Functional verification is performed to ensure the RTL design is done according to the specifications, RTL code is converted to gate level netlist using synthesis tools. Netlist is a description of the circuit in terms of gates and connections between them.	
Additive Manufacturing Designing and Industry 4.0	
Engineering Project Team Member	Bangalore, India
	12, 2023 - 04, 2024
<ul style="list-style-type: none">Led a team of 4 members to design and implement an auto cad fusion 360 system using for simulation.Utilized CAD software fusion 360 to develop and optimize the project design, achieving a 15% improvement in system efficiency.Conducted tests and experiments to validate and refine the project functionality, ensuring accurate and reliable performance.Using aiml tool by given dataPresented project updates and findings to faculty and peers, receiving positive feedback and recognition for innovation and technical excellence.	
Flower using python	
Mini project	06, 2023-12, 2024

- Designed and implemented an pycharm system using Turtle for Graphic Design
- PyCharm is an integrated development environment (IDE) specifically designed for Python programming. It's developed by JetBrains and provides a wide range of tools and features to help developers write, debug, and manage Python code
efficiently.PyCharm Community Edition: This version is free and open-source. It Provides a solid set of features for individual developers and small projects and free version available for developers,students,freshers.
- Turtle is a pre-installed Python library that enables users to create pictures and shapes by providing them with a virtual canvas.
- The onscreen pen that you use for drawing is called the turtle the Python turtle library helps new programmers get a feel for what programming with Python is like in a fun and interactive way.
great for Python beginners.With the Python turtle library, we can draw and create various types of shapes and images. Most developers use turtle to draw shapes, create designs, and make images.Others use turtle to create mini-games and animations
- Develop a mini project using turtle library to create mini games, designs,drawings,animations
- Drawing tools and commands on turtle by software of pycharm app (IDE)
- flower using turtle with design.

CERTIFICATION / TRAINING

Additive Manufacturing Designing and Industry 4.0
EOS, AMSI
Bangalore

RINEX
Entrepreneurship Cell
Python course and frontend web development course

bangalore

Centre for Outreach and digital Education
Indian Institute of Technology Madras
“Strategy Formulation and Data Visualization”
Bangalore

Blood donation certificate

VLSI Design system
Bangalore
