

VIT Vidyasankar Institute of Technology Accredited A+ by NAAC (Autonomous College Affiliated to University of Mumbai)		Weekly Test (R-2022) -(2023-2024)	
Date: 11/03/ 2024	Test No: 2	Time: 1 Hr	Branch: CMPN
Semester: VI		Subject: SPCC	Marks: 30
Q. 1)	Attempt any five (2 Marks Each)		CO
a)	Define a Macro in Pseudo Assembly Language that can perform any logical operation (as specified in call) on 2 memory locations		CO3
b)	Give functions of AIF and AGO statements		CO3
c)	Write a Macro in Pseudo Assembly Language to increment all locations of an array by a given value. Consider array name, array size and increment value to be passed as parameters.		CO3
d)	Compare Functions and Macro		CO3
e)	Give functions of both passes of 2 Pass Macro Preprocessor		CO3
f)	What is the role of KPT and EVS data structure?		CO3
g)	Compare Compile and Go loading scheme and General-purpose loading scheme		CO4
h)	What type of module loader is needed by Linker? Why?		CO4
Q. 2)	Attempt any two. (10 Marks Each)		
a)	Explain with a neat flowchart/ algorithm, working of 2 Pass Simple Macro Pre-processor		CO3
b)	Give databases required by a Single Pass Macro Pre-processor that handles Keyword parameters, Nested Macro Calls and Expansion time loops		CO3
Q 3)	Attempt any two. (5 Marks Each)		
a)	Explain with example, four functions of General Purpose Loader		CO4
b)	Compare Absolute loader, Relocatable loader, and Direct Linking loader		CO4
c)	What is dynamic loading and dynamic linking? What are their advantages over static loading and static linking?		CO4
CO3	Identify the need for different features and designing of macros		
CO4	Distinguish different loaders and linkers and their contribution in developing efficient user applications		

VITVidyalankar
Institute of
Technology

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MSE 2.

Weekly Exam 2 Solution

Mid Semester Examination

Branch	Date	Sem.	Roll No. / Exam Seat No.	Subject	Student's Signature	Junior Supervisor's Name and Sign
CMPN	11/3/24	VI	—	SPCC	V/S	V/S

Question No.	A	B	C	D	E	F	G	H	Total	Total out of (20 / 30 / 40)
1										
2										
3										
4										

Examiners Signature	Student's Sign (After receiving the assessed answer sheet)

1(a)	MACRO LOP &X, &Y, &OP LOAD &X &OP &Y STORE &X MEM)
1(b)	<p>AIIF: Conditional branch instruction which is processed by Macro processor. If the given condition is true Macro processor changes current Expansion pointer to the specified label.</p> <p>Format: AIIF (condition) . label</p> <p>AGO: It is and unconditional branch to specified label during macro expansion.</p> <p>Format: AGO . label</p>

1 (c) MACRO ← Array Name
 INRARR &X, &N, &V ← size
 LCL &I ← increment value
 &I SET 0 // optional..
 *AGAIN AIF (&I = &N) . EXIT
 &I SET &I + 1
 AIF (&I = &N) . EXIT

1 (c) MACRO ← Array Name
 INRARR &X, &N, &V ← Array size
 LCL &I ← increment value
 &I SET 0 // optional
 *AGAIN AIF (&I = &N) . EXIT
 LOAD &X + &I
 ADD ~~&X + &I~~, &V
 STORE &X + &I
 &I SET &I + 1
 AGO . AGAIN
 *EXIT MEND

Sample Macro call

1.
 INRARR G, 3, 5

↑ LOAD G+0
 ADD 5
 STORE G+0
 LOAD G+1
 ADD 5
 STORE G+1
 LOAD G+2
 ADD 5
 STORE G+2

1 (d) (i) Functions are linked post translation whereas macros are expanded before translation.

(ii) In Functions, control is transferred at run time, whereas, in macros, code substitution increases size of code but no control transfer occurs at run time.

1 (e) Pass 1: Define Macro in MNT & MDT
Pass 2: Expand Macro Calls.

1 (f) KPT: Used to store names of keyword parameters & their default.

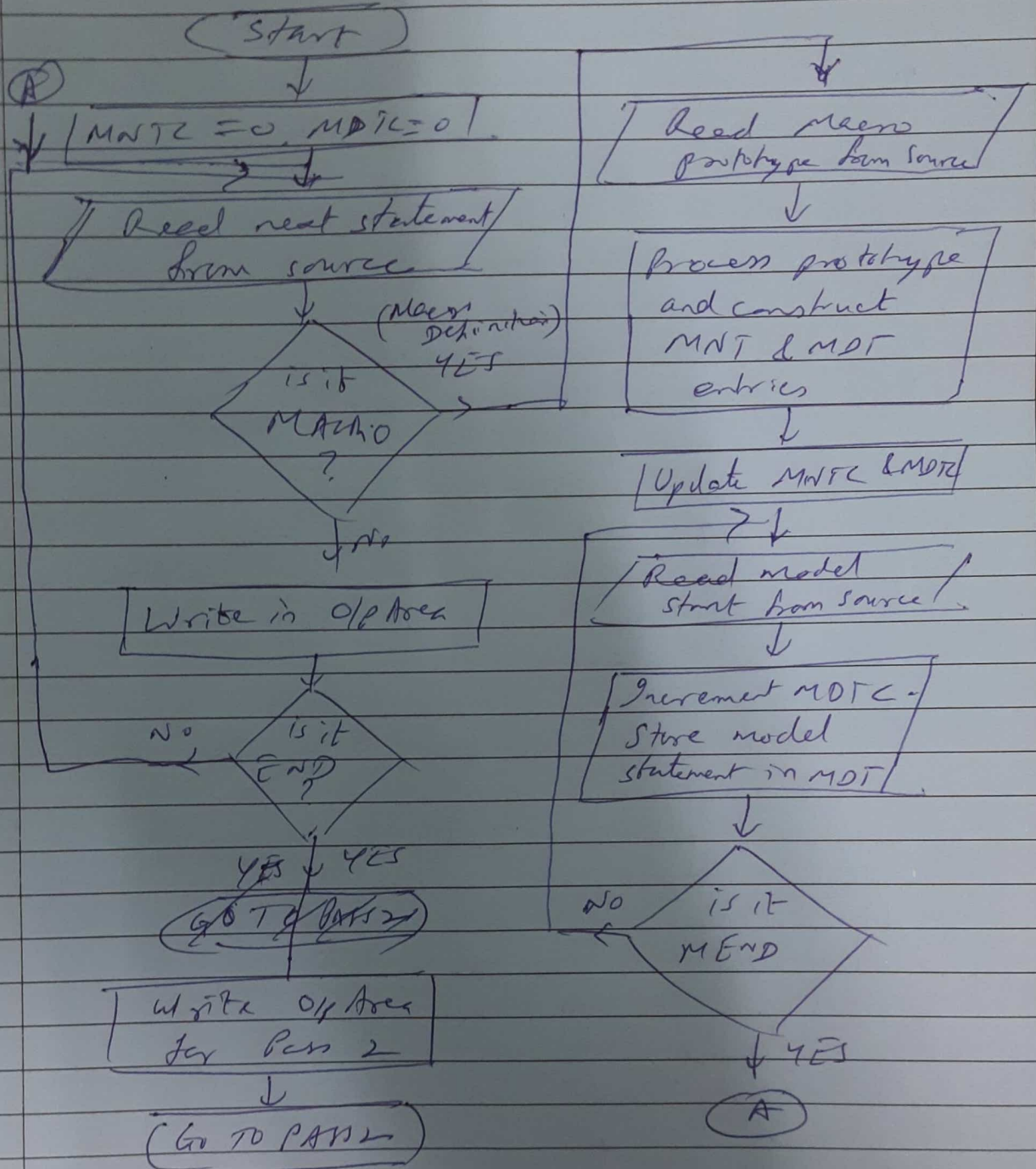
EVS: Used to store values of Expansion time variables during macro expansion.

1 (g) (i) Compile & Go loading scheme requires retranslation every time the program is to be executed. This is not needed in general purpose loader.

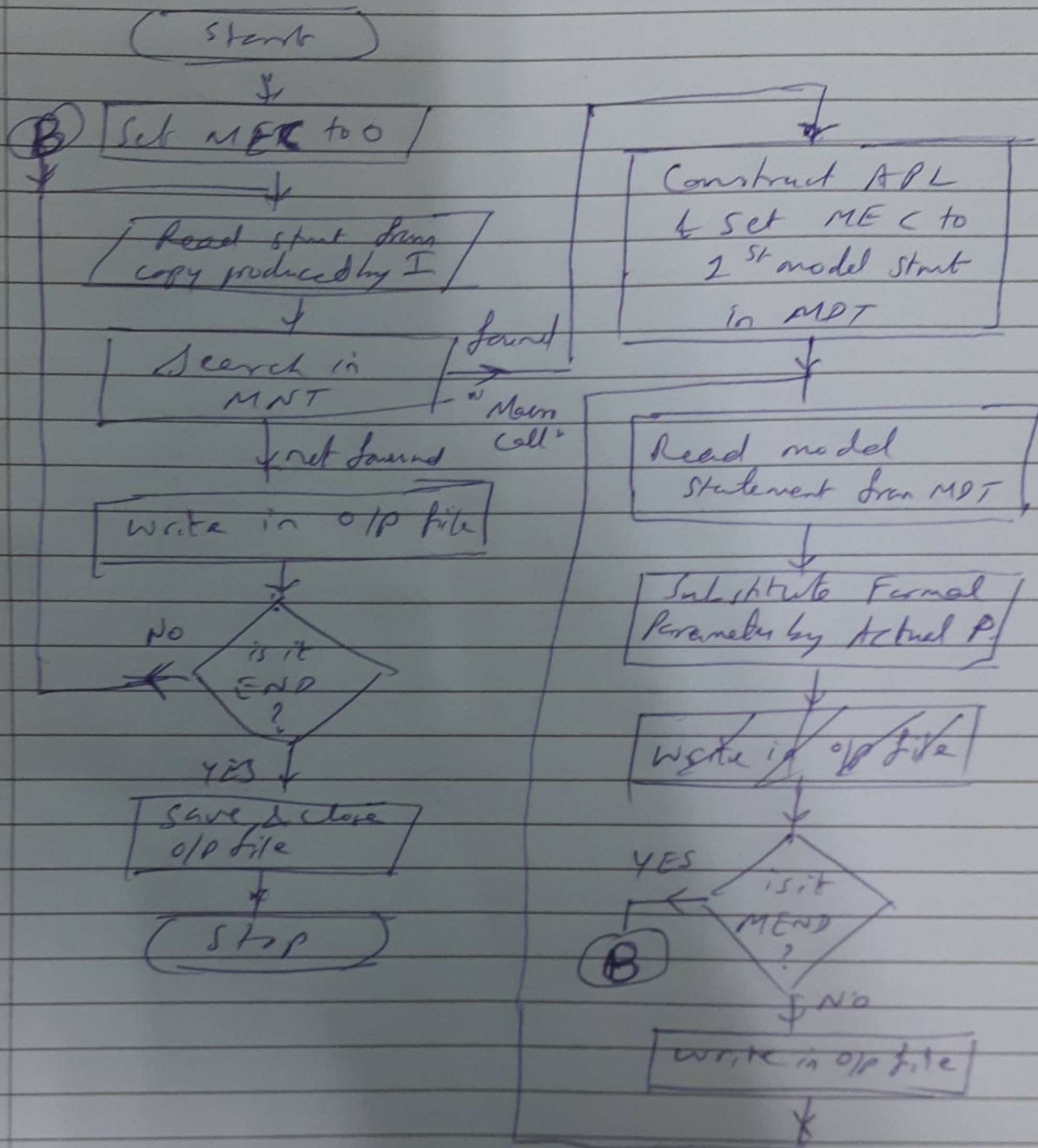
(ii) Compile & Go loading scheme requires compiler (translator) to reside in memory when user program is being loaded. This causes less memory available for user program. This problem is overcome by general purpose loader.

1 (h) Linker produces an executable file which can be loaded & executed at different memory locations everytime. Hence, Linker needs Relocatable loader which Allocates, Relocates & Loads.

2 (C1)



PASS 1 & 2-Pass Macro Processor.



Pass 2 of 2-Pass Macro Processor.

Explanation:

Pass 1 of 2-pass macro processor performs Macro Definition phase. It reads lines from i/p source files that contains Macro definitions & Macro calls and it looks for keyword "Macro".

On encountering keyword "Macro" Pass 1 constructs MNT & MDT entries wherein information of the macro is stored.

If Macro Keyword is not found in the source then it is written for Pass 2.

Hence, Pass 1 constructs MNT & ~~MDT~~ MDT with all macros defined in i/p file and also generates a copy of source for Pass 2 which contains assembly program with macro calls but no macro definitions.

Pass 2 reads statements from copy produced by Pass 1 and checks for Macro calls. If found, it calls for Macro expansion wherein it reads statements from MDT, replaces formal parameters by actual parameters and it writes them to o/p file. If macro call is not found then statements are written in o/p file till END of ALP is encountered.

This way Pass 2 will produce an o/p file which has no Macro definitions and macro calls are expanded, in the place of call, by their corresponding generated statements.

This o/p file of Pass 2 is sent to Translator (Compiler/Assembler) for translation to object file.

2 b) Macro Name Table (MNT)

[illegible]

Keyword Parameter Table (KPT)

Keyword	Default

МБТ

Macro prototype

S

MEND

APL

Expected Parameters	Actual Parameters

EVS

Stack

Value of EV

MGC	} enter data, memo
EVS	
APL	

3 (a) Functions of General Purpose Loader.

(i) Allocation: To allocate space for every module of the user program to be executed.

Ex MAIN \Rightarrow 2000
 SORT \Rightarrow 2500

(ii) Relocation: To change address sensitive statements from their assemble / translate time address to their load time address.

Ex :

MOV AX, 300 // in MAIN
to be relocated as
MOV AX, 2300 // with MAIN
 allocated from
 2000.

(iii) Linking: To provide address location of all external references (reference to symbols defined in other module)

Ex

CALL SORT
to be linked as
CALL 2500

(iv) Loading: To move bytes from secondary storage to the memory space allocated for the modules of the user program and make it start execution.

3 (b)

(a) Absolute loader	Relocatable loader	Direct Linking loader
(i) Loader only performs loading	Loader performs Allocation Relocation Loading (may perform partial linking)	Loader performs Allocation Relocation Linking Loading
(ii) User performs Allocation & hence no Relocation prog problem	User Relocation bits are used to tag address sensitive statements	Relocation entries state which statements to be relocated & w.r.t which module.
iii) Module are linked by user	Calls can be linked by loader using Transfer Vector No data linking	Calls & Data are linked by loader
iv) Occupies least space	Occupies more space than Absolute loader	Occupies large memory space
(v) Fastest: Only transfer of bytes required.	Moderate: Needs to relocate the program.	Slowest: Since it requires 2 scans of program

3(c) Dynamic loading involves loading of modules as per requirement. This is supported by OS & underlying hardware as Virtual memory. In this, when any virtual address (page or segment) referred is found to be not already loaded in memory then a fault occurs (page fault or segment fault). This fault causes call to memory management service of OS and hence the called module is loaded in memory (i.e. allocated space).

Adv:

- (1) Process can be large and still execute on system with less physical memory.
- (2) more degree of multiprocessing & concurrency.
- (3) only called modules are allocated space & hence no wastage of memory space.

Dynamic linking involves linking calls and data of one module with the address of other module which is not known at compile time. Hence, translator considers these external references as call to linker. When these statements are to be executed, the linker is called to identify the module to which it has to be linked. Such features are available from PL/I as function overloading or polymorphism.

Dynamic linking requires dynamic loading ~~and~~ but vice versa is not true.

Adv.

① There could be multiple modules with different body but same name. The correct module to be linked can be determined at run time.

② When project is very large and involves many modules ~~the~~ and reusable codes, it could be an overhead to link all modules as they may never be called. Hence, dynamic linking saves time by linking only those modules which are called.