Kevin Jarvis
705 Creekmont Dr. Round Rock, TX

https://www.linkedin.com/in/kevin-jarvis-328792a5
kjarvis2010@gmail.com
512-576-9819

SUMMARY

- Over 10 years semiconductor ATE test experience as a product / test engineer
- Over 10 years' experience as an engineer in research and development seismic electronics

SPECIAL CAPABILITIES

- Developed applications of Credence DUO/Quartet/ASL3000RF, Agilent 9400/92K, Teradyne J750, STAr., Acculogic SCORPION iCT7000 In-Circuit Tester and FLS980 Flying Prober
- Developed software and hardware solutions for custom production testers
- Specifies, designs, and integrates hardware and software into a complete system.
- Hardware bring up and software debugging.Spice simulation, schematic capture with Orcad, PADs, Dxdesigner and PCB layout for mixed signal and RF PCB
- Experience working with Ethernet TCP/IP, UDP & WiFi test tools on Credence ASL3000RF and Wireshark.
- Designed High speed SYNC board and system based on Broadcom BCM56340, BCM5461, and BCM54282 IEEE 1588v2 Precision Time Synchronization IEEE 802.3 10GBASE-KR 48port Ethernet in Backplane.
- Experience working in design team developing an Alter FPGA based 400mm board with over 5000 components using the Stratix, Cyclone, MAX CPLD and DDR3 SDRAM.
- Working knowledge of UNIX/Linux, c-shell, Perl, Python, C++, Teradyne IXGL and Labview
- Produce technical documentation for manufacturing, user and operational manuals.
- Designed high performance semiconductor test load boards for testing 24 bit delta sigma ADCs Designed high performance
- Designed 14 channel high speed IEEE JESD204B ADC based data acquisition system.
- Designed a delta sigma clocking technique to sample non-integer clock rates
- Designed electronics in fiber optic data acquisition system to capture ITU frequency grid returns from interferometer sensors.
- Characterization and failure analysis techniques, Product Reliability Testing.
- Designed high speed and performance data acquisition electronics.
- Experience designing power supplies for high power and low noise.
- Post semiconductor test experience – package and wafer.
- Experience with fiber optics DWDM, ITU, Filters, amplifiers, sensors
- Experience working with high output NKT and lasers pumps, single , multimode and PM fiber, Secore X75 Fiber Optic Fusion Splicer, Yokogawa AQ6370 Optical Spectrum Analyzer,Textronic TFP2 Fibermaster Optical Time Domain Reflectometer, Textronic Prism Data Analyzer, Textronic 1503 Time Domain Reflectometer, Hewlett-Packard 3562, 3551, 3582 and 4195 spectrum analyzers, Hewlett-Packard 4194A Impedance/Gain-Phase Analyzer

EXPERIENCE   Lithion Battery Round Rock, TX

Test Engineer   **2/22 – 11/23**

- New restart of existing business model within the USA which includes starting a new manufacturing line. Designing automatic testers for manufacturing.

Basler Electrical Taylor, TX   **10/19 – 2/22**

Sr. Test Engineer

- Sole test engineer working at Basler Taylor manufacturing location while reporting to the headquarters in Highland Illinois. Work with Labview based custom testers for functional testing of protective relays, generator controllers and engine controllers, excitation systems and Acculogic SCORPION iCT7000 In-Circuit Tester and FLS980 Flying Prober for PC board testing.

Skorpios Technologies Austin, TX   **06/19 – 08/19**

Principal Engineer

- Lead in setting up Test Engineering group in Austin TX Fab.

National Instruments Austin, TX   08/18 – 01/19
Sr. Engineer Contractor
- Engineer in system integration group responsible for designing custom interfaces to work with National Instruments equipment based on customer needs.

Criteria Labs Austin, TX   03/18 – 08/18
Sr. Test Engineer
- Develop test solutions for performing formal industry standards for product qualifications. Developed test solution to qualify a ULIS Pico1024 a thermal image sensor used in a camera being developed by NEPTEC Canada for NASA.

Rowley and Associates Austin, TX   06/16 – 03/18
Engineer Consultant hardware
- Designed high performance PCB with JESD204B based chip set for custom data acquisition system – NDA

Petroleum Geo-Services Austin, TX   07/08 to 4/16
Staff Engineer
- Designed mixed signal electronics used in data acquisition systems which includes sensitive analog circuitry with op-amps, both high speed and high bit count A/D and D/A converters with custom composite and cascade amps.
- Designed custom electronics testers used in production for all PGS electronics.
- Circuit design, Matlab, EDA Cadence HSpice and HyperLynx simulator, schematic capture with Orcad, and PCB layout for mixed signal PCB's

Skyworks Solutions   Newbury Park, CA   10/07 to 07/08
Senior Test Engineer
- Developed hardware and software applications on the Credence ASL3000RF for post semiconductor RF front-end modules with power amplifiers, low noise amplifiers with transmit/receive switches functions in a multichip module.

SigmaTel Semiconductor Austin, TX   04/02 – 11/06
Senior Product / Test Engineer
- Developed hardware and software applications Teradyne J750 for package and wafer testing of high definition audio ICs.
- Performed failure analysis, analyzed customer returns for root cause of failure and provide reports.
- Experienced going through Austin's biggest IPO up to 2003

Cirrus Logic Semiconductor Austin, TX   02/95 - 11/01
Product / Test Engineer II
- Developed hardware and software applications on the Agilent 9400 and Credence Duo/Quartet semiconductor testing for package and wafer probe.
- Customer returns – responsible for verification process and report to customer.
- Failure analysis gained experience with tools used in failure analysis lab to determine root cause of failures- EMI, X-ray, and use of a HP4155A parameter analyzer Received monetary service award for high quality support of customers, and FAEs.
- New Production – experience in releasing new products into production. Production support - solve problems in production.

Halliburton Geophysical Services Houston, TX   11/89 - 02/95 *R*
- Developed hardware and software solutions for testing designs done in the analog group.
- Gain experience with microwave, laser,wire, fiber and RF communications.
- Gained experience with various test equipment: Secore X75 Fiber Optic Fusion Splicer, Textronic TFP2 Fibermaster Optical Time Domain Reflectometer, Textronic Prism Data Analyzer, Textronic 1503 Time Domain Reflectometer, Hewlett-Packard 3562, 3551, 3582 and 4195 spectrum analyzers, Hewlett-Packard 4194A Impedance/Gain-Phase Analyzer, Textronic and Hewlett-Packard Analog and Digital scopes

EDUCATION

Bachelor of Science Engineering Technology University of Houston – Houston Texas

Presentation of Work History
https://drive.google.com/file/d/1_fOSDRzHSFyf6YePr7zHa_H12y-ExRjC/view?usp=sharing