Report Generated by Test Manager

Title: Test_Model_in_Loop Verification Report

Author: Executed for rarora by Jenkins

Date: 19-Jan-2021 18:24:20

Test Environment

Platform: PCWIN64 MATLAB: (R2020b)

Summary

Name	Outcome	(Seconds)
Results: 2021-Jan-19 18:23:15	3 🗸	34.676
□ Mode Logic TC	3 🗸	34.676
Mode_Logic_MIL	3 🗸	34.679
Mode Logic TestSequence	•	18.433
Mode Logic ExcelImport	•	5.402
Mode Logic TestSequence1	⊘	2.125

Results: 2021-Jan-19 18:23:15

Result Type: Result Set Parent: None

Start Time: 19-Jan-2021 18:23:32 End Time: 19-Jan-2021 18:24:06 Outcome: Total: 3, Passed: 3

Aggregated Coverage Results

Analyzed Model	Sim Mode	Complexity	Decision	Condition	MCDC
Mode Logic Ex	Normal	22	81%	83%	50%

Back to Report Summary

Mode_Logic_TC

Test Result Information

Result Type: Test File Result

Parent: Results: 2021-Jan-19 18:23:15

Start Time: 19-Jan-2021 18:23:32 End Time: 19-Jan-2021 18:24:06 Outcome: Total: 3, Passed: 3

Test Suite Information

Name: Mode_Logic_TC

Back to Report Summary

Mode_Logic_MIL

Test Result Information

Result Type: Test Suite Result Parent: Mode_Logic_TC

Start Time: 19-Jan-2021 18:23:32 End Time: 19-Jan-2021 18:24:06 Outcome: Total: 3, Passed: 3

Test Suite Information

Name: Mode_Logic_MIL

Back to Report Summary

Mode_Logic_TestSequence

Test Result Information

Result Type: Test Case Result
Parent: Mode Logic MIL
Start Time: 19-Jan-2021 18:23:32
End Time: 19-Jan-2021 18:23:50

Outcome: Passed

Test Case Information

Name: Mode_Logic_TestSequence

Type: Simulation Test

Logical and Temporal Assessments

Name	Assessment
Low Charge Engine Active	At any point in time, if ((GeneratorEnable == True) & (EngineEnable == True)) becomes tru e then, with no delay, (MotorEnable == False) must be true
	At any point in time, if (Brake >= FivePercent) becomes true then, with no delay, (MotorEna ble == True) must be true

Simulation

System Under Test Information

Model: Mode_Logic_Ex

Harness: Mode_Logic_Ex_Harness1

Harness Owner: Mode_Logic_Ex

Release: Current Simulation Mode: normal

Override SIL or PIL

Mode:

Configuration Set: ModelReferencing

1

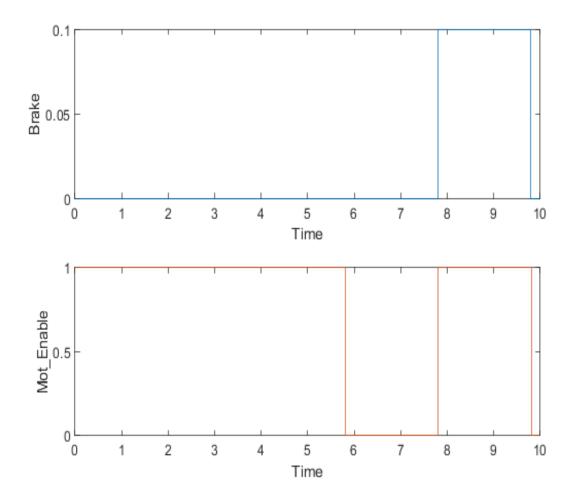
Start Time: 0
Stop Time: 10

Checksum: 3775655585 1064904917 2300883564 1937877756

Simulation Output

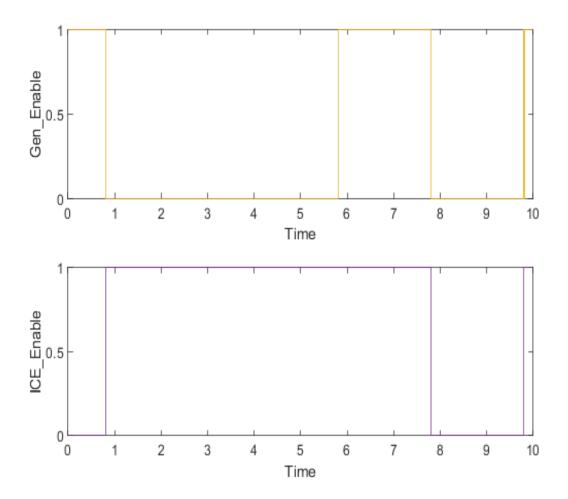
Name	Data Type	Data Type Units Sample Time		Interp	Sync	Link to Plo t
Brake	double		0.001	zoh	union	<u>Link</u>
Mot_Enable	double	i +	0.001	zoh	union	<u>Link</u>
Gen_Enable	double	i +	0.001	zoh	union	<u>Link</u>
ICE_Enable	double	i 	0.001	zoh	union	<u>Link</u>
Brake	double	i 	0.001	zoh	union	<u>Link</u>
Charge	double	 	0.001	zoh	union	<u>Link</u>
EngSpeed	double	 	0.001	zoh	union	<u>Link</u>
Speed	double	km/hr	0.001	zoh	union	<u>Link</u>
Mot_Enable	double	i i	0.001	zoh	union	<u>Link</u>
Gen_Enable	double	i i	0.001	zoh	union	<u>Link</u>
ICE_Enable	double	 	0.001	zoh	union	<u>Link</u>

Name	Data Type	Units	Sample Time	Interp	Sync
Brake	double		0.001	zoh	union
Mot_Enable	double		0.001	zoh	union



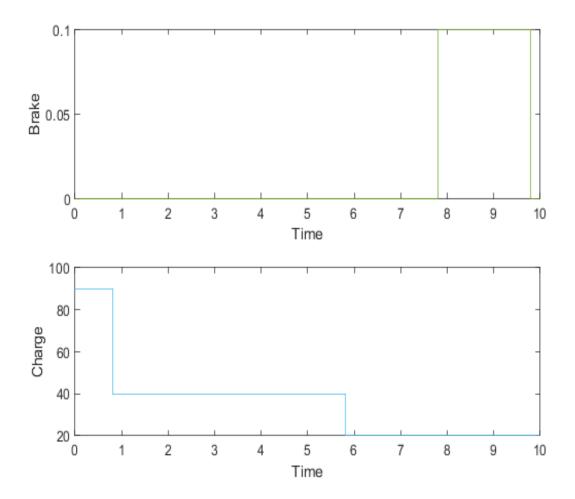
Back to Report SummaryBack to Signal Summary

Name	Data Type	Units	Sample Time	Interp	Sync
Gen_Enable	double		0.001	zoh	union
ICE_Enable	double		0.001	zoh	union



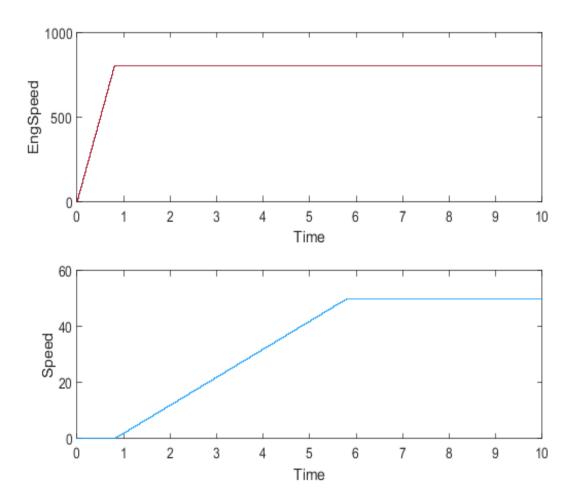
Back to Report SummaryBack to Signal Summary

Name	Data Type	Units	Sample Time	Interp	Sync
Brake	double		0.001	zoh	union
Charge	double		0.001	zoh	union



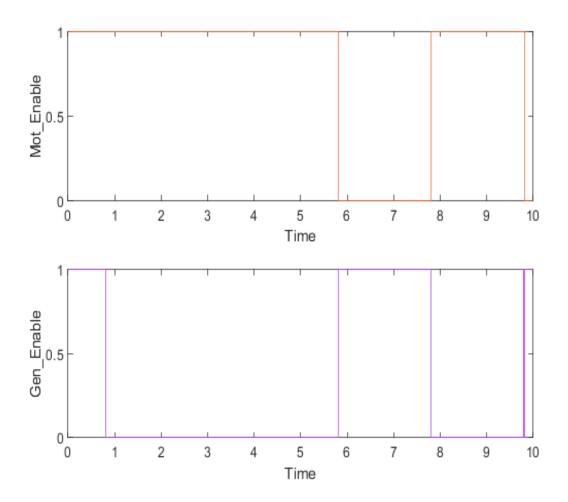
Back to Report SummaryBack to Signal Summary

Name	Data Type	Units	Sample Time	Interp	Sync
EngSpeed	double		0.001	zoh	union
Speed	double	km/hr	0.001	zoh	union



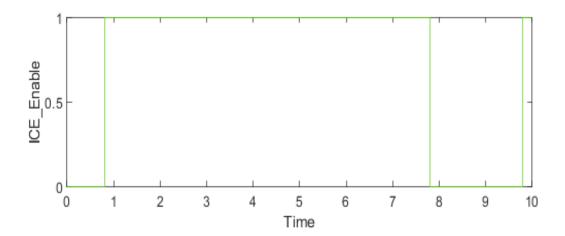
Back to Report SummaryBack to Signal Summary

Name	Data Type	Units	Sample Time	Interp	Sync
Mot_Enable	double		0.001	zoh	union
Gen_Enable	double		0.001	zoh	union



Back to Report SummaryBack to Signal Summary

Name	Data Type	Units	Sample Time	Interp	Sync	
ICE_Enable	double		0.001	zoh	union	



Back to Report SummaryBack to Signal Summary

Simulation Logs: 'ICE_Enable is defined, but is never used in the Test Sequence block. Delete this object.

<u>'ICE_Enable'</u> is defined, but is never used in the Test Sequence block. <u>Delete this object.</u>

Back to Report Summary

Mode_Logic_ExcelImport

Test Result Information

Result Type: Test Case Result
Parent: Mode Logic MIL
Start Time: 19-Jan-2021 18:23:57
End Time: 19-Jan-2021 18:24:02

Outcome: Passed

Test Case Information

Name: Mode_Logic_ExcelImport

Type: Baseline Test
Baseline Name: baseline_output

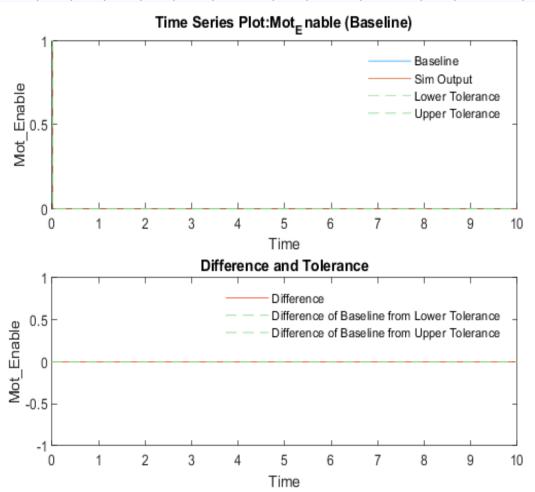
Baseline File: C:\Jenkins\workspace\Test_Model_in_Loop\testm

ngt\testcases\ModelLogic_TestScenario.xlsx

Baseline Comparison

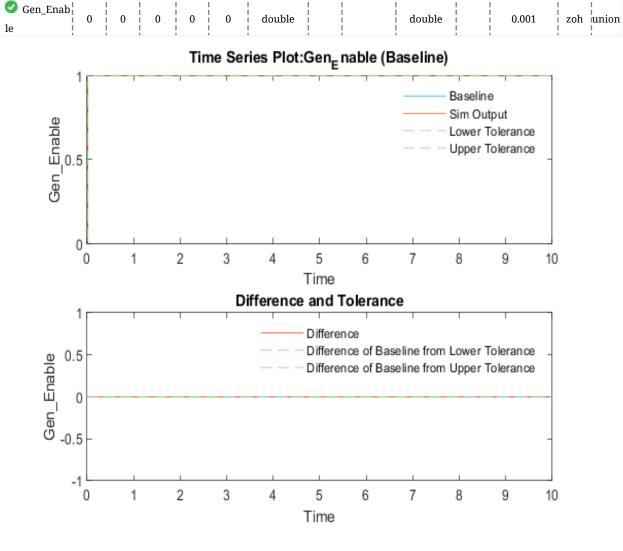
Name	Abs	Rel T	Lead	Lag	Max	Data Type 1		Sample	Data Type 2		Sample Tim	Interp		Link to Plo
Name	Tol	ol	Tol	Tol	Diff	Data Type I	1	Time 1		2	e 2	miter p	Sync	t
Mot_Enab	0	0	0	0	0	double	 		double	 	0.001	zoh	union	<u>Link</u>
Gen_Enab	0	0	0	0	0	double	 	 	double	 	0.001	zoh	union	<u>Link</u>
ICE_Enable	0	0	0	0	0	double	 	 	double	 	0.001	zoh	union	<u>Link</u>

Name	Abs T	Rel To	Lead T	Lag T ol	Max Di ff	Data Type 1	Sample Ti me 1	Data Type 2	Sample Time 2	Interp	Sync
Mot_Enab le	0	0	0	0	0	double		double	0.001	zoh lu	ınion



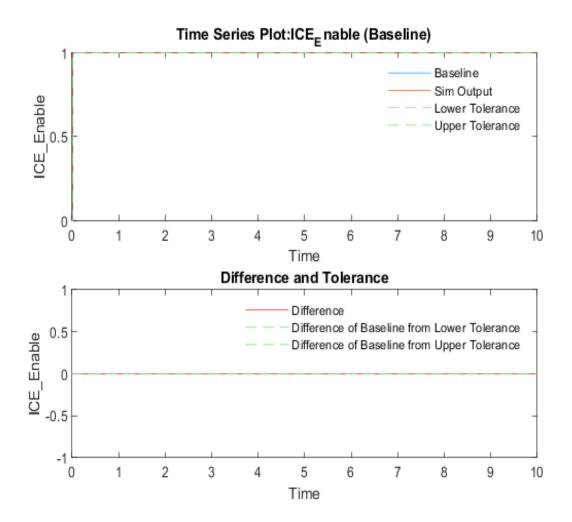
Back to Report SummaryBack to Criteria Results

	Abs T Rel To	Lead T	Lag T	Max Di			Sample Ti			Sample Time		
Name	ol l	ol	ol	ff	Data Type 1	1	me 1	Data Type 2	2	2	Interp S	Sync



Back to Report SummaryBack to Criteria Results

ľ	Name	Abs T ol	Rel To l	Lead T ol	Lag T ol	Max Di ff	Data Type 1	Sample Ti me 1	Data Type 2	Sample Time 2	Interp	Sync
② :	ICE_Enabl	0	0	0	0	0	double	 	double	0.001	zoh	union



Back to Report SummaryBack to Criteria Results

Simulation

System Under Test Information

Model: Mode_Logic_Ex

Harness: Mode_Logic_Ex_Harness2

Harness Owner: Mode_Logic_Ex

Release: Current Simulation Mode: normal

Override SIL or PIL

Mode:

Configuration Set: ModelReferencing

External Input Name: ModelLogic_TestScenario.xlsx

1

External Input File: C:\Jenkins\workspace\Test_Model_in_Loop\testm

ngt\testcases\ModelLogic_TestScenario.xlsx

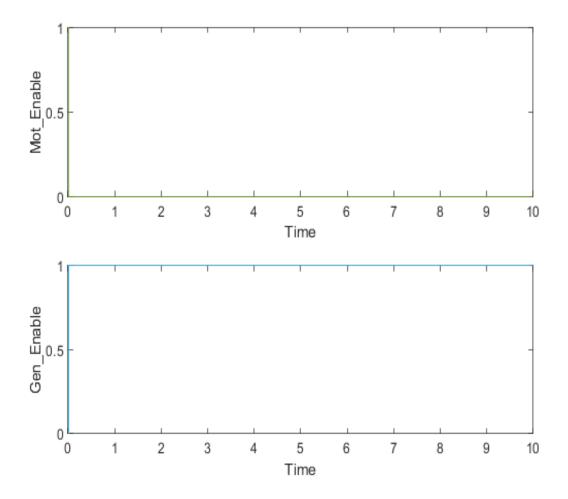
Start Time: 0 Stop Time: 10

Checksum: 2594654103 4143187215 2781615718 2486298153

Simulation Output

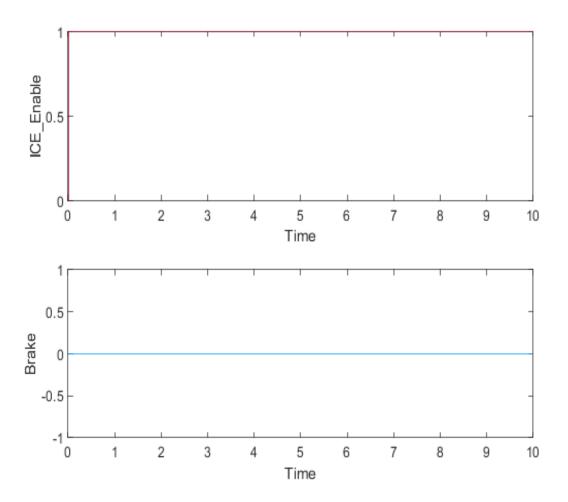
Name	Data Type	Units	Sample Time	Interp	Sync	Link to Plo t
Mot_Enable	double	i ∔	0.001	zoh	union	Link
Gen_Enable	double	i +	0.001	zoh	union	Link
ICE_Enable	double	i +	0.001	zoh	union	Link
Brake	double	i 	0.001	zoh	union	<u>Link</u>
Charge	double	 	0.001	zoh	union	<u>Link</u>
EngSpeed	double	 	0.001	zoh	union	<u>Link</u>
Speed	double	km/hr	0.001	zoh	union	<u>Link</u>

Name	Data Type	Units	Sample Time	Interp	Sync
Mot_Enable	double		0.001	zoh	union
Gen_Enable	double		0.001	zoh	union



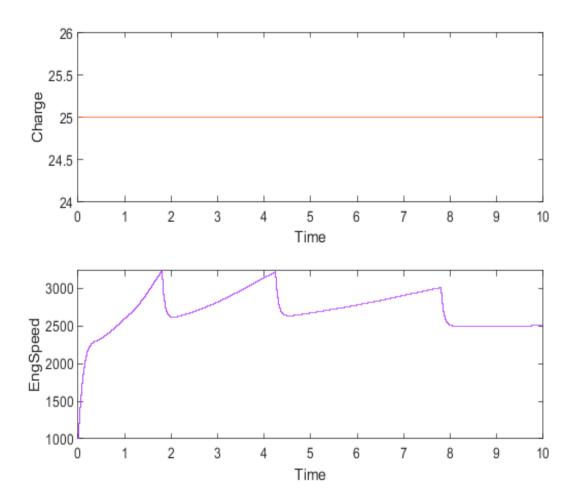
Back to Report SummaryBack to Signal Summary

Name	Data Type	Units	Sample Time	Interp	Sync
ICE_Enable	double		0.001	zoh	union
Brake	double		0.001	zoh	union



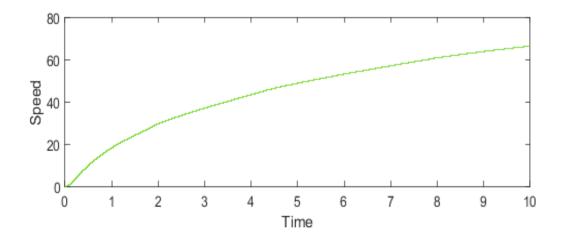
Back to Report SummaryBack to Signal Summary

Name	Data Type	Units	Sample Time	Interp	Sync
Charge	double		0.001	zoh	union
EngSpeed	double		0.001	zoh	union



Back to Report SummaryBack to Signal Summary

Name	Data Type	Units	Sample Time	Interp	Sync
Speed	double	km/hr	0.001	zoh	union



Back to Report SummaryBack to Signal Summary

Mode_Logic_TestSequence1

Test Result Information

Result Type: Test Case Result
Parent: Mode Logic MIL
Start Time: 19-Jan-2021 18:24:04

End Time: 19-Jan-2021 18:24:06

Outcome: Passed

Description:

Just Added Extra Test Case to Trigger new build

Test Case Information

Name: Mode_Logic_TestSequence1

Type: Simulation Test

Logical and Temporal Assessments

Name	Assessment
Low Charge Engine Active	At any point in time, if ((GeneratorEnable == True) & (EngineEnable == True)) becomes tru e then, with no delay, (MotorEnable == False) must be true
	At any point in time, if (Brake >= FivePercent) becomes true then, with no delay, (MotorEna ble == True) must be true

Simulation

System Under Test Information

Model: Mode_Logic_Ex

Harness: Mode_Logic_Ex_Harness1

Harness Owner: Mode_Logic_Ex

Release: Current Simulation Mode: normal

Override SIL or PIL 1

Mode:

Configuration Set: ModelReferencing

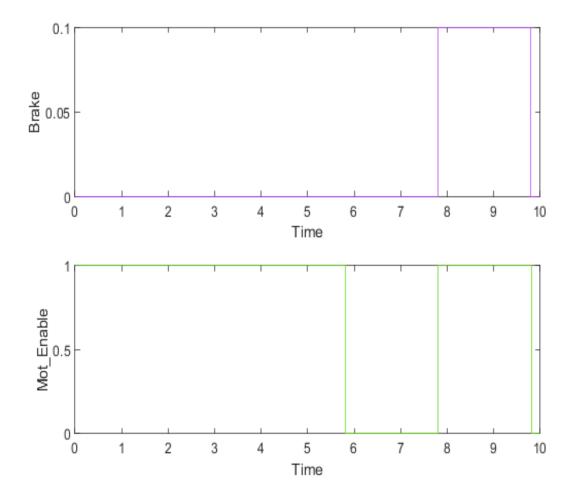
Start Time: 0 Stop Time: 10

Checksum: 3775655585 1064904917 2300883564 1937877756

Simulation Output

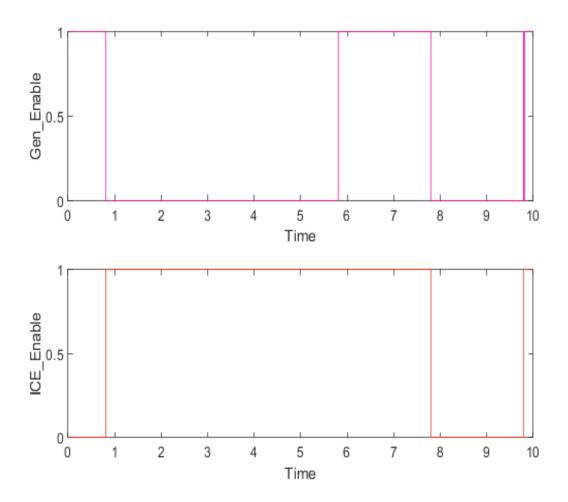
Name	Data Type	Units	Sample Time	Interp	Sync	Link to Plo t
Brake	double		0.001	zoh	union	<u>Link</u>
Mot_Enable	double	<u>i</u>	0.001	zoh	union	<u>Link</u>
Gen_Enable	double	<u>i</u>	0.001	zoh	union	<u>Link</u>
ICE_Enable	double		0.001	zoh	union	<u>Link</u>
Brake	double	i	0.001	zoh	union	<u>Link</u>
Charge	double		0.001	zoh	union	<u>Link</u>
EngSpeed	double		0.001	zoh	union	<u>Link</u>
Speed	double	km/hr	0.001	zoh	union	<u>Link</u>
Mot_Enable	double		0.001	zoh	union	<u>Link</u>
Gen_Enable	double		0.001	zoh	union	<u>Link</u>
ICE_Enable	double		0.001	zoh	union	<u>Link</u>

Name	Data Type	Units	Sample Time	Interp	Sync
Brake	double		0.001	zoh	union
Mot_Enable	double		0.001	zoh	union



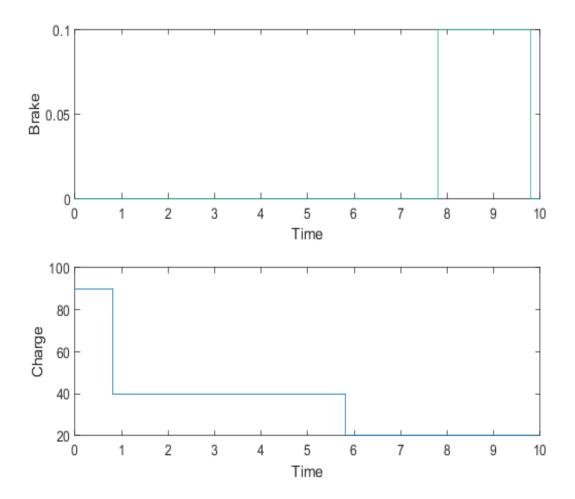
Back to Report SummaryBack to Signal Summary

Name	Data Type	Units	Sample Time	Interp	Sync
Gen_Enable	double		0.001	zoh	union
ICE_Enable	double		0.001	zoh	union



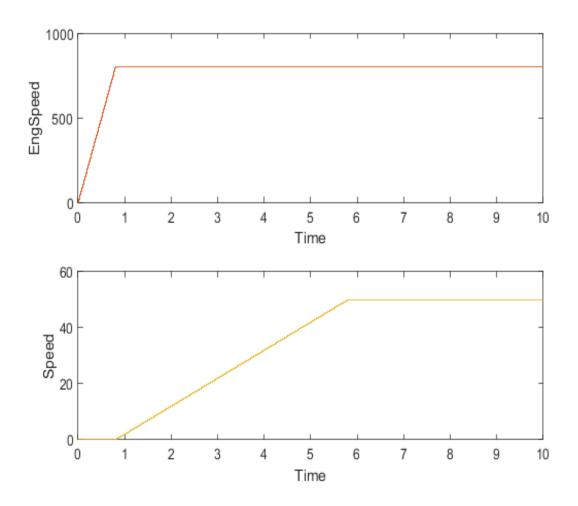
Back to Report SummaryBack to Signal Summary

Name	Data Type	Units	Sample Time	Interp	Sync
Brake	double		0.001	zoh	union
Charge	double		0.001	zoh	union



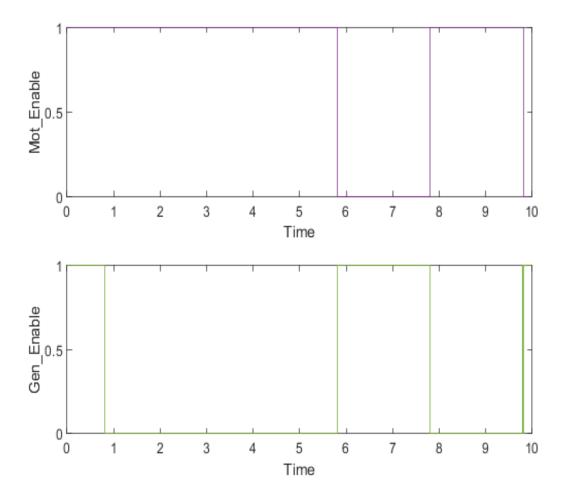
Back to Report SummaryBack to Signal Summary

Name	Data Type	Units	Sample Time	Interp	Sync
EngSpeed	double		0.001	zoh	union
Speed	double	km/hr	0.001	zoh	union



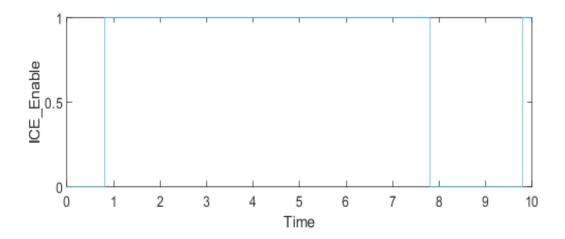
Back to Report SummaryBack to Signal Summary

Name	Data Type	Units	Sample Time	Interp	Sync
Mot_Enable	double		0.001	zoh	union
Gen_Enable	double		0.001	zoh	union



Back to Report SummaryBack to Signal Summary

Name	Data Type	Units	Sample Time	Interp	Sync
ICE_Enable	double		0.001	zoh	union



Back to Report SummaryBack to Signal Summary

Simulation Logs: 'ICE_Enable is defined, but is never used in the Test Sequence block. Delete this object.

Back to Report Summary