

# Goujin Chen

Department of Computer Science  
The Chinese University of Hong Kong  
☎ +86 18986888887 · ✉ cgjhaha@qq.com  
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RESEARCH INTERESTS	I am interested in Machine Learning, EDA, VLSI design. My current focuses include: <ul style="list-style-type: none"><li>• Machine Learning in VLSI Design.</li><li>• Reinforcement learning, computer vision.</li></ul>	
EDUCATION	<b>The Chinese University of Hong Kong</b> <b>M.Sc. in Computer Science</b> <ul style="list-style-type: none"><li>• Advisor: Prof. Bei Yu</li></ul>	Hong Kong Sep 2019 – Nov 2020
	<b>Huazhong University of Science and Technology</b> <b>Bachelor of Computer Science</b>	Wuhan, China Sep 2015 – Jun 2019
RELEVANT WORKING EXPERIENCE	<b>Smartmore Co.Ltd.</b> <b>Research Intern</b>	SHENZHEN, China Nov 2020 – Jan 2021
	<b>Tencent Technology Co.Ltd.</b> <b>Research Intern</b>	SHENZHEN, China May 2018 – Nov 2018
AWARDS	<b>Scholarship</b> <ul style="list-style-type: none"><li>• Distinguished Academic Performance Scholarship, CUHK.</li><li>• National Encouragement Scholarship, HUST, Ministry of Education, PRC</li><li>• First Class Scholarship, HUST, the highest scholarship in HUST.</li></ul>	May 2020 Nov 2016 2018, 2019
	<b>Internship</b> <ul style="list-style-type: none"><li>• First Prize, Tencent SNG Hack Week.</li><li>• Excellent Intern, Tencent.</li></ul>	Jun 2019 Sep 2019
PROJECTS	<b>DAMO</b> : Towards High Accuracy DL-Based OPC With Deep Lithography Simulator. This paper present a novel method for Deep Learning based OPC which results surpass the famous OPC tool Mentor Calibre. The manuscript was accepted by ICCAD2020. <b>CUDA-OPC</b> : This is a CUDA acceleration project that aims to improve the ILT computation efficiency, it speeds up the lithography process nearly 40 times than before.	
SKILLS	<b>Programming</b> C/C++, Python, Ruby, Matlab, L <sup>A</sup> T <sub>E</sub> X, Bash, Javascript, Rust, Java <b>Machine Learning</b> Skilled in Pytorch, Tensorflow, and CUDA programming. <b>Tools</b> Vim, Git, macOS, Linux	
PUBLICATIONS	<ol style="list-style-type: none"><li>1. <b>C. Guojin</b>, C. Wanli, M. Yuzhe, Y. Haoyu, and Y. Bei, "DAMO: Deep agile mask optimization for full chip scale," in <i>IEEE/ACM International Conference on Computer-Aided Design (ICCAD '20)</i>, Nov. 2020. [Online]. Available: <a href="https://arxiv.org/abs/2008.00806">https://arxiv.org/abs/2008.00806</a>.</li><li>2. Y. Ziyang, <b>C. Guojin</b>, M. Yuzhe, and Y. Bei, "A gpu-enabled level set method for mask optimization," in <i>IEEE/ACM Proceedings Design, Automation and Test in Europe, (DATE '21)</i>, Nov. 2020.</li></ol>	
TALKS	<ol style="list-style-type: none"><li>1. CUDA based Convolution and FFT on OPC. <i>CUDA Group Presentation.</i>, CUHK. Mar 2020</li><li>2. DLS-DMO: High Accuracy DL-Based OPC With DLS. <i>CUDA Group Presentation.</i>, CUHK. May 2020</li></ol>	