

Goujin Chen

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RESEARCH INTERESTS	I am interested in Machine Learning, EDA, VLSI design. My current focuses include: <ul style="list-style-type: none">• Machine Learning in VLSI Design.• Reinforcement learning, computer vision.	
EDUCATION	The Chinese University of Hong Kong M.Sc. in Computer Science <ul style="list-style-type: none">• Advisor: Prof. Bei Yu	Hong Kong Nov 2019 – Feb 2020
	Huazhong University of Science and Technology Bachelor of Computer Science	Wuhan, China Sep 2015 – Jun 2019
AWARDS	Scholarship <ul style="list-style-type: none">• Distinguished Academic Performance Scholarship, CUHK.• Entrance Scholarship, CUHK.• National Encouragement Scholarship, HUST, Ministry of Education, PRC• First Class Scholarship, HUST, the highest scholarship in HUST.	May 2020 Nov 2019 Nov 2016 2018, 2019
	Internship <ul style="list-style-type: none">• First Prize, Tencent SNG Hack Week.• Excellent Intern, Tencent.	Jun 2019 Sep 2019
PROJECTS	DAMO : Towards High Accuracy DL-Based OPC With Deep Lithography Simulator. This paper present a novel method for Deep Learning based OPC which results surpass the famous OPC tool Mentor Calibre. The manuscript was submitted to ICCAD2020. CUDA-OPC : This is a CUDA acceleration project that aims to improve the ILT computation efficiency, it speeds up the lithography process nearly 40 times than before.	
RELEVANT WORKING EXPERIENCE	Smartmore Co.Ltd. Research Intern	SHENZHEN, China Nov 2020 – Jan 2021
	Tencent Technology Co.Ltd. Research Intern <ul style="list-style-type: none">• Responsible for web development, including F.E. Development and Web Server Development.• AI server (Developed): A web server using Node.js to build services for an artificial intelligence online video cutting service. I am the designer and the main developer for it.	SHENZHEN, China May 2018 – Nov 2018
SKILLS	Programming C/C++, Rust, Java, Python, Ruby, Matlab, \LaTeX , Bash, Javascript Machine Learning Skilled in Pytorch, Tensorflow, and CUDA programming. Tools Vim, Git, macOS, Linux	
PUBLICATIONS	1. C. Guojin , C. Wanli, M. Yuzhe, Y. Haoyu, and Y. Bei, "DAMO: Deep agile mask optimization for full chip scale," in <i>IEEE/ACM International Conference on Computer-Aided Design (ICCAD '20)</i> , Nov. 2020.	
TALKS	1. CUDA based Convolution and FFT on OPC. <i>CUDA Group Presentation.</i> , CUHK. Mar 2020 2. DAMO: Deep Agile Mask Optimization on Full Chip Scale. <i>CUDA Group Presentation.</i> , CUHK. May 2020	