

Goujin Chen

Department of Computer Science

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RESEARCH INTERESTS	I am interested in Machine Learning, EDA, VLSI design. My current focuses include: <ul style="list-style-type: none">Machine Learning in VLSI Design.Reinforcement learning, computer vision.	
EDUCATION	The Chinese University of Hong Kong M.Sc. in Computer Science <ul style="list-style-type: none">Advisor: Prof. Bei Yu	Hong Kong Sep 2019 – Nov 2020
	Huazhong University of Science and Technology Bachelor of Computer Science	Wuhan, China Sep 2015 – Jun 2019
RELEVANT WORKING EXPERIENCE	Smartmore Co.Ltd. Research Intern	SHENZHEN, China Nov 2020 – Jan 2021
	Tencent Technology Co.Ltd. Research Intern	SHENZHEN, China May 2018 – Nov 2018
AWARDS	Scholarship <ul style="list-style-type: none">Distinguished Academic Performance Scholarship, CUHK.National Encouragement Scholarship, HUST, Ministry of Education, PRCFirst Class Scholarship, HUST, the highest scholarship in HUST.	May 2020 Nov 2016 2018, 2019
	Internship <ul style="list-style-type: none">First Prize, Tencent SNG Hack Week.Excellent Intern, Tencent.	Jun 2019 Sep 2019
PROJECTS	DAMO : Towards High Accuracy DL-Based OPC With Deep Lithography Simulator. This paper present a novel method for Deep Learning based OPC which results surpass the famous OPC tool Mentor Calibre. The manuscript was accepted by ICCAD2020. CUDA-OPC : This is a CUDA acceleration project that aims to improve the ILT computation efficiency, it speeds up the lithography process nearly 40 times than before.	
SKILLS	Programming C/C++, Python, Ruby, Matlab, \LaTeX , Bash, Javascript, Rust, Java Machine Learning Skilled in Pytorch, Tensorflow, and CUDA programming. Tools Vim, Git, macOS, Linux	
PUBLICATIONS	<ol style="list-style-type: none">C. Guojin, C. Wanli, M. Yuzhe, Y. Haoyu, and Y. Bei, "DAMO: Deep agile mask optimization for full chip scale," in <i>IEEE/ACM International Conference on Computer-Aided Design (ICCAD '20)</i>, Nov. 2020. [Online]. Available: https://arxiv.org/abs/2008.00806.Y. Ziyang, C. Guojin, M. Yuzhe, and Y. Bei, "A gpu-enabled level set method for mask optimization," in <i>IEEE/ACM Proceedings Design, Automation and Test in Europe, (DATE '21)</i>, Nov. 2020.	
TALKS	<ol style="list-style-type: none">CUDA based Convolution and FFT on OPC. <i>CUDA Group Presentation.</i>, CUHK. Mar 2020DLS-DMO: High Accuracy DL-Based OPC With DLS. <i>CUDA Group Presentation.</i>, CUHK. May 2020	