\$40	( read_n / write )	ID \$08	MSX2+ FS-A1 Series	Z80B 5.37MHz			SMART COMMANDS TABLE
Ş40	(Teau_II) write)	ID \$D4 BIT 0	1chipMSX / Zemmix Neo	OCM-PLD v2.4 or later	\$00	( 000 )	Null Command (reserved)
\$41	( read_n / write )	BIT 1 BIT 2			\$01 \$02	(001)	Set Turbo Pana Redirection OFF (default) Set Turbo Pana Redirection ON
		BIT 3 BIT 4	Smart Command ID	CPL or \$FF (Null)	\$03 \$04	(003)	Set Standard Speed 3.58MHz Set Custom Speed 4.10MHz
		BIT 5 BIT 6			\$05 \$06	(005)	Set Custom Speed 4.48MHz Set Custom Speed 4.90MHz
		BIT 7 BIT 0	CPU Clock	Virtual DIP-SW1	\$07 \$08	(007)	Set Custom Speed 5.39MHz Set Custom Speed 6.10MHz
	( read / write_n )	BIT 1 BIT 2	Video Output (MSB) Video Output (LSB)	Virtual DIP-SW2 Virtual DIP-SW3	\$09 \$0A	(009) (010)	Set Custom Speed 6.96MHz Set Custom Speed 8.06MHz (aka "Turbo 10MHz") (default)
\$42		BIT 3 BIT 4	Cartridge Slot-1 Cartridge Slot-2 (MSB)	Virtual DIP-SW4 Virtual DIP-SW5	\$0B \$0C	(011)	Set Turbo MegaSD OFF Set Turbo MegaSD ON (default)
		BIT 5 BIT 6	Cartridge Slot-2 (LSB) Current Mapper Size	Virtual DIP-SW6 Virtual DIP-SW7	\$0D \$0E	(013) (014)	Set External Slot-1 + External Slot-2 Set Internal SCC-I Slot-1 + External Slot-2
		BIT 7 BIT 0	Current MegaSD Mode	Virtual DIP-SW8 CPU Clock	\$0F \$10	(015)	Set External Slot-1 + Internal SCC-I Slot-2 Set Internal SCC-I Slot-1 + Internal SCC-I Slot-2
		BIT 1 BIT 2		Video Output Audio Mixer & CMT	\$11 \$12	(017)	Set External Slot-1 + Internal ASCII-8K Slot-2 Set Internal SCC-I Slot-1 + Internal ASCII-8K Slot-2
\$43	( read / write_n )	BIT 3 BIT 4	Lock Mak for Toggles	Cartridge Slot-1 Cartridge Slot-2	\$13 \$14	(019)	Set External Slot-1 + Internal ASCII-16K Slot-2 Set Internal SCC-I Slot-1 + Internal ASCII-16K Slot-2
		BIT 5 BIT 6		Hard Reset Key Internal Mapper	\$15 \$16	( 021 ) ( 022 )	Set Japanese Keyboard Layout Set Non-Japanese Keyboard Layout
		BIT 7		Internal MegaSD Led 1 Status	\$17 \$18	(023)	Set Display Mode 15KHz Composite/S-Video Set Display Mode 15KHz RGB w/ Audio Out (requires a VGA to SCART cable)
\$44	( read / write_n )	BIT 1 BIT 2		Led 2 Status Led 3 Status	\$19 \$1A	(025)	Set Display Mode 31Khz VGA for LED TV or LED Display (50Hz+60Hz) Set Display Mode 31Khz VGA+ for CRT Monitor (legacy output) (50Hz+60Hz)
		BIT 3 BIT 4	Lights I/O	Led 4 Status Led 5 Status	\$1B \$1C	( 027 ) ( 028 )	Set VDP Speed Normal Mode (default) Set VDP Speed Fast Mode (V9958 only)
		BIT 5 BIT 6		Led 6 Status Led 7 Status	\$1D \$1E	( 029 ) ( 030 )	Reserve MegaSD OFF (warm reset to set) Reserve MegaSD ON (warm reset to set)
		BIT 7 BIT 0		Led 8 Status BIT 0 (LSB)	\$1F \$20	(031)	Set MegaSD Blink OFF Set MegaSD Blink ON (default)
	( read / write_n )	BIT 1 BIT 2	PSG Volume Level	BIT 1 BIT 2 (MSB)	\$21 \$22	(033)	Set Lights Mode OFF w/ Auto LEDs Control (default) Set Lights Mode ON + Red Led OFF
\$45	( read only )	BIT 3 BIT 4	PSG Mute	Status BIT 0 (LSB)	\$23 \$24	( 035 ) ( 036 )	Set Lights Mode ON + Red Led ON Internal Audio Preset #1 "Mute Sound"
	( read / write_n )	BIT 5 BIT 6	Master Volume Level	BIT 1 BIT 2 (MSB)	\$25 \$26	( 037 ) ( 038 )	Internal Audio Preset #2 "Middle Sound" Internal Audio Preset #3 "High Sound" (default)
	( read only )	BIT 7 BIT 0	Master Mute	Status BIT 0 (LSB)	\$27 \$28	(039)	Set CMT OFF (default) (disabled w/ MSX turbo-R BIOS)  Set CMT ON (requires a cassette recorder) (disabled w/ MSX turbo-R BIOS)
\$46	( read / write_n )	BIT 1 BIT 2	OPLL Volume Level	BIT 1 BIT 2 (MSB)	\$29 \$2A	( 041 ) ( 042 )	Lock Turbo Toggles Unlock Turbo Toggles
	( read only )	BIT 3 BIT 4	OPLL Mute	Status BIT 0 (LSB)	\$2B \$2C	( 043 ) ( 044 )	Lock Display Toggles Unlock Display Toggles
	( read / write_n )	BIT 5 BIT 6	SCC-I Volume Level	BIT 1 BIT 2 (MSB)	\$2D \$2E	( 045 ) ( 046 )	Lock Audio Mixer & CMT Toggles Unlock Audio Mixer & CMT Toggles
	( read only )	BIT 7 BIT 0	SCC-I Mute	Status BIT 0 (LSB)	\$2F \$30	( 047 )	Lock Slot-1 Toggles Unlock Slot-1 Toggles
\$47	( read only )	BIT 1 BIT 2	CPU Custom Speed Level	BIT 1 BIT 2 (MSB)	\$31 \$32	( 049 ) ( 050 )	Lock Slot-2 Toggles Unlock Slot-2 Toggles
		BIT 3 BIT 4	Turbo MegaSD (tMSD) Turbo Pana Redirection (tPR)	Status Status	\$33 \$34	( 051 ) ( 052 )	Lock Slot-1 & Slot-2 Toggles Unlock Slot-1 & Slot-2 Toggles
		BIT 5 BIT 6	VDP Speed Mode Next Mapper Size	0=Normal, 1=Fast 0=2048kB, 1=4096kB	\$35 \$36	( 053 ) ( 054 )	Lock Hard Reset Key Unlock Hard Reset Key
\$48	( read only )	BIT 7	Next MegaSD Mode  Turbo Pana	Status Status	\$37 \$38	( 055 )	Lock Mapper Toggle Unlock Mapper Toggle
		BIT 1 BIT 2	Current Keyboard Layout CMT (Cassette Magnetic Tape)	0=JP, 1=Non-JP Status	\$39 \$3A	( 057 ) ( 058 )	Lock MegaSD Toggle Unlock MegaSD Toggle
		BIT 3 BIT 4	Lights Mode Red Mode (Led 0)	0=Auto, 1=ON Status	\$3B \$3C	( 059 ) ( 060 )	Lock All Toggles Unlock All Toggles (default)
		BIT 5 BIT 6	Last Reset Flag Reset Required Flag	0=Cold, 1=Warm Status	\$3D \$3E	( 061 ) ( 062 )	Set Pseudo-Stereo OFF (default) Set Pseudo-Stereo ON (requires an external sound cartridge)
\$49		BIT 7	MegaSD Blink Pseudo Stereo	Status Status	\$3F \$40	( 063 )	Sync External Bus Clock to CPU Speed (default) Set External Bus Clock 3.58MHz
	( read only )	BIT 1 BIT 2	External Clock Mode	0=Sync to CPU, 1=3.58Mhz BIT 0 (LSB)	\$41 \$42	( <mark>065 )</mark> ( 066 )	Set Turbo Pana 5.37MHz Set Right Inverse Audio OFF (default)
		BIT 3 BIT 4	Machine Type ID (0=1chipMSX, 1=Zemmix Neo,)	BIT 1 BIT 2	\$43 \$44	( 067 ) ( 068 )	Set Right Inverse Audio ON Internal Audio Preset #4 "Emphasis PSG Sound"
		BIT 5 BIT 6	NTSC/PAL Type	BIT 3 (MSB) 0=Forced, 1=Auto	\$45 \$46	(069)	Internal Audio Preset #5 "Emphasis SCC-I Sound" Internal Audio Preset #6 "Emphasis OPLI Sound"
		BIT 7	Forced Video Mode Right Inverse Audio	0=60Hz (NTSC), 1=50Hz (PAL) Status	\$47 \$48	(071)	Vertical Offset 16 (useful for Ark-A-Noah)  Vertical Offset 17
	( read only )	BIT 1 BIT 2	Pixel Ratio 1:1 for LED Display	BIT 0 (LSB) BIT 1	\$49 \$4A	( 073 ) ( 074 )	Vertical Offset 18 Vertical Offset 19 (default)
\$4A		BIT 3 BIT 4	Centering YJK Modes/R25 Mask Assignment of Legacy Output	BIT 2 (MSB) Status 0=To VGA, 1=To VGA+	\$4B \$4C	( 075 ) ( 076 )	Vertical Offset 20 Vertical Offset 21 Vertical Offset 22
		BIT 5 BIT 6 BIT 7	Internal Slot-1 Linear Internal Slot-2 Linear	Status Status	\$4D \$4E \$4F	( 077 ) ( 078 ) ( 079 )	Vertical Offset 22  Vertical Offset 24  Vertical Offset 24 (useful for Space Manbow)
	( read only )	BIT 0 BIT 1	Internal Slot-2 Linear	Status	\$7F \$80	(127)	Pixel Ratio 1:1 for LED Display (default is 0) (range 0-7) (60Hz only)  Null Command (useful for programming)
		BIT 2 BIT 3	Free	SFF (Null)	\$81 \$82	(128) (129) (130)	Assign Legacy Output to VGA Assign Legacy Output to VGA+ (default)
\$4B		BIT 4 BIT 5			\$83 \$84	(131)	Set Internal Slot-1 Linear OFF (default) Set Internal Slot-1 Linear ON (requires SCC-I preset)
		BIT 6 BIT 7			\$85 \$86	(132) (133) (134)	Set Internal Slot-2 Linear OFF (default) Set Internal Slot-2 Linear ON (requires SCC-I or ASCII-8K/16K preset)
\$4C		BIT 0 BIT 1	CPU Clock Video Output (MSB)	Hard DIP-SW1 Hard DIP-SW2	\$B0 \$B7	(176 183)	Set Master Volume 0 7 (default level is 7)
	( read only )	BIT 2 BIT 3	Video Output (MSB) Video Output (LSB) Cartridge Slot-1	Hard DIP-SW2 Hard DIP-SW3 Hard DIP-SW4	\$B8 \$BF	( 184 191 )	Set PSG Volume 0 7 (default level is 4)
		BIT 4 BIT 5	Cartridge Slot-2 (MSB) Cartridge Slot-2 (LSB)	Hard DIP-SW5 Hard DIP-SW6	\$C0 \$C7	( 192 199 )	Set SCC-I Volume 0 7 (default level is 4)
		BIT 6 BIT 7	Internal Mapper Internal MegaSD	Hard DIP-SW7 Hard DIP-SW8	\$C8 \$CF	( 200 207 )	Set OPLL Volume 0 7 (default level is 4)
640	( read / write_n )	BIT 0 BIT 1		BIT 0 (LSB) BIT 1	\$D0 \$D1	( 208 ) ( 209 )	Force NTSC Mode Standard NTSC/PAL Mode (bound by Control Register 9) (default)
		BIT 2 BIT 3	64kB VRAM Slot ID (Page 0)	BIT 2 BIT 3 (MSB)	\$D2 \$D3	(210) (211)	Force PAL Mode Restore Default Keyboard Layout only
\$4D		BIT 4 BIT 5	CALID VIDANA CLEE ID (Deep 4)	BIT 0 (LSB) BIT 1	\$D4 \$D5	(212) (213)	Null Command (reserved) Restore Default Turbo Mode only
		BIT 6 BIT 7	64kB VRAM Slot ID (Page 1)	BIT 2 BIT 3 (MSB)	\$D6 \$D7	(214) (215)	Set Centering YJK Modes/R25 Mask OFF (default) Set Centering YJK Modes/R25 Mask ON
	( read only )	BIT 0 BIT 1		BIT 0 (LSB) BIT 1	 \$F9	 ( 249 )	Reserve Slot-0 Primary Mode (warm reset to set) (internal OPLL disabled)
645		BIT 2 BIT 3	OCM-PLD main version X.Y(.z)	BIT 2 BIT 3	\$FA \$FB	(250) (251)	Reserve System Logo ON (warm reset only) Cold Reset
\$4E		BIT 4 BIT 5	(range 0.0.z - 25.5.z)	BIT 4 BIT 5	\$FC \$FD	( 252 ) ( 253 )	Warm Reset w/ Mapper 2048kB Warm Reset
		BIT 6 BIT 7		BIT 6 BIT 7 (MSB)	\$FE \$FF	( 254 ) ( 255 )	Warm Reset w/ Mapper 4096kB Restore All Default + Reserve Default Mapper & MegaSD
\$4F	( read only )	BIT 0 BIT 1	BIT 0 BIT 1 BIT 2 I/O Revision ID (0 - 31)	BIT 0 (LSB) BIT 1			essembly Page   v http://man.grauy.nl/resource/mey is nexts aballeritish is a
		BIT 2 BIT 3		BIT 2 BIT 3		wore into at MSX As	ssembly Page! < http://map.grauw.nl/resources/msx_io_ports.php#switch_io >
		BIT 4 BIT 5 OCI	OCM-PLD sub version (x.y).Z	BIT 4 (MSB) BIT 0 (LSB)	R/W Logic	Positive	0 = OFF 1 = ON ( read / write )
		BIT 6 BIT 7	(range x.y.0 - x.y.3) Default Keyboard Layout	BIT 1 (MSB) Status	N/W Logic	Negative	0 = ON 1 = OFF (read_n / write_n )
CPU Clock         [F12] or [DIP-SW1]           Video Output         [(SHIFT+)PRNSCR] or [DIP-SW2/3]							[F12] or [DIP-SW1] ut [(SHIFT+)PRNSCR] or [DIP-SW2/3]
			Reserved to IPL-ROM		Taggla	Audio Mixer & Cartridge Slot	CMT [(SHIFT+)PGUP/PGDOWN/F9/F10/F11] & [SCRLCK]
\$4E	( write_n only)	BIT 0 - 6 BIT 7	Free JIS2 Enabler	\$FF (Null) 0=JIS1+JIS2, 1=JIS1 only	Toggles	Cartridge Slot System Rese	t-2 [SHIFT+SCRLCK] or [DIP-SW5/6] et [HARD RESET KEY] Fast or Long-Click (normal or full reboot)
\$4F	( write_n only)	BIT 0 - 6 BIT 7	Free F4 Device Mode	\$FF (Null) 0=Normal, 1=Inverted		Internal Mapp Internal Mega	per [DIP-SW7] only