**Configurare registre MIPS16 Pipeline**

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| --- | --- | --- | --- |
| **IF/ID** | **ID/EX** | **EX/MEM** | **MEM/WB** |
| Instruction\_IF\_ID(16) | RegDst\_ID\_EX(1) | Branch\_EX\_MEM(1) | RegWr\_MEM\_WB(1) |
| PC\_1\_IF\_ID(16) | Branch\_ID\_EX(1) | MemWr\_EX\_MEM(1) | MemtoReg\_MEM\_WB(1) |
|  | RegWr\_ID\_EX(1) | MemtoReg\_EX\_MEM(1) | ALURes\_MEM\_WB(16) |
|  | Br\_ne\_ID\_EX(1) | RegWr\_EX\_MEM(1) | MemData\_MEM\_WB(16) |
|  | ALUSrc\_ID\_EX(1) | Br\_ne\_EX\_MEM(1) | RD\_MEM\_WB(3) |
|  | ALUOp\_ID\_EX(2) | Zero\_EX\_MEM(1) |  |
|  | MemWr\_ID\_EX(1) | BrAddr\_EX\_MEM(16) |  |
|  | MemtoReg\_ID\_EX(1) | ALURes\_EX\_MEM(16) |  |
|  | RD1\_ID\_EX(16) | RD\_EX\_MEM(3) |  |
|  | RD2\_ID\_EX(16) | RD2\_EX\_MEM(16) |  |
|  | ExtImm\_ID\_EX(16) |  |  |
|  | func\_ID\_EX(3) |  |  |
|  | sa\_ID\_EX(1) |  |  |
|  | RD\_ID\_EX(3) |  |  |
|  | RT\_ID\_EX(3) |  |  |
|  | PCinc\_ID\_EX(16) |  |  |
|  |  |  |  |
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