

NATIONAL INSTITUTE OF TECHNOLOGY,TRICHIRAPALLI-620015

OFFICE OF THE DEAN (ACADEMIC)

REF:NITT/DA/Cycle Test-I/2014/69 Date

To

HOD

Department of **Faculty Department**

Sir/Madam,

Sub:B.Tech-ODD Semester-(2014)-First Cycle Test Question Paper Setting-Reg.

The schedule of the Second Cycle Test of ODD Semester(2014) for the subject handled by you is given below:

Branch:	APPLIED ELECTRONICS ENGINEERING
Semester:	sem
Coarse Code No:	EC217
Course:	APPLIED ELECTRONICS ENGINEERING
Date:	date
Time:	time
Venue:	venue

The question paper may be set for 20 marks keeping in view of the model of the assignment given prior to the test. Arrangements for the multiple copies of the question paper for the test may kindly be made well ahead of the scheduled time of the test. The evaluated answer papers may be returned to the students within a week from the date of completion of the test. Cycle test marks should be entered simultaneously in the MIS.

You are requested to be invigilator for the test. Kindly do not send M.Tech Students/Ph.D. Scholars to invigilate. Invigilators are requested to be present in the Examination hall atleast 10 minutes before the commencement of examination.

Thanking You.

Deputy Registrar (Student Activities)