



global AI bootcamp

March 4th 2023, Torino - Italy

Azure ML And ONNX



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AI Architect and AI Ethicist



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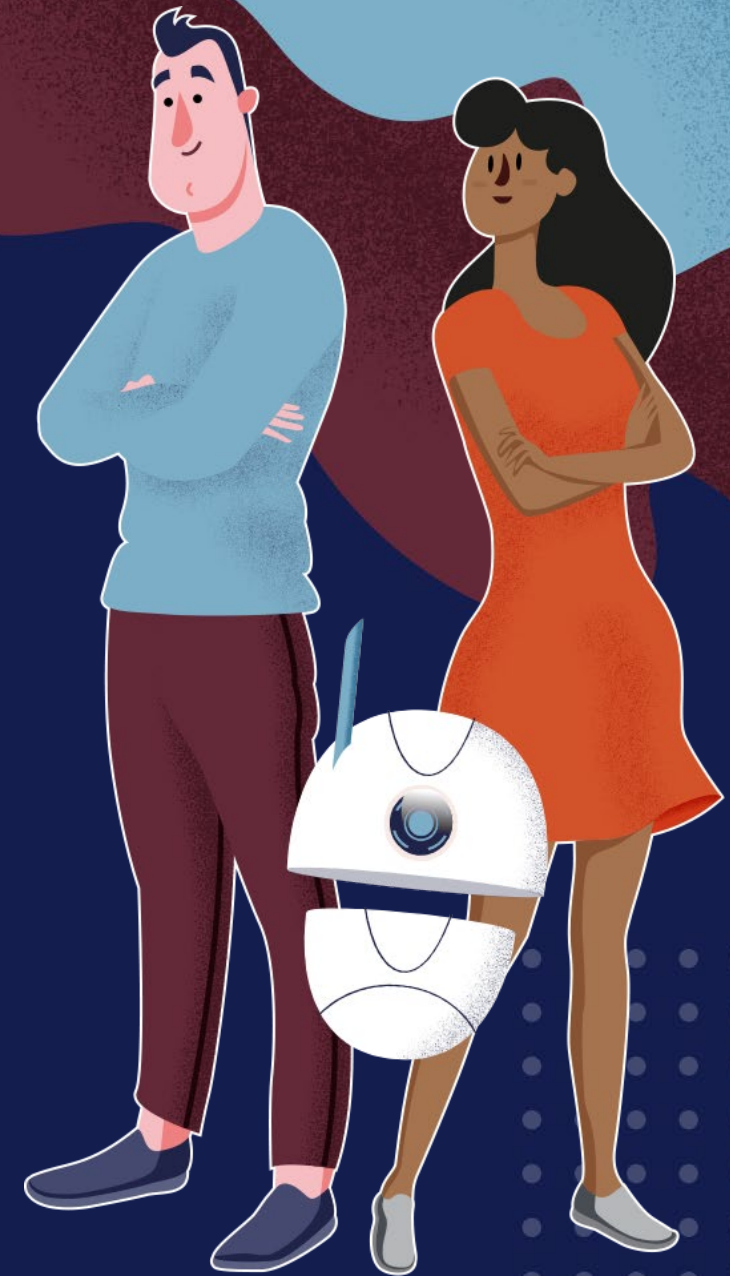
@maurobennici

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Everyone has their own origins

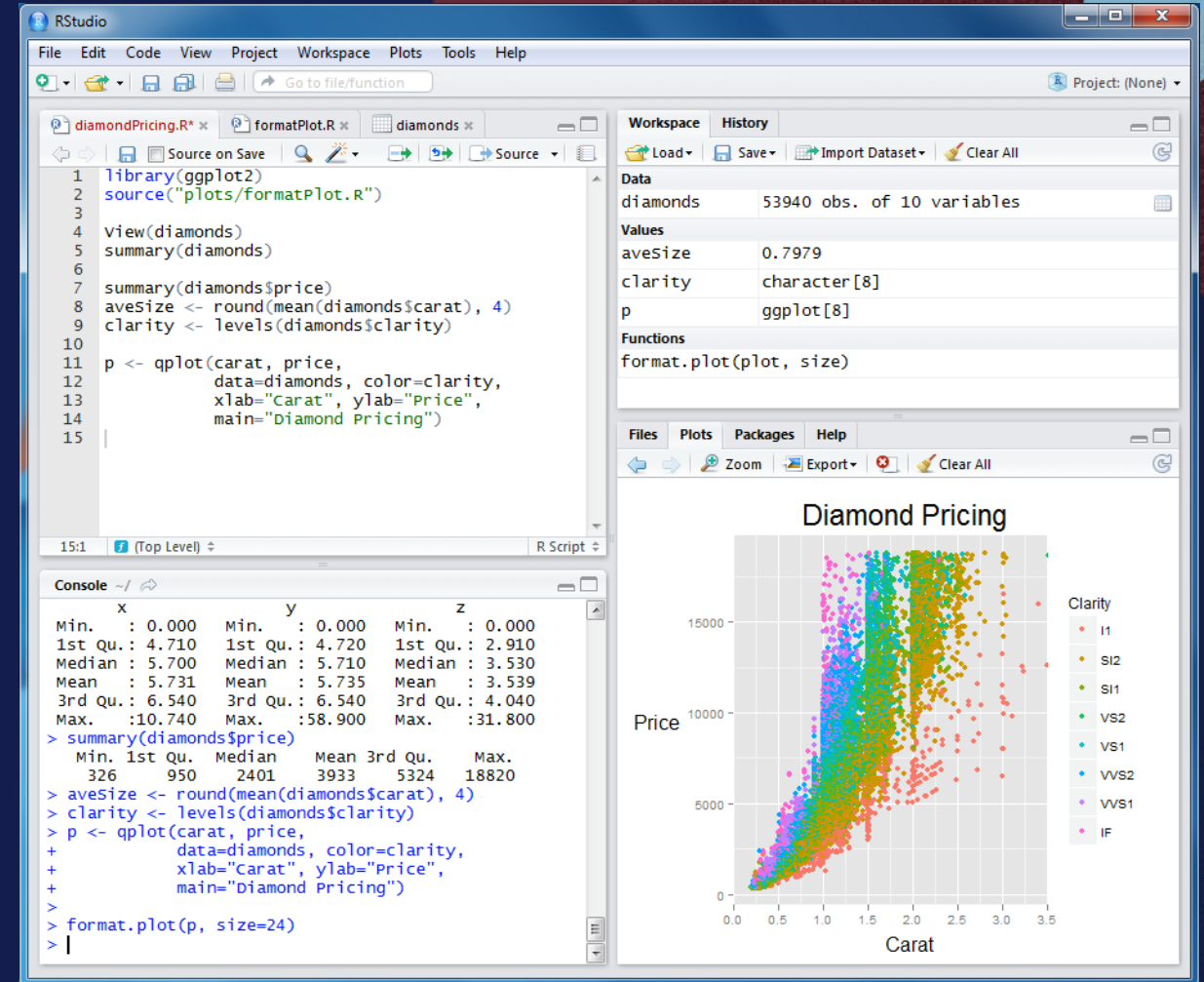
```
***** COMMODORE 64 BASIC V2 *****  
64K RAM SYSTEM 38911 BASIC BYTES FREE  
READY.  
10 INPUT "YOUR NAME";X$  
20 PRINT "HI ";X$  
30 INPUT "HOW ARE YOU";X$  
40 PRINT "NICE TO MEET YOU!"  
50 PRINT "HAVE ■"
```

The cloud dilemma



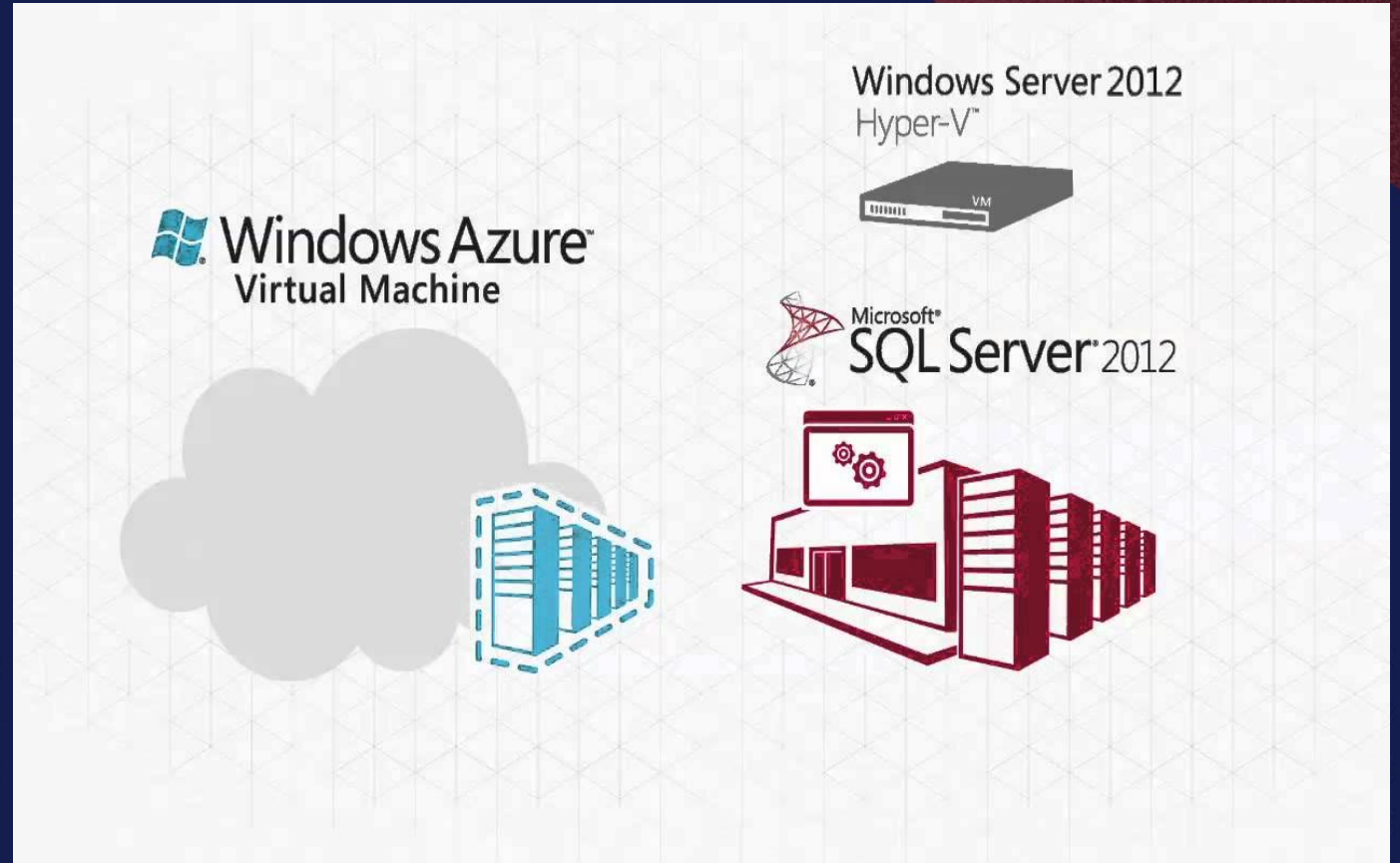
The old way

- Data science was a laboratory science
- Data and processing took place in private data centers
- Custom software was a lot



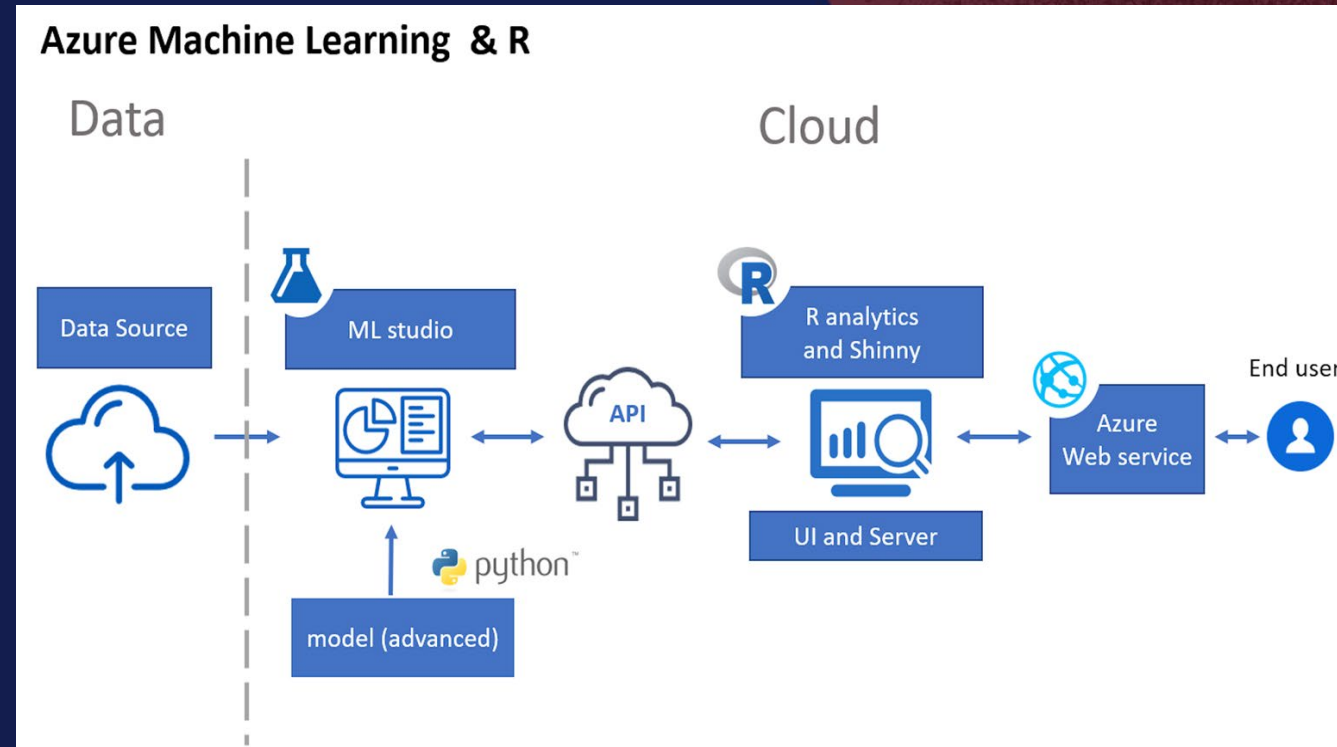
The first cloud era

- Here comes the cloud
- Online transposition of on-prem applications
- Problem shifted



The arrival of ML services

- Specialized services such as AzureML
- Everything in one place (data lake, sql, nosql, files, models, APIs)
- Vendor lock-in?
- Hardware lock-in?

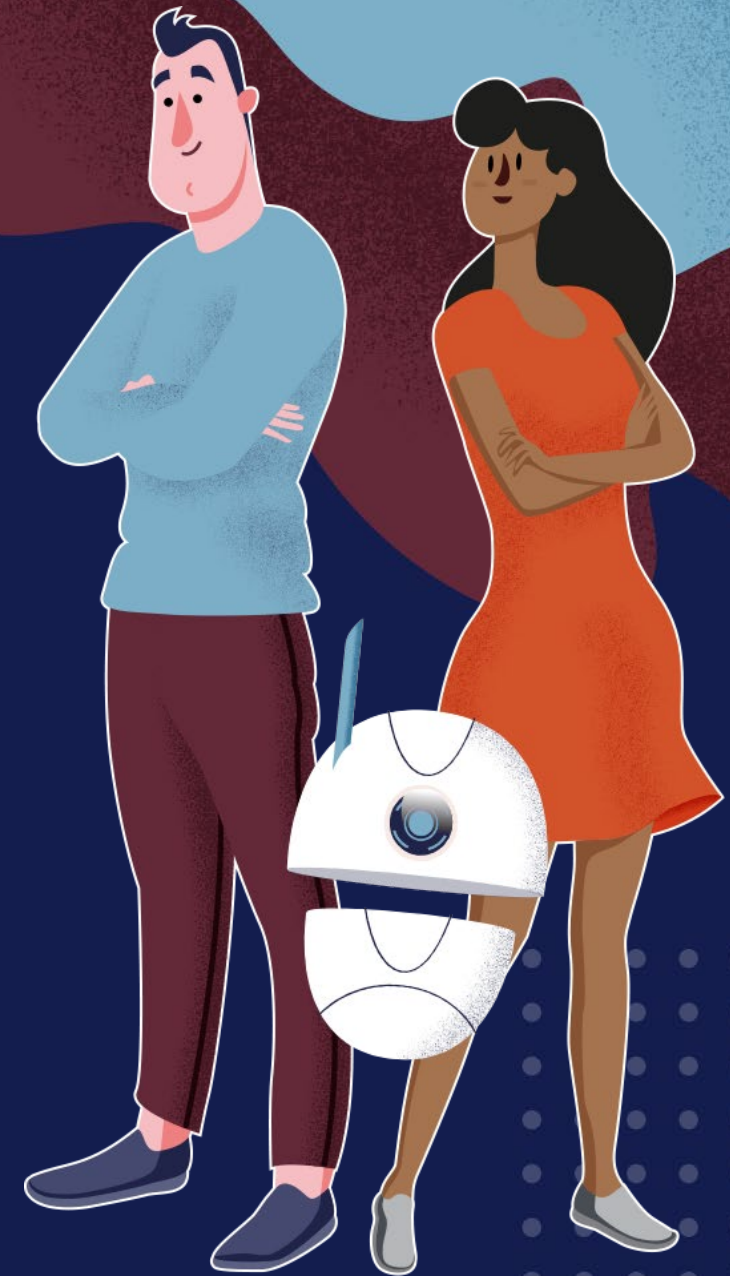


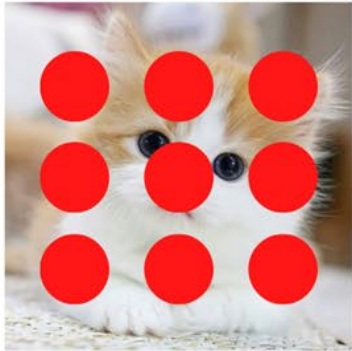
What about AI models?

- Frameworks updated frequently with breaking changes
- Custom implementations for different languages
- Incompatibility between models (frameworks)
- Increasingly high costs



One ONNX to gherm them...





Ha'DIbaH mangHom uSgheb

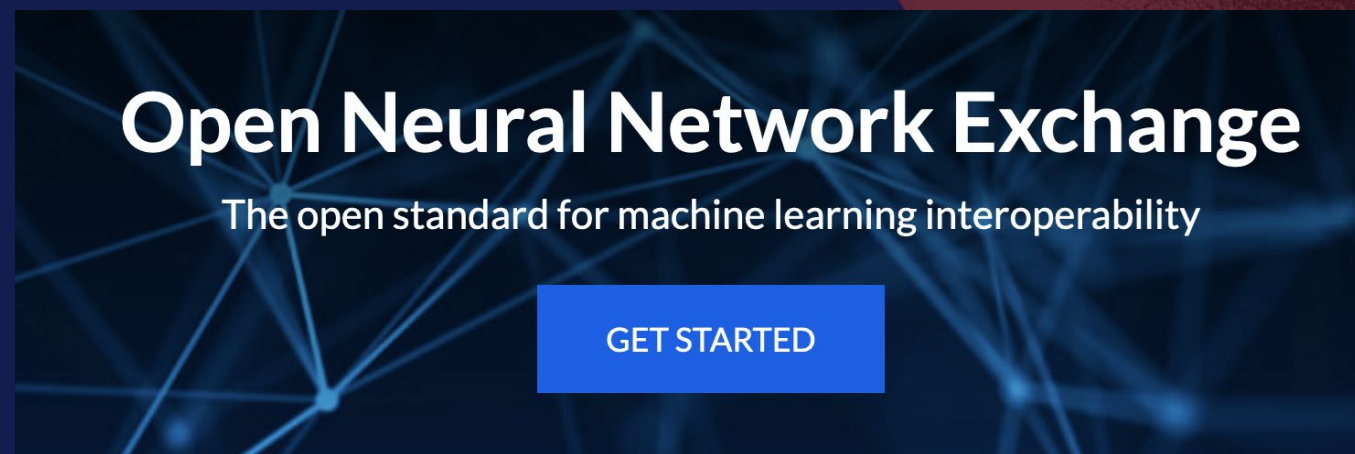
Convert

- The models eventually contain (for inference) the same thing
- Weights, structure and types
- Tools to convert models are born
- Tools to serve models are born



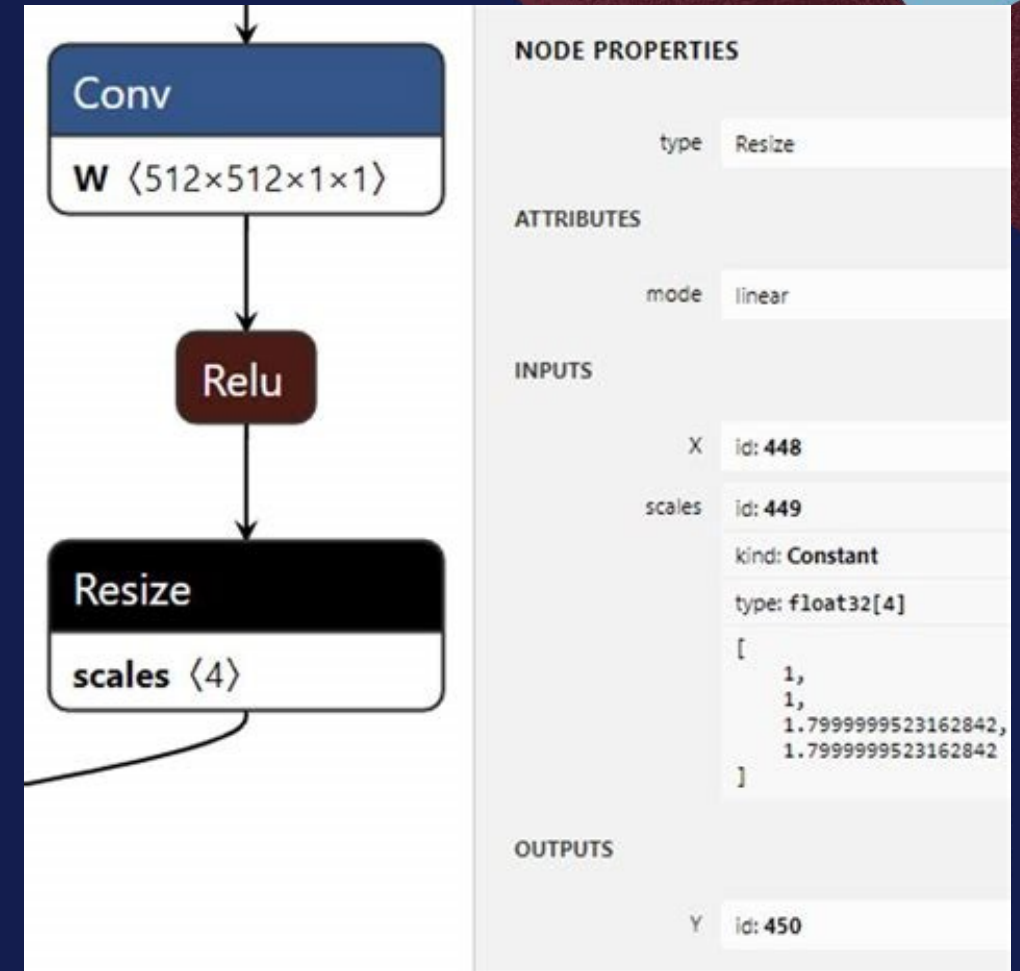
Lingua franca

- One conversion format
- OpSet (versions)
- Visualizers and Analyzers

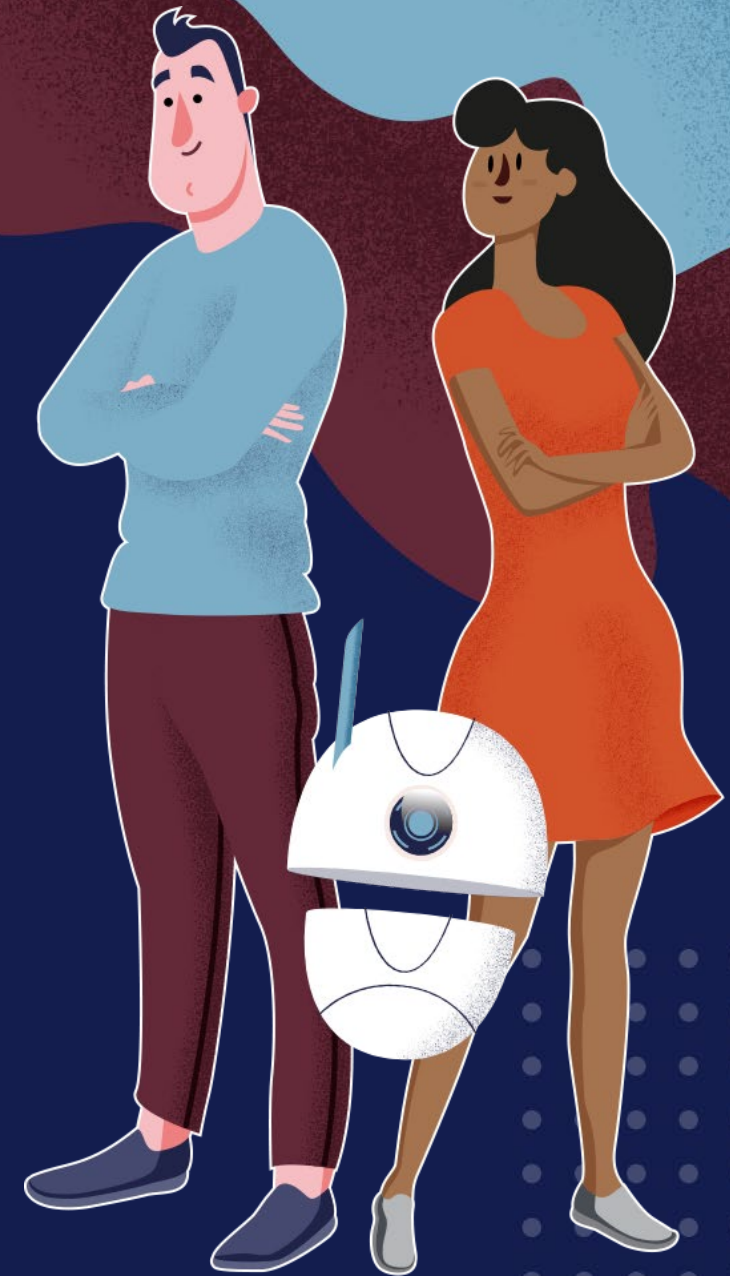


Load

- Change framework
- Use the preferred framework
- Do targeted testing



ONNX and runtimes



Run

- ONNX Runtime
- Inference with the exchange model
- Optimization

	Optimize Inferencing	Optimize Training						
Platform	Windows	Linux	Mac	Android	iOS	Web Browser (Preview)		
API	Python	C++	C#	C	Java	JS	Obj-C	WinRT
Architecture	X64	X86	ARM64	ARM32	IBM Power			
Hardware Acceleration	Default CPU	CoreML	CUDA	DirectML				
	NNAPI	oneDNN	OpenVINO	SNPE				
	TensorRT	ACL (Preview)	ArmNN (Preview)	CANN (Preview)				
	MIGraphX (Preview)	ROCm (Preview)	Rockchip NPU (Preview)	TVM (Preview)				
	Vitis AI (Preview)	XNNPACK (Preview)						
Installation Instructions	Please select a combination of resources							

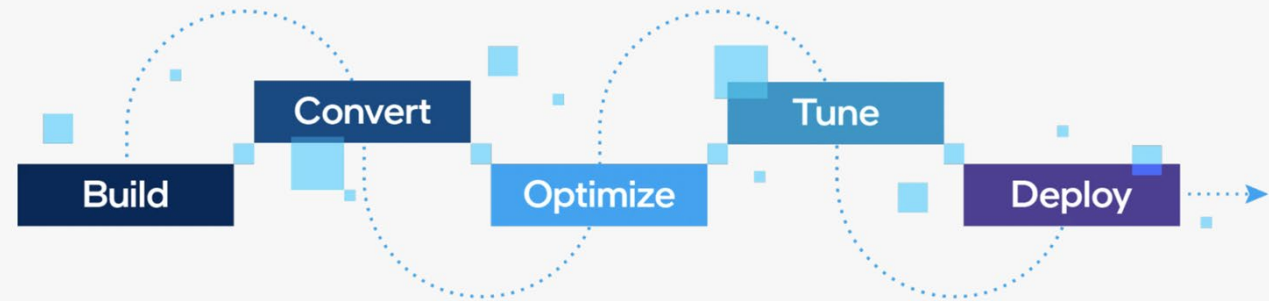
All-in

- Library and runtime for language and hardware
- Optimization libraries on the ONNX model
- Training directly in ONNX (ORModule for PyTorch)

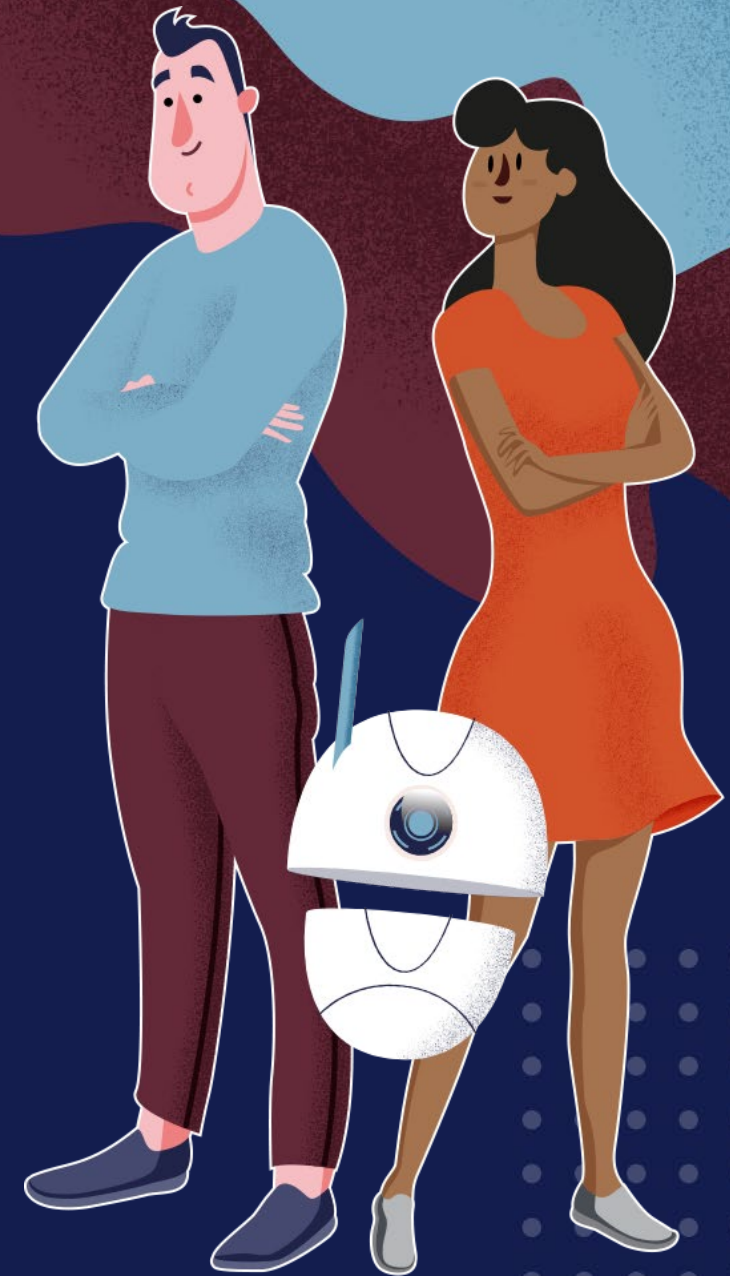


Intel OpenVINO

- Libraries and runtimes for Intel hardware
- Multi Device Clusters
- Training directly in ONNX (TF and Pytorch in two lines)



DEMO



NNAPI Execution Provider

Accelerate ONNX models on Android devices with ONNX Runtime and the NNAPI execution provider. [Android Neural Networks API \(NNAPI\)](#) is a unified interface to CPU, GPU, and NN accelerators on Android.

SNPE Execution Provider

The SNPE Execution Provider for ONNX Runtime enables hardware accelerated execution on Qualcomm Snapdragon CPU, the Qualcomm Adreno™ GPU, or the Hexagon DSP. This execution provider makes use of the Qualcomm Snapdragon Neural Processing Engine SDK.

This execution provider uses the AOT converted DLC code as an embedded node in the ONNX model file.

XNNPACK Execution Provider

Accelerate ONNX models on Android devices and WebAssembly with ONNX Runtime and the XNNPACK execution provider. [\(XNNPACK\)](#) is a highly optimized library of floating-point neural network inference operators for ARM, WebAssembly, and x86 platforms.

CoreML Execution Provider

[Core ML](#) is a machine learning framework introduced by Apple. It is designed to seamlessly take advantage of powerful hardware technology including CPU, GPU, and Neural Engine, in the most efficient way in order to maximize performance while minimizing memory and power consumption.

Arm® CPU Device

Introducing the Arm® CPU Plugin

The Arm® CPU plugin is developed in order to enable deep neural networks inference on Arm® CPU, using [Compute Library](#) as a backend.

Note

This is a community-level add-on to OpenVINO™. Intel® welcomes community participation in the OpenVINO™ ecosystem, technical questions and code contributions on community forums. However, this component has not undergone full release validation or qualification from Intel®, hence no official support is offered.

The set of supported layers and their limitations are defined on the [Op-set specification page](#).



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Thank you! Q & A Time 😊



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