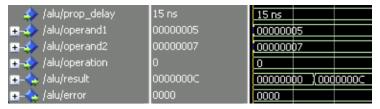
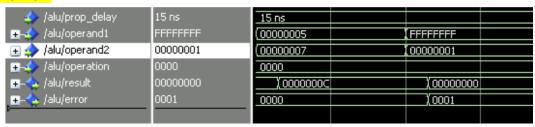
## Lab 2 Transcript (Screenshots)

I. Unsigned Add

A. Without Overflow



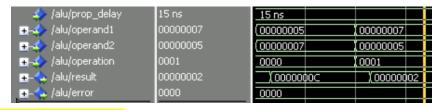
## 1. E- / III



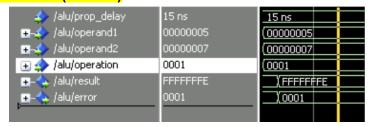
II. Unsigned Sub

A. Without Overflow

1.



B. With Overflow (5-7 = -2)



C.

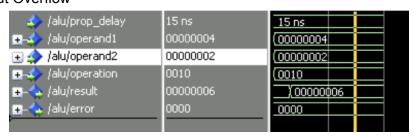
Add

III.

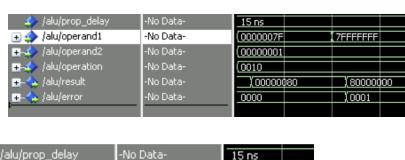
A. Without Overflow

1.

1.

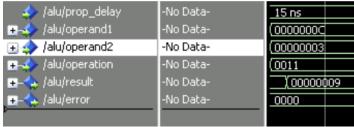


B. With Overflow (0h7FFFFFFF + 0h01 = -2 million something in bin)



IV. Sub

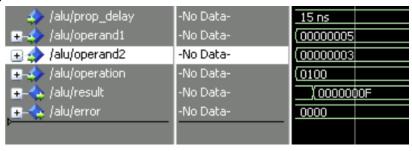
1.



V. Multiply

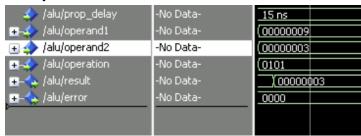
Α.

A.



VI. Divide

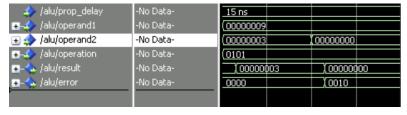
A. Without Div by Zero



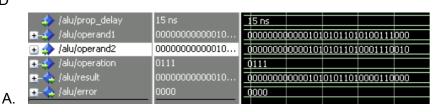
B. With Div by Zero (9/0)

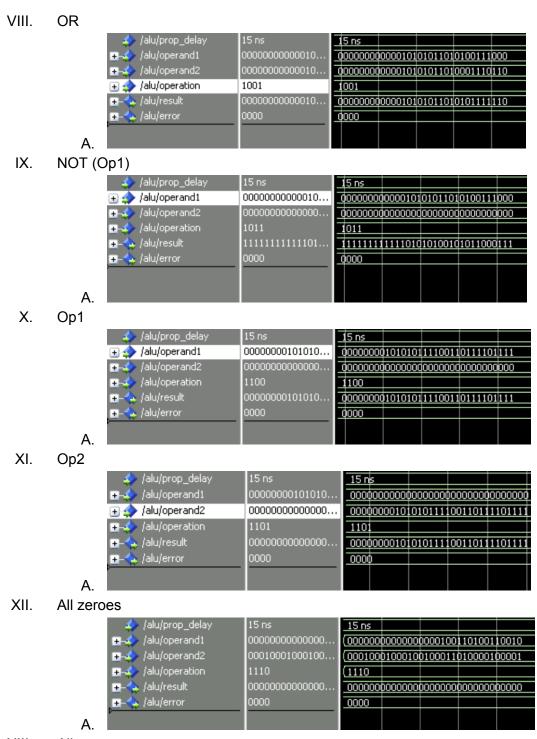
1.

1.



VII. AND





XIII. All ones

	/alu/prop_delay	15 ns	15 n	s				
	+- / /alu/operand1	0000000000000000	0000	0000000	0000000	0011011	1101101	
	🕳 🧆 /alu/operand2	000000000000000	0000	0000000	0000000	00011111	1111111	
	+- / /alu/operation	1111	1111					
	+-4 /alu/result	000000000000000000000000000000000000000	1111	1111111	1111111	1111111	11111111	Ī
	💶 👍 /alu/error	0000	0000					Ē
								Ī
A.								