

Parallelization of the Probabilistic Roadmap Method with GPU Acceleration

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the date of receipt and acceptance should be inserted later

1 Abstract

In trajectory optimization with obstacles, it is often necessary to provide feasible initial trajectories. For example in robot motion planning, where the dimensions of the underlying spaces are rather high, this can be expensive. A further problem is that because of the transformation from work into configuration space, the obstacles are often no longer given in a closed form. An efficient approximative solution for this is using probabilistic roadmaps (PRMs).

In this paper, a parallelized version of the PRM method has been developed in general. A robotics application then was implemented as an special case of this with GPU support.

2 Probabilistic Roadmap Method

2.1 Problem Statement and the Sequential PRM Method

The abstract problem treated here is to find a path in a d -dimensional space $\Omega \subset \mathbb{R}^d$, which avoids intersecting obstacles. The area occupied by obstacles is given by an indicator function

$$I : \Omega \rightarrow \{0, 1\} \quad (1)$$

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on a region $\Omega = [q_{min,1}, q_{max,1}] \times \dots \times [q_{min,d}, q_{max,d}] \subset \mathbb{R}^d$. Hence, the goal is to find a continuous curve

$$\Gamma : [0, 1] \rightarrow \Omega \quad (2)$$

between a given start and endpoint, $\Gamma(0) = q_b$, $\Gamma(1) = q_e$, such that

$$\forall s \in [0, 1] : I(\Gamma(s)) = 0. \quad (3)$$

The PRM algorithm now grows a graph $G = (V, E)$, where V is the set of vertices and E the set of edges, form points q_s and q_e by randomly sampling nodes $v \in \Omega$ and inserting them into the set V . Two nodes $v, w \in V$ are connected by an edge, if they are near enough each other and the line between them is free from obstacles, based on definition 1, see below. The algorithm terminates, when there exists a path from q_s to q_e on the graph G .

Definition 1 For two points q_1 and q_2 we define the line segment partitioned with stepsize $h > 0$ as

$$[q_1, q_2]_h := \{q = \lambda q_1 + (1 - \lambda)q_2 \mid \lambda \in [0, 1] \wedge \|q - q_1\| = kh, k \in \mathbb{N}_0\}. \quad (4)$$

We say, q_1 and q_2 are connected with stepsize $h > 0$, if $I(q) = 0$ for all $q \in [q_1, q_2]_h$.

Algorithm 1 shows the sequential version. Hereby, the probability density p_G (depending on the current graph) determines the sampling of new nodes. p_G is defined for example as first selecting randomly a node $q_0 \in V$ and then sampling a new point q in the neighbourhood of q_0 . Theory about success probabilities and convergence results can be found in [1], [2] or [3].

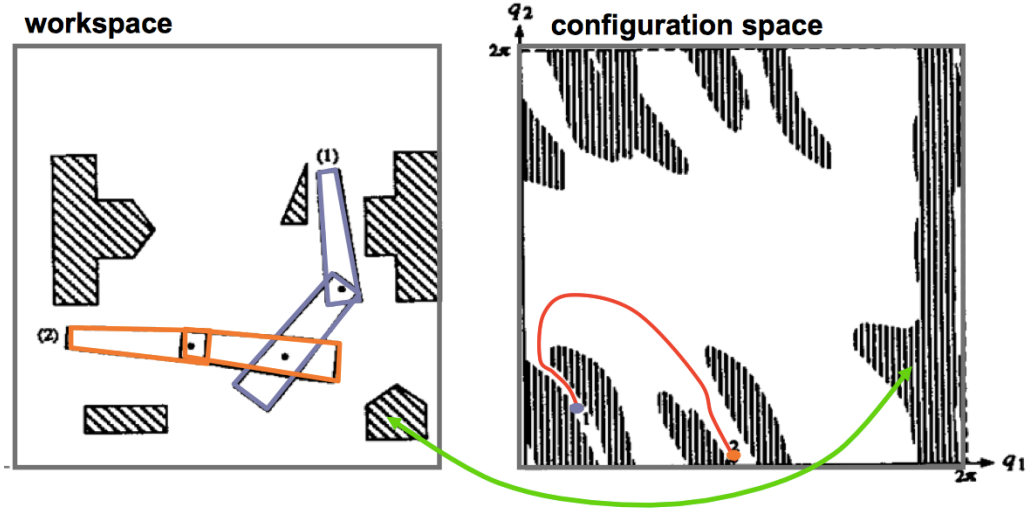


Fig. 1 Example from [1] for an indicator function in robotics. The given obstacles on the left are transformed into occupied areas of the configuration space on the right.

Algorithm 1: Probabilistic Roadmap Algorithm

Data: $q_s, q_e \in \Omega, h > 0, d_0 > 0$
Result: $q_s = q_1, \dots, q_n = q_e \in \Omega$, such that all q_i and q_{i+1} are connected with stepsize h
 Initialize graph $G = (V, E)$ with $V = \{q_s, q_e\}$
 If q_s and q_e are connected, $E \leftarrow \{q_s, q_e\}$
while *there exists no path in G between q_s and q_e* **do**
 Sample $q \in \Omega$ from a probability density p_G ,
 resample until $I(q) = 0$
 for *all nodes $\tilde{q} \in V$, with $\|q - \tilde{q}\| \leq d_0$* **do**
 if q and \tilde{q} connected with stepsize h **then**
 $E \leftarrow \{q, \tilde{q}\}$
 end
 end
 $V \leftarrow q$, if at least one edge was inserted
end
 Determine the shortest path $q_1, \dots, q_n \in V$ on G from q_s to q_e .

2.2 Application to Robotics

In the paper, the PRM algorithm is used to find trajectories for a robot arm, which avoid self collisions and collisions with obstacles in the environment. Such trajectories can be expressed as functions describing the joint variables changing over time, that means as a curve in the configuration space with dimension equal to the number of degrees of freedom of the robot. With this association, the trajectory finding problem becomes a path finding problem in the configuration space exactly of the form defined in the previous section. The real obstacles transform into occupied areas in the configuration space. Those areas are given by the indicator function, which returns for a given vector of joint angles, if it is feasible or leads to a collision.

2.3 Project Overview

Like in the stated robotics example, in many cases the most costly operations of the PRM algorithm are the many evaluations of the indicator function $I(q)$, which however can be done independently for every new q . Therefore the evaluation is implemented in a vectorized form as a function $(q_1, \dots, q_P) \mapsto (I(q_1), \dots, I(q_P))$. The single indicator function computations are done in parallel on one or more graphics processing units (GPU). The implementation is done with the programming interface CUDA [7], [8] by Nvidia. In the robotics case, this means, the direct kinematics and collision calculations are made on the GPU in parallel for different joint angle configurations. Although this will be the only major application in the paper, the PRM solver and the configuration space are implemented separately to provide a flexible interface, as shown in figure 2.

While the distribution of the indicator function to different CUDA threads is a first level of parallelization, a second level can be achieved by sampling and connecting several new nodes in parallel. This is more difficult, because all new nodes have to be connected and the graph data has to be updated on all processes. Different approaches have been implemented, with a description provided in chapter 4. Before that, chapter 3 gives more details about the robotics indicator function.

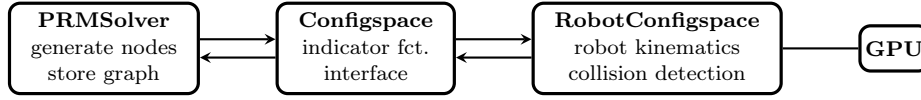


Fig. 2 Project structure. Probabilistic roadmap solver and configuration space implementation are separated.

3 Implementation of an Indicator Function for Robot Arms

The probabilistic roadmap algorithm is applied to create collision-free trajectories of a robot arm. For a robot with given joint angles q_i one can calculate straightforward all positions and orientations of the single parts of the robot. For this, coordinate frames according to the Denavit-Hartenberg (DH) [4] convention are used. The coordinate frames of reference of the single parts are described by transformation matrices $T_i \in \mathbb{R}^{4 \times 4}$, $i = 1, \dots, d$, which transform points from the body frame of reference into the world frame of reference. They depend on the state $q = (q_1, \dots, q_d)^T$ of the robot and on the geometry of the joint angles given by the DH parameters of the robot.

The geometry of the robot parts is defined by a set of convex polytopes, each of them belonging to one of the robot coordinate frames. Applying the corresponding transformation T_i , the vertices are transformed into the world frame. Environment obstacles are also defined by convex polytopes, given in the world frame.

Now the movement of the robot can be described by a curve $q(t) \in \mathbb{R}^d$, $t \in [0, 1]$, in the configuration space. The movement is collision-free, if at no time, any two polytopes of the robot intersect with each other or with the environment after being transformed into the world frame. To apply the probabilistic roadmap algorithm, we express this setting by the indicator function

$$I(q) = \begin{cases} 0, & \text{for no collision of any polytope pair} \\ 1, & \text{if at least one collision occurs} \end{cases}. \quad (5)$$

The indicator function is implemented in a vectorized form in a CUDA kernel, which means that, given a set of states q^1, \dots, q^M , the indicator function is evaluated for each state by one CUDA thread. Therefore, the kinematics and collision algorithm are implemented as device code for the use in the CUDA kernel.

It has to be noted that the trajectories $q(t)$, generated by the PRM algorithm, are piecewise linear and are therefore not differentiable at all points. Hence, before the use on a robot they have to be smoothed. This is not included in the project. The task is only to generate feasible trajectories to be possibly passed to an optimizer.

3.1 Direct Kinematics

The Denavit-Hartenberg convention is a method to describe the geometry of the joints of a robot, that is, how the transformations between the robot coordinate frames look like in dependency of its joint values q_1, \dots, q_d . A robot consists of $d + 1$ bodies each of which has a coordinate frame B_i attached to the body, which is described by the transformation matrix T_i to the world frame. It is assumed that each joint is either rotational, that means a rotation around a fixed axis is performed, or prismatic, where a translation along an axis is performed. These joints can now be defined through DH parameters $a_{i-1}, \alpha_{i-1}, d_i, \theta_i \in \mathbb{R}$, $i = 1, \dots, d$, by setting up the transformations $T_{i-1,i} = T_{i-1}^{-1}T_i$ between two bodies as

$$T_{i-1,i} = R_z(\theta_i)T_z(d_i)R_x(\alpha_{i-1})R_x(a_{i-1}) \quad (6)$$

where R_x, R_z are rotations around and T_x, T_z are translations along the x and z axes. With this and $T_0 = I$, all transformations T_i are determined.

One can show, that by an proper choice of the body frames, every robot with rotational or prismatic joints can be described by such a set of DH parameters, i.e. there exists a set of parameters, such that the above definition leads to the correct transformations for this robot. For the exact calculations, see [4].

3.2 2D and 3D Geometry Library

For the implementation of the kinematics and later the collision algorithm, a geometry library was written, based on the CUDA datatypes `float2` for 2D vectors and `float4` for 3D vectors. To guarantee aligned access on the GPU, `float4` is used instead of `float3`. Furthermore, as the precision of the geometric calculations is numerically not very critical, the usage of single precision instead of double precision did not cause problems. The library contains host and device inline functions for common vector operations as shown in code 1. Operators like $+$ or $*$ are not overloaded to prevent unnecessary temporary variables. Transformation matrices are implemented as `float4` arrays with inline functions for common matrix operations. Again operators with temporaries were avoided, wherever possible.

Code 1 Operator examples of the written float4 library.

```

__host__ __device__ inline float4& operator += (float4& u, const float4& v);
__host__ __device__ inline float4& operator *= (float4& u, const float f);
__host__ __device__ inline float4& add(const float4& u, const float4& v, float4& w);

```

Code 2 Class structure for a geometric transformation matrix.

```

class trafo4{
public:
    float4 col[4];
    __host__ __device__ trafo4(){}
    __host__ __device__ trafo4(float a, float alpha, float q, float d);
    __host__ __device__ inline float4& apply(float4& u) const;
    __host__ __device__ inline float4& apply(const float4& u, float4& Tu) const;
    .
    .
}

```

3.3 Polytopes

To calculate the intersection between two convex polytopes, the Chung-Wang algorithm [5] is used. It only needs the polytope vertices and the data, which of them are connected by an edge. For this purpose, the adjacency matrix of the edge graph is stored in compressed row storage format.

Code 3 Storing polytopes in CRS format.

```

struct polytope4{
    //vertices
    float4* vertices;    //length n
    int n;
    //edge adjacency matrix
    int* dsp;    //length n
    int* cnt;    //length n
    int* dest;   //length m
    int m;
};

```

This means, that for $i = 0, \dots, n-1$ the vertices with indices $\text{dest}[\text{dsp}[i]], \dots, \text{dest}[\text{dsp}[i] + \text{cnt}[i] - 1]$ are the ones connected to vertex i by an edge. The polytopes are given by the user as convex hull of a list of vertices. However, to determine the edge graph only from the vertices is a nontrivial task, which is done by Matlab using builtin high level functions.

3.4 Chung-Wang Collision Algorithm

To check if two polytopes collide an algorithm by Chung and Wang [5] is used here. It uses the separating vector theorem and tries to find a separating vector by an iterative search. Furthermore, a sub-algorithm determines in each iteration from the candidate history, if it is still

theoretically possible to find a separating vector. If not, a collision is returned. If on the other hand a separating vector is found, the algorithm returns no collision. For a detailed description of the algorithm see [5] and [6], pp. 410-412. The method is implemented as device code for the use in CUDA kernels.

In our application, the polytopes are mostly parts of the robot, which are given in the body frame of reference and have to be first transformed into world frame according to section 3.1. However, from the structure of the Chung-Wang algorithm follows, that only a small number of the polytope vertices are needed for the computations. Therefore, it would be rather wasteful to transform all vertices before passing them to the algorithm. To avoid this problem, all polytopes are passed in their body frames together with their transformations and the algorithm transforms a vertex, whenever it is needed in the calculations. This is also the version proposed in [5].

3.5 Robot Kernel

Code 4 exemplarily shows the class definition of the robot configuration space. The geometry and robot data does not change during runtime and is loaded once at the beginning and copied to the GPU by the `init` function. The indicator function gets as input a list of start and end points $q_{start}^j, q_{end}^j, j = 0, \dots, N$ and checks now for each pair, if the indicator function is always 0 on the line in between.

$$\text{res}[j] = \begin{cases} 0 & \text{if } \forall q \in [q_{start}^j, q_{end}^j]_{\Delta q} : I(q) = 0 \\ 1 & \text{else} \end{cases} \quad (7)$$

Code 4 Robot configuration space class structure.

```

template<int ndof>
class RobotConfigspace : public Configspace<ndof>
{
public:
    RobotConfigspace(const Robot<ndof>* robot_,
                     const polytope *polys_,
                     const int* sys_,
                     const int N_,
                     const int *from_, const int *to_,
                     const int M_,
                     const float* mins_, const float* maxs_, const float dq_,
                     const int nbuf_);
    int init(const int ressource_rank=0, const int ressource_size=1);
    int indicator2(const float* qs, const float* qe, int *res, const int N, const int offset);
    .
    .
private:
    const Robot<ndof>* robot; //host object
    Robot<ndof>* robotdev;    //GPU object
    collision4::polytope4data* polydata; //host object
    collision4::polytope4data* polydatadev; //GPU object
    collision4::polytope4data_restrict* polydatadev_restrict; //restricted pointers collection
    .
    .
}

```

Code 5 GPU collision kernel for testing robot configurations.

```

__global__ void kernel_indicator2(const Robot<ndof>* __restrict__ robot,
                                 const collision4::polytope4data_restrict polydata,
                                 const float* __restrict__ qs, int offsets,
                                 const float* __restrict__ qe, int offsete,
                                 int* res,
                                 const int* __restrict__ testpos, const int* __restrict__
                                     testnum,
                                 int N, int numthreads)

```

This computation is done by calling the kernel in Code 5. The start and end points are stored in structures of arrays defined by

$$\begin{aligned}
 q_{start}^j &= (q_s^j, q_s^{j+N_{buf}}, \dots, q_s^{j+(d-1)*N_{buf}})^T \\
 q_{end}^j &= (q_e^j, q_e^{j+N_{buf}}, \dots, q_e^{j+(d-1)*N_{buf}})^T
 \end{aligned} \tag{8}$$

As the PRM solver only needs to know, which start and end points can be connected by a line, the kernel delivers exactly this minimal necessary information. CPU and communication time is also saved by computing interpolating points directly on the GPU and not to exchange them. Algorithms 2 and 3 show in pseudo code, how the work is split between host and device.

Furthermore, in order to overlap computations on the GPU and CPU, an asynchronous version of the indicator function was implemented. Here the host function is split into the part till the kernel launch and a function, which waits, until the kernel has finished and

Algorithm 2: Host indicator function

Data: q_s, q_e, N, N_{buf}

Result: res

compute distances $d_j = |q_{end}^j - q_{start}^j|$;

testnum[j] = $d_j / \Delta q$: number of threads for each line ;

testpos[j] = thread displacements ;

reset res ;

call kernel with $\sum_{j=0}^N \text{testnum}[j]$ threads;

receives the result data. This implementation also allows overlapping of multiple requests, for example two calls of the launch function and then the two corresponding waiting calls. With that, it is possible to use the full computation power of the GPU.

Algorithm 3: Robot kernel: code for one thread

Data: arrays q_s , q_e , testpos, testnum and threadindex
Result: res
 compute associated edge number j , defined by
 $testpos[j] \leq threadindex \leq testpos[j] + testnum[j]$;
 $c = (threadindex - testpos[j]) / (testnum[j] - 1)$;
 $q = cq_{start}^j + (1 - c)q_{end}^j$;
 calculate robot kinematics $T_0(q), \dots, T_d(q)$;
 res_tmp = 0 ;
for all registered pairs pairs of polytopes P, Q **do**
 res_tmp =
 separating_vector_algorithm(P, T_{i_P}, Q, T_{i_Q}) ;
 if res_tmp $\neq 0$ **then**
 res[j] = res_tmp ;
 break ;
 end
end

4 The PRM Solver

The solver builds in a first phase the roadmap graph from both the start and the end node, until the graph is connected. It is always ensured that the graph consists of two connected components containing the start respectively the end node. With this fact, it can be easily determined at each step, if the whole graph is now connected.

At the end, in a second phase of the algorithm, a shortest path from the start to the end node is searched by the Dijkstra algorithm. It was observed, that this part of the algorithm needs a neglectable amount of time compared to building the graph. Therefore, it is not parallelized here and the focus lies on the first phase.

4.1 Storing the Graph

Apart from the indicator function evaluation, the main problem in building the probabilistic roadmap is, that for each new node q all possible neighbors have to be determined. That is the set of all nodes \tilde{q} of the graph, with $\|\tilde{q} - q\| \leq D$, with $D > 0$ an ajustable algorithm parameter. If no special order of the nodes is given, one has to calculate these norms for all nodes of the graph, which is rather time consuming. Therefore, a special indexing is used. The nodes are stored in a vector, which is organized in blocks with fixed sizes. The blocks are referred to by ids which are stored in a map. See Code 6 for the data structures.

The key of a block is computed by the mapping

$$key(q) = \left\lfloor \frac{q_1}{H} \right\rfloor \quad (9)$$

such that $H > 0$ becomes a parameter to adjust the grid density. The parameter H has to be chosen so large

that enough nodes are stored in one block in order to have efficient memory accesses. On the other hand, the smaller H is, the less unnecessary computations have to be done for the neighborhood requests. For illustration of the structure, the following pseudocode, algorithm 4, shows the insertion of a node.

Algorithm 4: Inserting a node

Data: $q \in \mathbb{R}^d$
 Compute key=key(q)
if key exists in map **then**
 block=map[key]
 while block full **do**
 block=block->next
 end
else
 insert new block at map[key]
end
 Insert node into block

For getting the neighbors of a node q it is then enough to look at the keys from $key(q_1 - cD)$ to $key(q_1 + cD)$. If $D = H$, then $c = 1$ is possible. The key mapping could be generalized by taking more components of q into account or using kd-trees, see [2], p. 209, but that was not implemented.

4.2 Parallelization Approaches

The main parallelization is done with MPI. All processes store their own versions of the graph, which are held up to date between the processes. It is assumed that every process has its own GPU. The work of a single MPI process could be parallelized further with OpenMP, but this has not been pursued. Instead, different approaches to distribute the work over several GPUs have been investigated.

Version 1

In the first approach, that has been evaluated, every process generates new nodes independently from each other. Then the possible neighbors are determined and the connections which have to be tested. The indicator function is called and returns the edge data. After this, the graph must be synchronized between all processes, which needs an MPI all-to-all communication call. As the graph building phase does not need the edge data, it is sufficient to exchange only the nodes and collect the edges at the end.

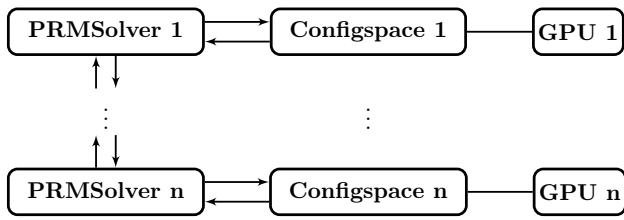
Code 6 Roadmap graph structure.

```

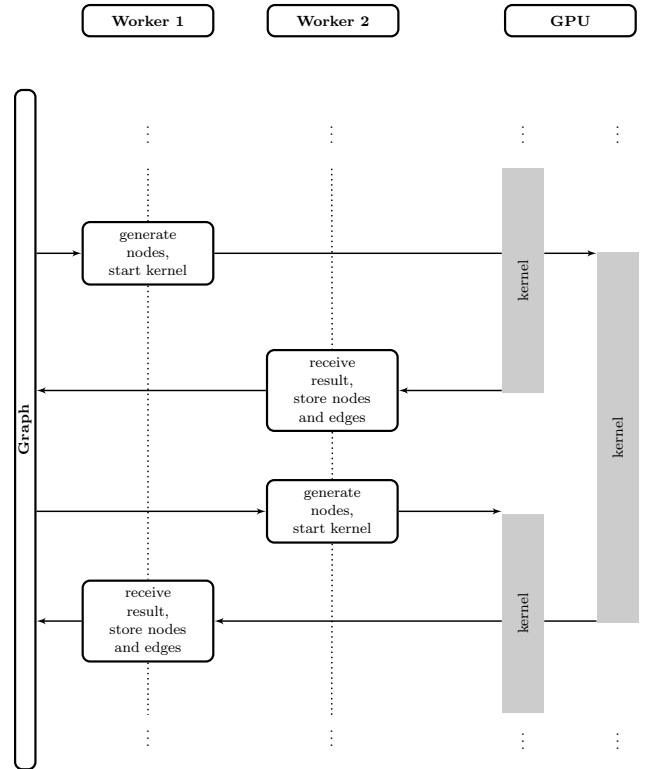
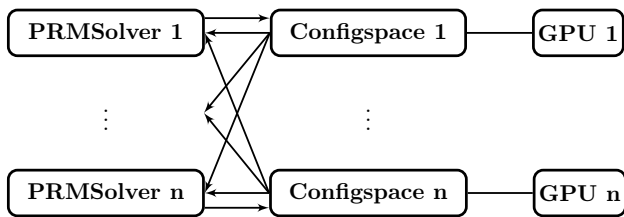
struct block{
    int pos;           ///! position of first vertex
    int num;           ///! current number of vertices stored
    block* next;       ///! if num==blocksize -> pointer to next block
};

struct graph{
    std::map<int, block*> map; ///map for accessing blocks
    std::vector<block> blocks;
    std::vector<float> qstorage; ///length ndof*N
    int newblockpos; ///position of next new block
    int blocknum;    ///number of used blocks
    .
    .
};

```

*Version 2*

The second approach uses the fact, that by synchronizing a random seed, different processes can produce the same sequence of random numbers independently from each other. By using this property, every process can generate all new nodes and determine the connection data. Then the indicator function computation is split over their configuration space instances and the returned data shared afterwards with each other. With this data, all graphs can be updated and it is guaranteed, that they grow exactly in the same way on each process. The advantage of this version compared to the first one is, that less data has to be exchanged. Furthermore, in the first version it can occur, that the generated nodes can result in very different counts of neighbors and therefore uneven loads on the GPUs. Here, the distribution of the computation amount over the GPUs can be chosen very flexible, without performance loss.

**Fig. 3** Structure of asynchronous kernel launches.*Version 3*

The third version includes a further optimization by using the asynchronous version of the indicator function. Here, every process runs two workers overlapped, between which the node generation and GPU requests are split up. Figure 3 shows the detailed work flow. With this, it is possible, that the computational resources of the GPU are fully used, and overlapped with the communication.

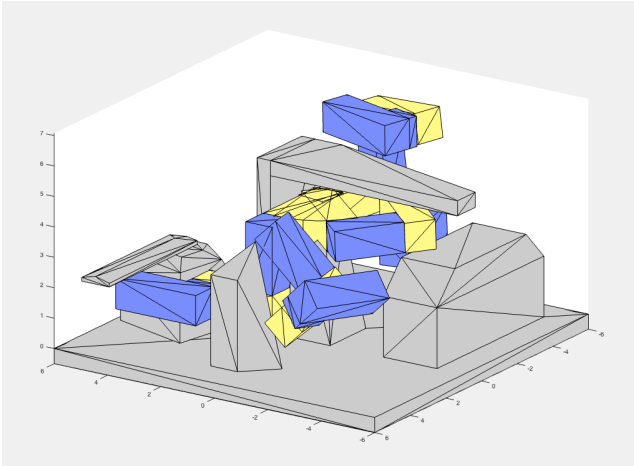


Fig. 4 PRM generated collision-free movement for a 4-axis robot

Table 1 Timing results for the parallelization versions from chapter 4 on a cluster node with 2x Intel Xeon Eight-Core CPU E5-2650 0 @ 2.00GHz and 4x Tesla K20m.

version	processes/ GPUs	time (ms)	GPU threads	time per 10 ⁶ threads (ms)
1	1	6864	6171525	1070
	2	1580	4489213	342
	4	869	5853763	149
2	1	2136	6933768	337
	2	1778	6933768	290
	4	1677	6933768	279
3	1	2492	19096275	130
	2	1345	19096275	72
	4	942	19096275	52
version	CPU threads	time (ms)	indicator evaluations	time per 10 ⁶ evals. (ms)
3	4	32730	10038198	3281
(CPU	8	17676	10038198	1785
only)	16	9936	10038198	1012

5 Results

Benchmark runs have been performed for a realistic scenario with a robot with four joints, shown in figure 4. This scenario is difficult because of the small path to be passed in the middle of the domain in figure 4. For creating and plotting the environment and the resulting trajectories, Matlab functions were written. Because of the dependency on random numbers, the execution times differ for different runs. Therefore, 10 runs were made for each version and number of processes. However, the different versions lead to a different average amount of GPU threads needed. Hence, to be able to better compare how the benchmarks scale, the time per number of GPU threads is used as a quantity. Table 1 shows the results.

As the indicator function has also been implemented on the CPU, all versions could be run without GPU. The timing results for the last one are shown in the

last section of table 1. One can see an improvement of up to a factor around 10 between the CPU and GPU benchmarks (based on version 3). However, the robot code is not especially optimized for the CPU version, as the GPU kernel has been rewritten with an for loop reusing all device functions for collision detection. The GPU performance significantly improves from version 1 to 3. The code is available on Github, [11].

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