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| **Homework 2 Report** | |
| Student ID: 123456789 | Name: 王大明 |

1. Design concept (You may write the report in Chinese):

* Explanation of the overall hardware architecture and block diagram of each component.
* State diagram and its detailed description (if any).
* Explanation of the dataflow.

1. Result

Paste the screenshot of your RTL simulation result.

1. END\_CYCLE (optional)

If you need a larger END\_CYCLE than the default, please let us know what value you are using.

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| END\_CYCLE |  |

1. Others (optional)

* Suggestions or comments about this class to teacher or TA.