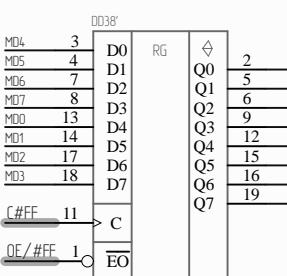
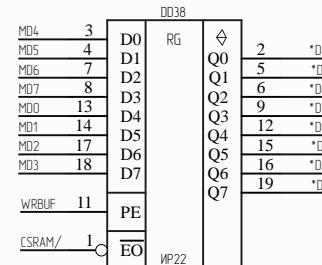
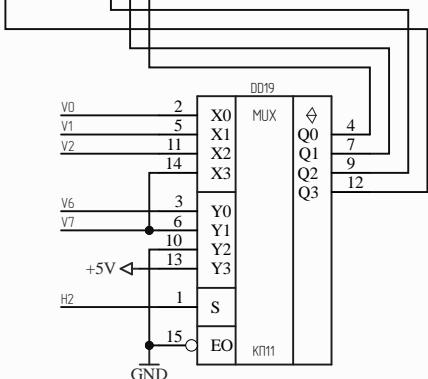
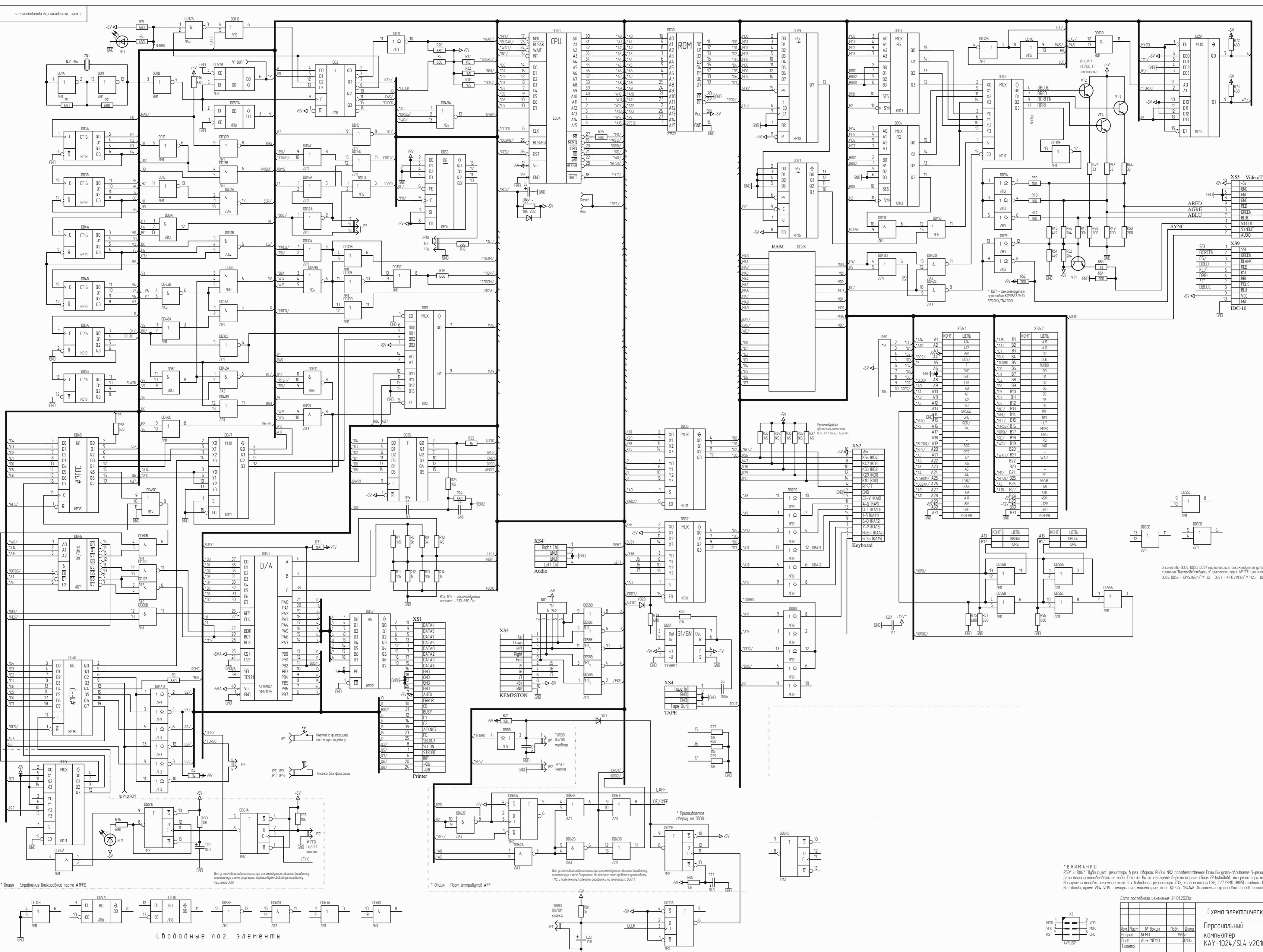


Разъём SIMM30





A

XS7.1	
КОНТ.	ЦЕЛЬ
*A14 A1	A14
*A12 A2	A12
*DOS/+5V A3	+5V
*F A5	DOS/
A6 GND A4	GND
*CLOCK A8	CLK
*AO A9	AO
*A1 A10	A1
*A2 A11	A2
*A3 A12	A3
GND A14	GND
*RDR/ A15	RDR/
*RS A16	RS
A17	-
A18	-
*BUSRQ/A19	BRQ
*RES/ A20	RES
*AT A21	A7
*A6 A22	A6
*A5 A23	A5
*A4 A24	A4
*CSROM/A25	CSR/
*BUASK/A26	BAK
*A9 A27	A9
*A11 A28	A11
+5V A29	+5V
A30	-
+12V A31	+12V
GND	РЕЗРВ

XS7.2	
КОНТ.	ЦЕЛЬ
*A15 B1	A15
*A13 B2	A13
*D7 B3	D7
*BLK B4	BLK
*TURBO B5	TURBO
*D0 B6	D0
*D1 B7	D1
*D2 B8	D2
*D6 B9	D6
*D5 B10	D5
*D3 B11	D3
*D4 B12	D4
*INT/ B13	INT
*NMI/ B14	NMI
*HLT/ B15	HLT
*MREQB16	MREQ
*RDR/ A15	RDR/
*RS A16	RS
A17	-
*RD/ B18	RD
*WR/ B19	WR
B20	-
*WAIT/B21	WAIT
B22	-
B23	-
*M1/ B24	M1
*RES/B25	RES
*A8 B26	A8
*A10 B27	A10
B28	-
+5V A29	+5V
A30	-
+12V A31	+12V
GND	РЕЗРВ
GND	РЕЗРВ

XS8.1	
КОНТ.	ЦЕЛЬ
*A14 A1	A14
*A12 A2	A12
*D7 B3	D7
*DOS/+5V A4	+5V
*F A5	F
GND A6	GND
*CLOCK A8	CLK
*AO A9	AO
*A1 A10	A1
*A2 A11	A2
*A3 A12	A3
GND A14	GND
*RDR/ A15	RDR/
*RS A16	RS
A17	-
*BUSRQ/A19	BRQ
*RES/ A20	RES
*AT A21	A7
*A6 A22	A6
*A5 A23	A5
*A4 A24	A4
*CSROM/A25	CSR/
*BUASK/A26	BAK
*A9 A27	A9
*A11 A28	A11
B29	-
+5V A29	+5V
A30	-
+12V A31	+12V
GND	РЕЗРВ
GND	РЕЗРВ

XS8.2	
КОНТ.	ЦЕЛЬ
*A15 B1	A15
*A13 B2	A13
*D7 B3	D7
*BLK B4	BLK
*TURBO B5	TURBO
*D0 B6	D0
*D1 B7	D1
*D2 B8	D2
*D6 B9	D6
*D5 B10	D5
*D3 B11	D3
*D4 B12	D4
*INT/ B13	INT
*NMI/ B14	NMI
*HLT/ B15	HLT
*MREQB16	MREQ
*RD/ B18	RD
*WR/ B19	WR
B20	-
*WAIT/B21	WAIT
B22	-
B23	-
*M1/ B24	M1
*RFSH/B25	RFSH
*A8 B26	A8
*A10 B27	A10
B28	-
+5V A29	+5V
A30	-
+12V A31	+12V
GND	РЕЗРВ
GND	РЕЗРВ

A

B

C

D

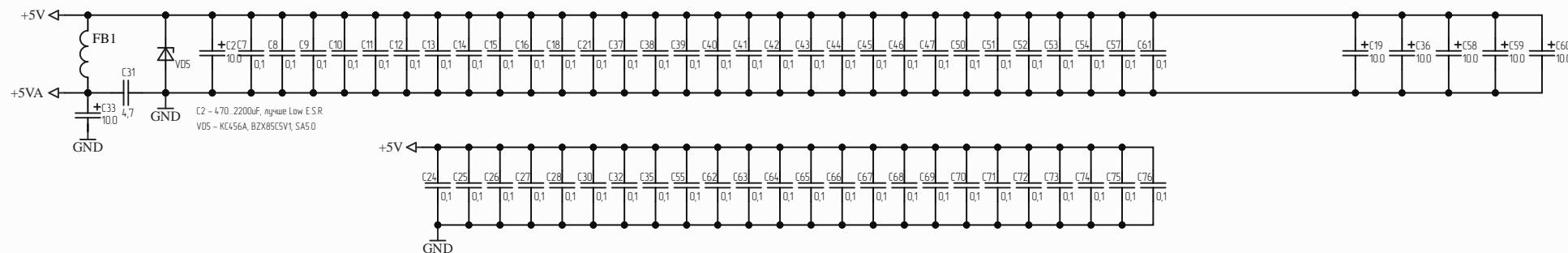
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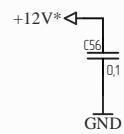
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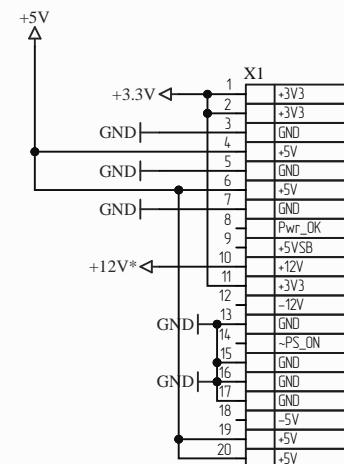
A



B



C



D

1

2

3

4

