1: Init transaction: TsxData	_
1.1: out hmacs	
2: Precompute sub-address: major, minor idx list	\rightarrow
3: Set inputs: input[i]	
3.1: vin[i], hmacs, pseudo_out, alpha	
4: Inputs done	_
5: Set Inputs permutation	
6: Set inputs: input[i], tx.vin[i], pseudo_out[i], hmacs	\rightarrow
7: Set outputs: out[i], hmac	\rightarrow
7.1: vout[i], rsig, out_pk, ecdh_info, hmacs	\rightarrow
8: All out set	
8.1: tx_prefix_hash, tx_extra	\rightarrow
9: Gen RctSig	
9.1: RctSig base	\rightarrow
10: mlsag done	
10.1: final_message hash	\rightarrow
11: Sign: input[i], pseudo_out[i], alpha[i], hmacs	
11.1: Signature for input i	\rightarrow