1.1: out hmacs
2: Precompute sub-address: major, minor idx
3: Set inputs: input[i]
3.1: vin[i], hmacs, pseudo_out, alpha
4: Inputs done
5: Inputs permutation
6: Set inputs: tx.vin[i], hmacs
7: Set outputs: out[i], hmac
7.1: vout[i], rsig, out_pk, ecdh_info, hmacs
8: All out set
8.1: tx_prefix_hash, tx_extra
9: Gen RctSig
9.1: RctSig base
10: Inc hashing: pseudo_out[i], hmac
11: mlsag done
11.1: final_message hash
12: Sign: input[i], pseudo_out[i], alpha[i], hmacs
12.1: Signature for input i
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