

Résumé

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CAREER PATH




ASIC expert with 15+ years of experience and a proven record working with embedded system (Linux/RTOS), and digital and analog circuit design with AI companies in China and USA.

PROFESSIONAL QUALIFICATION

Licensed and Certified Electronic Engineer, China, 2008

WORKING EXPERIENCE

Self-employed, California, USA

Network Security Admin, Website Designer, Feb 2021 - Now    <https://www.isaycakissmyass.com/>

C-Matrixtech CO.LTD., Zhuhai, China

Integrated Circuit Engineer, Dec 2017 - Feb 2019

- * Verilog + FPGA for self-powered and low-power MCU SOC chips for iPhone Lightning cables.
- * Mixed (Analog) Low-power Circuit design.
- * Xilinx autobuild & remote download firmware platform for remote team work.
- * Port C from Assembly for the 13-bits MCU (PIC); MCU debug.

Viomi CO.LTD., Foshan, China

Software (Firmware) Engineer, May 2017 - Oct 2017

- * Buildroot; Linux driver for the WiFi chip; Speech Recognition & Synthesis App for Linux.
- * UART, GPIO, SPI, NAND flash, TCP/UDP/IP, WiFi driver debug.

Blueway CO.LTD., Huizhou, China

Software (Firmware) Engineer, May 2016 - Apr 2017

- * FreeRTOS port to STM32 for AI mowers. Ingenic X1000E USB OnTheGo (OTG).
- * Mathematica to optimize the walking path of the mowers (AI path planning).

Adon CO.LTD., Foshan, China

Software (Firmware) Engineer, Oct 2013 - Dec 2015

- * Linux driver & app for the Homepod (iPhone WiFi speaker/charger).
- * Das U-Boot (Bootloader) porting; Qualcomm AR9331 : WiFi, I2S, UART, SPI, GPIO. OpenWRT.

Skyit Software CO.LTD., Shunde, China

Software Engineer, Dec 2011 - Jul 2013

- * Developed the first meal order App for Android in China; Windows C#; MySQL API by C.

Vbridge Microsystem Inc, San Jose, California, USA.

Digital Integrated Circuit Engineer, Apr 2005 - May 2008

- * IP cores of security engineer (TLS) : AES, DES, 3DES, MD5, SHA1, CRC + 5-bits MCU core.
- * All IP cores tapout in a dual-MIPS SOC Chip. Bootloader porting. NC(Incisive) + Verdi.
- * UDP data package sender/receiver by Altera. OmniVision Image sensor data modified by Altera.
- * Linux/VxWorks board support package(BSP): UART, I2C, SPI, SDIO. OpenOCD for jtag.

EDUCATION

Yuba College, ESL Class, 2021

Evergreen Valley College, ESL Class, 2020

Xi'an University of Technology, China

MSc in Microelectronics & Solid State Electronics, 2005

BSc in Microelectronics Technology, 2002

ADDITIONAL TRAINING AND RELATED SKILLS

Cadence, Layout and verification, March, 2002

Mentor Graphics, Calibre DRC/LVS & xRC Training, April, 2007

Mentor Graphics, Eldo Simulation & ADMS Training, April, 2007

* C, Verilog, Vim, Bash, Awk, Sed, Perl, Golang, Gnumake, Git.

* Linux / FreeRTOS driver; UART, SPI, I2C, I2S, USB OnTheGo, WiFi hostapd/wpa_supplicant.

PERSONAL CHARACTERISTICS

I can work well individually and/or with team members. I have been working as a team member or a team leader. I find satisfactions in seeing projects completed and perfected.

REFERRALS

Dan Teuthorn, Teledyne Microwave, VP Engineer, (408)421-7303, dan.teuthorn@teledyne.com

Tang Shanqiang, Cadence, Principal Application Engineer, +65-9054-6301, ansent@cadence.com

Stephen Fung, Qualcomm, Semiconductor Engineer, (408)338-5175, stephen@thepoint.church