



10840 Guilford Rd, #401-402  
Annapolis Junction, MD 20701

(240) 787-8000  
[www.steer-tech.com](http://www.steer-tech.com)

## **FPGA Firmware Engineer**

STEER Tech develops low speed automated vehicle technologies in the commercial and consumer vehicle space. STEER Tech has several next generation R&D and production projects impacting a wide range of segments that will alter the automated vehicle landscape. At STEER we believe in innovative, high-quality engineering and design, allowing us to develop market leading products and services. In short, we love creating value for our customers by solving difficult problems.

As of today, the STEER has 453 sites for Level 4 automated operations across the United States and interests in EU and Asia.

### **Job Description**

STEER Tech is looking for a talented Firmware Engineer to join its production engineering team. The successful applicant can expect to join a growing development team. While being able to work closely with others they will also need to demonstrate the ability to work independently.

### **Role**

In this challenging Firmware Engineer role, the successful applicant will focus on the design and development of FPGAs and digital modules (VHDL) in a sensor and communications related environment.

- Design, implementation, test, integration and delivery of digital signal processing systems
- Development of VHDL for FPGAs in accordance with defined specs and algorithms
- Authoring of supporting design documentation to a high standard
- Development of test harnesses to verify developed items using self-checking testbenches, BFM's etc.
- Providing cryptographic and cyber security support in hardware including MACSec
- Integration of the developed firmware on the target hardware with other system components and software
- Release of developed items in accordance with specified configuration control procedures
- Provide progress/technical updates to the project manager
- Provide project schedule estimates and technical risk identification
- Contribute to peer design reviews
- Undertake requirements capture
- Ensure that all business activity is conducted in line with company values, policies and ethics codes of conduct.
- Undertake any other activity as reasonably requested by management.
- Provide project schedule estimates and technical risk identification.

### **Qualifications**

#### **Essential Requirements:**

- Degree qualified in electronics/ engineering related subject; however, experience will be considered in lieu of formal academic qualifications
- At least 2 years post graduate experience of FPGA development using VHDL, preferably in the communications or signal processing industry



10840 Guilford Rd, #401-402  
Annapolis Junction, MD 20701

(240) 787-8000  
[www.steer-tech.com](http://www.steer-tech.com)

- Excellent design standards and strong debug skills
- An appreciation of digital hardware design
- Experience of developing self-checking testbenches, BFM's
- Documentation generation: Requirements, design, interface specification, test specifications
- Ability to communicate effectively in English with clients and colleagues (written and oral.)

#### **Desired Criteria:**

- An appreciation of other engineering disciplines; Software, Digital, Cybersecurity, Networking
- Familiarity with digital communications protocols, signal processing algorithms and general networking experience
- Experience of developing on Broadcom, Xilinx, Intel chips
- Experience of ARM System-On-Chip
- Experience of designing for security
- Working knowledge of the following engineering tools; Vivado, FPGA Simulator (Synopsys), Hydra (ARM/DSP), SAMv70, JAMA, Jira, Confluence, CI/CD, Github
- Experience of engineering development in the V model environment

#### **Personal Attributes**

- A good team player capable of delivering results under pressure and to strict deadlines.
- Quality focused with good attention to detail.
- Proactive self-starter who is structured and organized; A completer / finisher.
- A positive "can do" enthusiastic attitude towards addressing the technical challenges
- Demonstrate initiative, communication and interpersonal skills.
- Innovative thinker and problem solver.

STEER Tech is an Equal Opportunity Employer with competitive salaries and benefits. For more details and the latest info visit [www.steer-tech.com](http://www.steer-tech.com) and follow us on:

[LinkedIn](#)

[Twitter](#)

[YouTube](#)

[Instagram](#)

[Facebook](#)