

— Parallel String Matching —

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Abstract

This project explores and implements CPU-parallel using Cilk and GPU parallelism using Cuda for seven different string-matching algorithms and evaluates the performance of each algorithm. Each algorithm was evaluated on three different sets of data: natural language text, genomic data, and program-generated data. The results showed that most algorithms can benefit from CPU parallelism, achieving 3-10x speedup in 16 threads. GPU performs poorly on most files less than 100MB and only has a huge speedup on special data, such as the 1000 repeat letters on the brute force algorithms. Based on the CPU and GPU in the experiment, GPU has a 2x speedup on the KMP algorithms, and 2/3 Cuda cores speedup in the 1000 repeat letters on the brute force algorithms.

1 Introduction

String matching is a fundamental problem in computer science, with applications in fields such as bioinformatics, search engines, and large database searches. While sequence algorithms are effective at solving this problem, their performance can be limited by the linear increase in the size of the matching string, leading to long search times for large datasets. In recent years, there has been a trend towards using modern CPUs with multiple cores and GPUs for data processing, and parallelization has become an important consideration in algorithm design. Given the data structure of string matching, it can potentially be highly amenable to parallel programming, allowing for efficient solutions to large-scale matching problems on large-scale systems.

The goal of this project was to investigate the parallel implementation of traditional sequence string matching algorithms, implementing their parallel versions using Cilk and Cuda, and comparing the benefits of different string matching algorithms in different parallel architectures. We also examined the potential applications and limitations of these algorithms on a distributed architecture. In this project, we implemented seven different string-matching algorithms in CPU parallel and five in GPU. For algorithms that are difficult to parallelize algorithmically, we partitioned the index to perform parallel operations. Some of the algorithms also used bit parallel or SSE instructions to improve performance. We analyzed the speedup obtained by different algorithms and the worst case speedup obtained.

We can choose a more effective algorithm through analysis of pattern and text length, as well as preprocessing. Our experimental results also demonstrate this point. The type of data being used can also impact efficiency, as a genomic sequence with four letters may have more repetitions than natural language text, which can affect the performance of the algorithm. We will compare different datasets to explore the differences and determine the best algorithm.

2 Literature Review

String Matching is an important problem in computer science, and many people continue to study it. The most recent comparison was Philip Pfafe, who discussed 7 parallelized string matching algorithms based on SIMD[1]. The Flynn classification divides computer architectures into four categories, Single Instruction Single Data (SISD), Single Instruction Multiple Data (SIMD), Multiple Instruction Single Data (MISD), and Multiple Instruction Multiple Data, and for different architectures, there have different methods to minimize latency and improve performance. Moreover, the CPU and GPU architectures also have different memory hierarchies, which need to be taken into account when designing string-matching algorithms. This project compares the advantages and disadvantages of different architectures in solving string-matching problems and measures the performance of CPU parallelism and GPU parallelism.

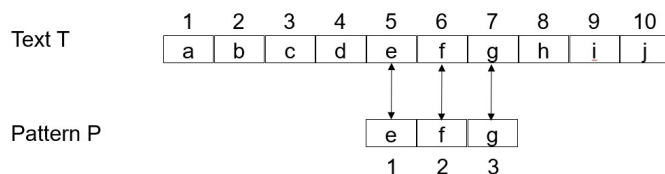


Figure 1: String Matching wants to find the pattern from Text

2.1 Sequential String Matching

String Matching is one of the common fundamental problems, and many algorithms have been proposed to solve it. Faster solutions were proposed as early as 1977, the KMP algorithm and the Boyer–Moore algorithm[1]. Since 1970, more than 80 String Matching algorithms have been proposed. These algorithms use preprocessing techniques to speed up the search process. In addition, algorithms such as the edit distance algorithm and suffix tree can be used to solve more complex string-matching problems. Based on previous research those algorithm deformations, combinations, and expansion, String Matching has been continuously optimized. Now, using parallelized algorithms to improve performance is a more mainstream method[1], by running the traditional algorithm in parallel, the original algorithm can be

accelerated. The specific algorithm is listed in section 4 Implementation.. By exploring the details of these algorithms, the efficiency of parallelized algorithms can be further improved.

2.2 Parallel String Matching

Parallel computing has great potential and extremely high scalability. Making good use of parallel computing can greatly speed up the calculation speed. Although not all algorithms can benefit from parallel computing. Specific to the String Matching algorithm, It can be parallelized from data input, It is also possible to parallelize certain operations. In conclusion, parallel computing has great potential and can be used to speed up the calculation speed of string-matching algorithms. However, depending on the algorithm, the best core utilization can vary and it is important to analyze the speedup achieved in different numbers of threads.

2.2.1 Parallel String Matching base on MISD

Although there exist MISD architectures dedicated to pattern matching, such as Halaas's recursive architecture [6], which can be used to solve string-matching problems, its performance is not competitive compared to other solutions. It can match up to 127 patterns at a clock frequency of 100 MHz, but this is not much better than a 1GHZ single-core CPU. Moreover, MISD processors lack practical applications, and research in this area has been shelved.

2.2.2 Parallel String Matching base on SIMD

SIMD uses one instruction stream to process multiple data streams, which can lead to significant performance improvements compared to executing the same instruction on each data element sequentially. Most Parallel String Matching benefits from SIMD architecture. Such as Chinta Someswararao's Butterfly Model [9], and some string matching algorithms are developed based on the SSE instruction set [1], for example, the SSEF algorithm. which is a SIMD instruction set developed by Intel. Another typical SIMD device is the GPU.

2.2.3 Parallel String Matching base on GPU(CUDA)

Compared with the CPU, modern GPU has huge advantages in parallel computing. A single GPU integrates thousands of computing units, so thousands of parallel computing can be realized on a single GPU. There are also some ways to use CUDA programming provided by Nvidia GPU for String Matching, Giorgos Vasiliadis implements string search and regular expression matching operations for real-time inspection of network packets through the pattern matching library [2], And in 2011 it reached a data volume close to 30 Gbit/s. Efficient GPU algorithms can be 30 times faster than a single CPU core, and Approximate String Matching with k Mismatches reports an 80 times speedup [3]. This makes GPU computing have certain advantages in cost and speed, but the research in this area is not as much as the traditional string algorithm.

2.2.4 Parallel String Matching base on MIMD

MIMD machines can execute multiple instruction streams at the same time. Many modern CPUs belong to this type. In order to fully mobilize multiple instruction streams, targeted

parallel algorithms are inevitable. There are not many studies in this area. Hitoshi developed an algorithm called PZLAST used for MIMD processor PEZY-SC2 and compared the performance of BLAST+, CLAST, and PZLAST algorithms[11], which are specially optimized for the biological field. However, they didn't explore the percentage of parallel utilization of the algorithm.

2.2.5 Distributed Memory Parallel String Matching

There are few articles discussing the String Matching algorithm on Shared and Distributed-Memory Parallel Architectures, except Antonino Tumeo's Aho-Corasick algorithm in 2012 compared and analyzed the distributed memory architecture and shared memory architecture[10]. Results at the time showed that shared-memory architectures based on multiprocessors were the theoretical best performance, but there is an upper limit to the amount of parallelism, at 80 threads he starts to get degraded speedup, and beyond 96 threads the speedup becomes marginal. considering cost constraints, GPUs that did not reach the PCI-Express bandwidth limit had the best price/performance ratio. The performance of GPU has developed several times in the past ten years, and theoretically, its performance is far faster than that of CPU now. Although distributed can provide sufficient space and computing resources, limited by the cost of communication, the performance of distributed computing is not satisfactory. Especially for a single String Matching problem. Nonetheless, this can lead to considerable performance gains for multiple patterns search, as shown by Panagiotis' work[8], In a 16-processor cluster, the matching time can be reduced by half of the original.

3 Problem Statement

A definition of the problem of string matching is to find a pattern P in a text T . We define the pattern as having length p and the text as having length t , with both the pattern and text based on the same alphabet. The output is the positions of every occurrence of P in T . The number of threads used is c . The input is dynamic, and the time cost of preprocessing the pattern or text should be included in the runtime. We are only concerned with finding exact matches, and are not considering the problem of finding approximate matches or regular expression patterns.

4 Implementation

The algorithms explore range from the most basic brute force algorithms, and partial optimization algorithms to modern advanced algorithms using SSE and bit parallelism. In the parallel implementation of the algorithm, except for the Brute Force algorithm, which is a direct parallel loop, the CPU of other algorithms uses the method of index segmentation in parallel, so that each thread is allocated to a string of $(t/c + p - 1)$, thereby realizing parallel operations. The slice algorithm is shown in Algorithm 1. GPU is to limit the index range, so that one thread accesses n string lengths. And try to parallelize the algorithm by allocating a fixed length of string to slice. Some of the algorithms use the SSE instruction set to improve bit parallelism, in which case it cannot be parallelized on the GPU. The following subsections provide a brief overview of the implemented algorithms.

Algorithm 1: Slice text by thread

Data: Pattern, Text, Total Threads

Result: count of the matching number

$i \leftarrow 0;$

while $i \leq (tread - 1)$ **do**

$T \leftarrow Text[i \times \frac{N}{tread}, i \times \frac{N+1}{tread} + N - 1]$
 $Algorithm(Pattern, T); i++;$

end

4.1 Brute Force

The basic Brute Force algorithm compares the pattern to the text, one character at a time until unmatching characters are found. Brute Force means the worst algorithm in most problems. But that's not exactly the case in String Matching Problem. In the average case, Brute Force takes $O(P + T)$ steps. The result reflected in the experiment is that in some natural language searches, the Brute Force algorithm is faster than some algorithms. But there's no way to avoid bad worst-case outcomes. In the worst case, the Brute Force algorithm requires $(p \cdot t)$ steps, which is unacceptable in large searches. Many algorithms benefit from the pattern to avoid the worst case but brute force algorithms do not.

4.2 KMP algorithm

KMP algorithm, maybe the first optimization algorithm[12] for string match algorithm, KMP calculates the number of times it can skip each time it reads a string, It uses a preprocessing phase on the pattern to build a partial match table. Although this avoids the worst result, In theory, KMP has a time cost of $O(t)$, but due to the calculations at the same time, KMP can't speed up too much. Excellent performance in some cases, but doesn't take high scores in most cases. relatively, due to more calculations, it gets more benefit from parallelism.

4.3 Boyer-Moore

Boyer-Moore algorithm calculates the bad character rule and the good suffix rule[14], matches from the last index to the front, and skips unmatch string by rule. This algorithm takes advantage of the fact that most strings don't match the pattern, and skips large portions of the string without checking. Thus exceed linear time in algorithm analysis for most natural languages and large pattern capacity matching problems with better speedup than KMP. it has (p) Preprocessing time and (t/p) at best, $O(p \cdot t)$ at the worst Matching time, and requires (p) space to store the good and bad rule.

4.4 Rabin-Karp

The Rabin-Karp algorithm gets inspiration from the hash function. When scanning strings for comparison, it uses the hash value generated by Rolling hash comparison instead of directly comparing strings[13]. This avoids directly scanning the entire pattern. Only strings with the same hash can be compared. A full scan will be performed. Because the hash is fixed,

the speed of the algorithm depends on the hash function and the string alphabet. The worst result is still mn , but as a heuristic algorithm, it is faster than the expected value of the brute force algorithm.

4.5 Shift-Or

The Shift Or algorithm uses bitwise techniques[1], it keeps an array of bits, R , showing if prefixes of the pattern don't match at the current place. Before searching, mismatch arrays are computed for each character in the alphabet and saved in an array, S . For the next position, with the character c , $R = \text{shift}(R) \text{ or } S[c]$. If the last bit of R is 0, the pattern matches. The runtime is deterministic and in $O(n*m)$. Nevertheless, it is a fast operation due to bit parallelism.

4.6 SSEF

The SSEF algorithm precomputes 65536 filter lists based on the k th bit of each character on the patterns[1]. These filters are then applied efficiently, utilizing SSE instructions, on shifting alignments of pattern and text. SSEF is restricted to patterns with a minimum length of m greater than 32. The worst-case runtime is in $O(n*m)$. If consider the probability to filter possible matches, SSEF achieves an average runtime in $O(n*m/65536)$. The SSEF algorithm doesn't exactly cover all implementations, but it's a good comparison since Philip Pfafe claims it has the best performance on their evaluation[15].

4.7 Exact-Packed-String-Matching

Exact-Packed-String-Matching (EPSM) was presented by Faro and Külekci in 2013[16]. EPSM packing several characters into a bit-word based on the Intel streaming SIMD extensions and compared with a packed pattern bit-word. it uses compare idea like shift or algorithm, shift and bitwise-and operations are used to efficiently compare text chunks with the pattern. The asymptotic runtime of the algorithm is $O(p*t)$, but in practice very fast.

5 Experimental Evaluation

In this section, I present the performance evaluation of parallel string matching algorithms. The sequential implementation of some codes comes from String Matching Algorithms Research Tool, then modified to accommodate multi-threading requirements.

Here are the datasets I used for comparison:

- A sample of natural language (the text of the Bible, 3.1MB)

- A set of genome sequences (consisting of four letters alphabet A,T,C,G, 98MB)

- Extreme data generated by some programs (consisting only two letters alphabet a,b and only 1 b,1GB)

In order to evaluate the benefits of parallelization, the input file is directly read into memory completely to reduce I/O latencies, and then my algorithm is run on a number of 1, 2, 4, 8, 16, 32 threads, and their respective running times are recorded. And test patterns with

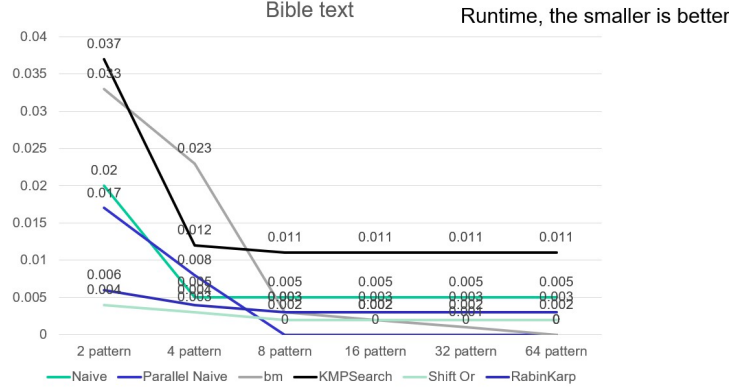


Figure 2: Runtime of seven algorithms for 1,2,4, 8, 16, 32, and 64 patterns. natural language text (bible).

lengths of 2, 4, 8, 16, 32, 64 and 1000.

All CPU parallel experiments were performed on AMD ryzen9 3950x and compiled by open-cilk, link sse4.2. The CPU has 16 CPU cores (32 hardware threads) clocked at 4.1GHz(overclock).

The GPU is based on nVidia RTX 2070 8GB running on the latest CUDA 22.04.

In the following subsection, I discuss an excerpt of my result data.

5.1 Pattern

This is about the impact of patterns on parallel performance. When I want to analyze the benefits of threads, I want to find a relatively stable pattern as an input. Figure 2 3 4 is the test result, in the test, it is indeed affected Very short pattern (pattern less than 8) does have a certain impact on the algorithm, but when a pattern greater than 8, only the Boyer-Moore algorithm benefits from it, and the impact on other algorithms is marginal.

However, in the worst case, brute force and EPSM increase the running time significantly with the increase of patterns, while the KMP algorithm remains stable. This is due to the fact that EPSM partitions the text T into blocks to use bit parallelism compared to packed mode bit words. Due to the characteristics of memory comparison, the length of each comparison using bit parallelism is fixed, and the advantages cannot be utilized for large patterns., a disadvantage of using bit parallelism. However, KMP does not increase the running time because it can jump enough distance each time. Of course, in this case, the running time of the brute force algorithm reached 15 minutes, which is why cannot directly use the brute force algorithm, the running time, in this case, is unacceptable.

5.2 Thread

I focused on the impact of threads on the performance of different algorithms and the resulting speedup ratios. Figure 4 shows the variation in running time of the matching algorithm on the

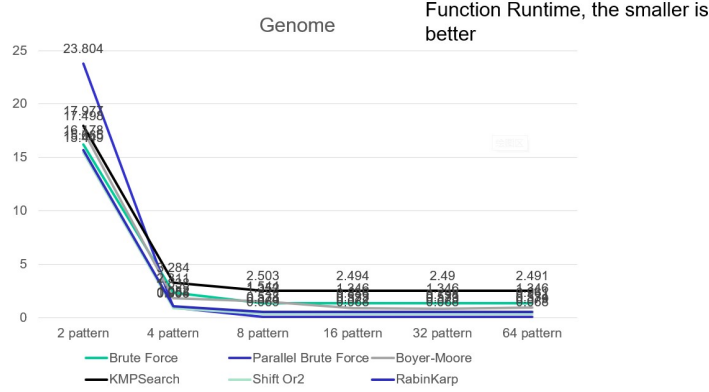


Figure 3: Runtime of seven algorithms for 1,2,4, 8, 16, 32, and 64 patterns. genome data set.

Bible text under different numbers of threads. Most algorithms show a speedup under multi-core, except for EPSM. However, the parallel version of EPSM has a faster running time than the sequential version. The running speed on CUDA for some algorithms is slightly better than the sequential version, but it is difficult to exceed the parallel speedup of 16 threads. Figure 5 shows the data for the genome file, where the nature of the genes leads to a large number of repeats in the small dataset ATCG. This causes an increase in the running time of the brute force algorithm, while the other algorithms remain stable. Figure 6 shows the poor quality data being generated. In this case, the brute force algorithm takes 15 minutes, while most algorithms take only seconds, with Shift-Or and KR being the fastest at less than a second. In this case, we see a significant speedup, with all algorithms achieving a speedup of more than 5x in a 32-thread parallel. The time spent on Cuda is much faster than that of CPU parallel in this case, with the brute force algorithm taking only 0.3 seconds, a 1500x speedup. This speedup ratio is much higher than that of all sequential algorithms, indicating that under normal text, the GPU is unable to fully utilize its performance.

5.3 Speed up

Analyzing which algorithm has a good speedup is the main purpose of this project. Figure 8 9 10 show the speedup ratios achieved in different types of data, respectively. Figure 8 shows the speedup ratio in general text, the speed-up ratio is not high. On average, the speedup ratio can only reach less than half of the number of cores. When there are 32 threads, the performance of the RabinKrap algorithm even drops slightly, while the KMP algorithm achieves a good speedup ratio. And the brute force algorithm speeds up poorly, only achieving 2.3X speedup in 32 threads

Figure 9 shows the speedup ratio on the gene. Except for EPSM, the algorithms have achieved good speedup ratios, but EPSM has brought delays. KMP algorithm still achieved good speedup ratios, even 14X speedup ratio in 32 threads. Brute Force, SSEF, and Boyer-Moore have a speedup of 6-8X, while Shift-Or and Rabin-Karp are 4 and 5 times respectively.

In the worst text shows in Figure 10, the brute force algorithm has achieved a good speedup

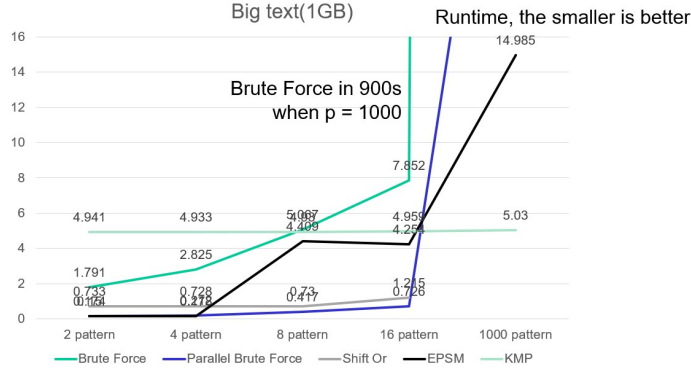


Figure 4: Runtime of five algorithms for 1,2,4, 8, 16, and 1000 patterns.complex generated data set.

ratio, which is not without cost, and the time spent dropping from 10 minutes to 40 seconds is still unacceptable. In contrast, the speedup achieved by KMP and RabinKrap is not high, but it is far more than other algorithms, KMP even came to third place in speed in this case.

6 Conclusions

"I implemented parallel versions of six different string-matching algorithms by slicing the index according to the number of threads, and compared their performance to the version on the GPU on different types and sizes of datasets. Most algorithms benefited from CPU parallelism, except for EPSM, with a 3-10x speedup observed in 16 threads. The brute force algorithms showed the greatest benefit from parallelism, with parallel brute force algorithms performing best on most natural texts, followed by EPSM. For large patterns, Rabin-Karp had the best performance. While EPSM had the best performance in genetics and generated text in a sequential setting, it did not benefit from parallelism except in the worst case. In the worst case, the GPU-parallelized brute force algorithm had a speedup matching the number of Cuda cores, and an average 4x speedup on normal text. In the general case, the algorithm had a 2x speedup on the GPU. I also observed the effect of the pattern size on the performance of the string-matching algorithms. For short patterns, EPSM had far superior performance to its parallel version, with only the worst case seeing a speedup from parallelism. However, for 1000 patterns, EPSM was outperformed by other algorithms. This is because EPSM using bit parallelism only needs one instruction to compare within the range allowed by the memory, and the excess part still needs to be searched through a loop. In this case, Rabin-Karp and GPU-parallelized brute force algorithms were the clear winners.

Therefore, by analyzing the pattern, it is possible to select a better method for the string matching problem. In the future, I plan to further analyze patterns and provide an optimal solution by choosing the most appropriate method for string comparison."

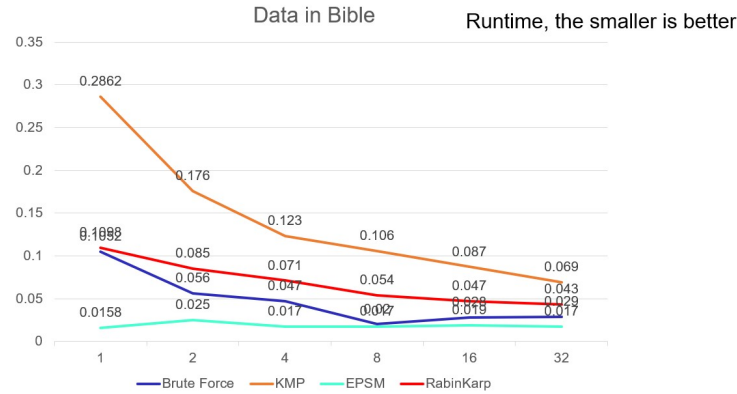


Figure 5: Runtime of four algorithms for 1,2,4, 8, 16, and 32 threads. natural language text, pattern length of 32.

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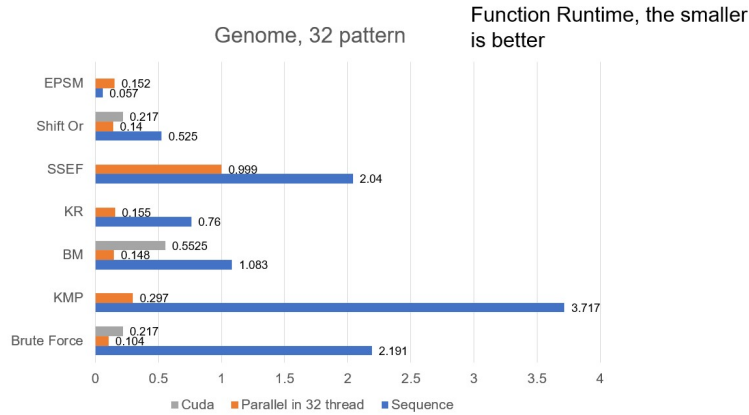


Figure 6: Runtime for 1 and 32 threads and gpu. Genome data set, pattern length of 32.

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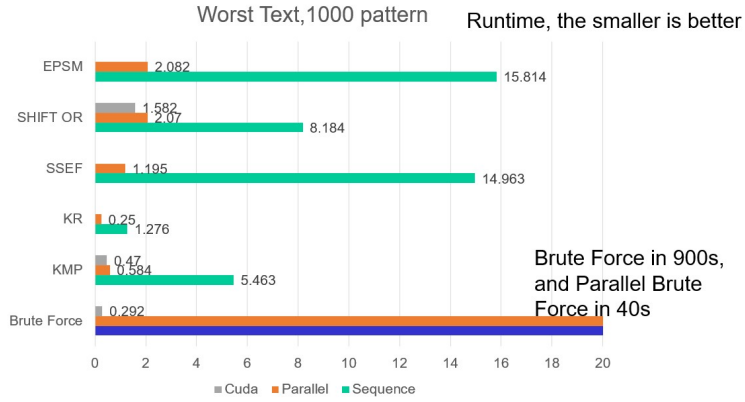


Figure 7: Runtime of seven algorithms for 1, 32 threads and gpu. complex generated data set, pattern length of 1000.

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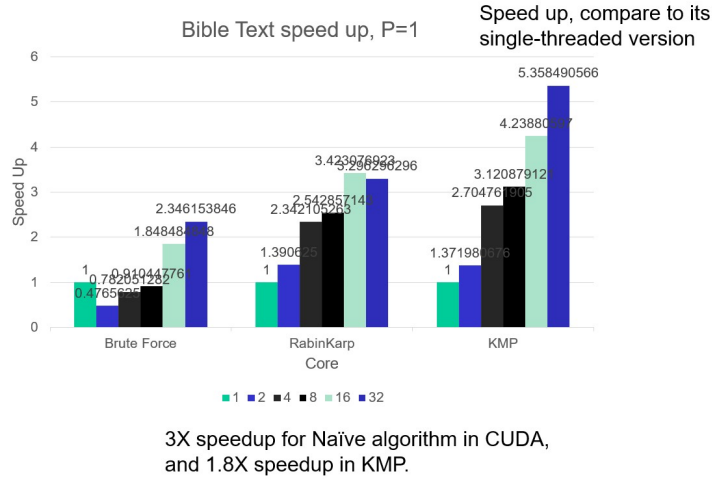


Figure 8: Speedup of seven algorithms for 1,2,4, 8, 16, and 32 threads. natural language text, pattern length of 1.

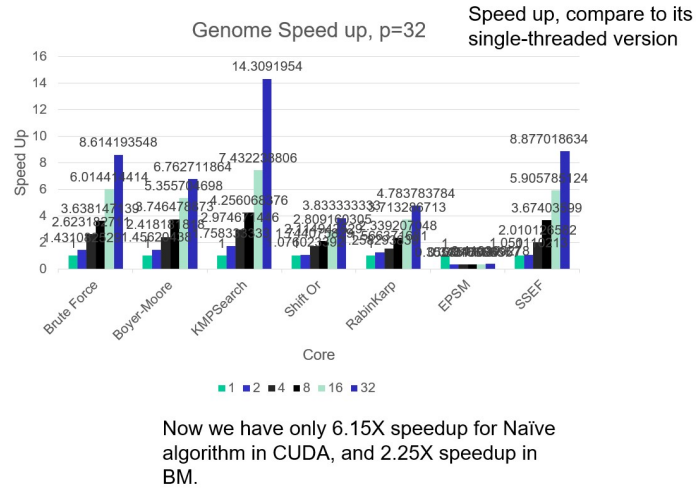


Figure 9: Speedup of seven algorithms for 1,2,4, 8, 16, and 32 threads. Genome data set, complex generated data sett, pattern length of 32.

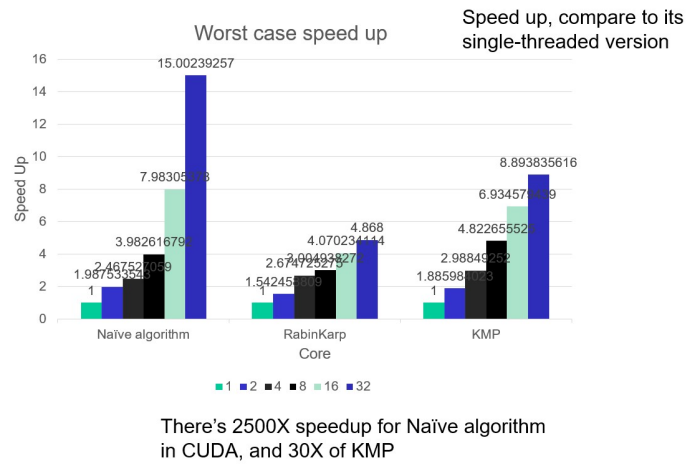


Figure 10: Speedup of seven algorithms for 1,2,4, 8, 16, and 32 threads. Genome data set, pattern length of 32.

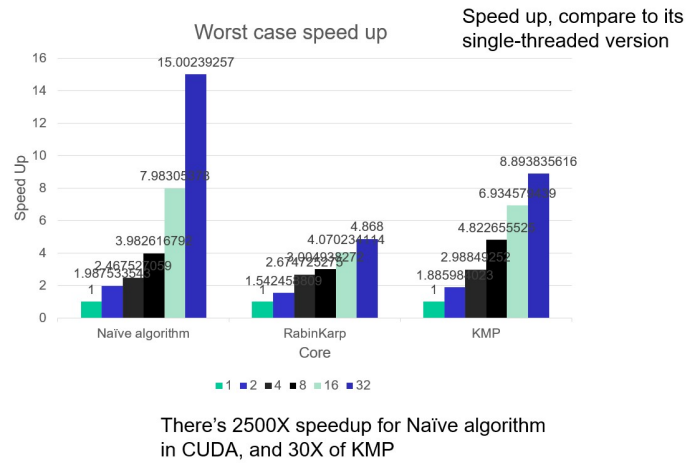


Figure 11: Speedup of seven algorithms for 1,2,4, 8, 16, and 32 threads. Genome data set, pattern length of 32.

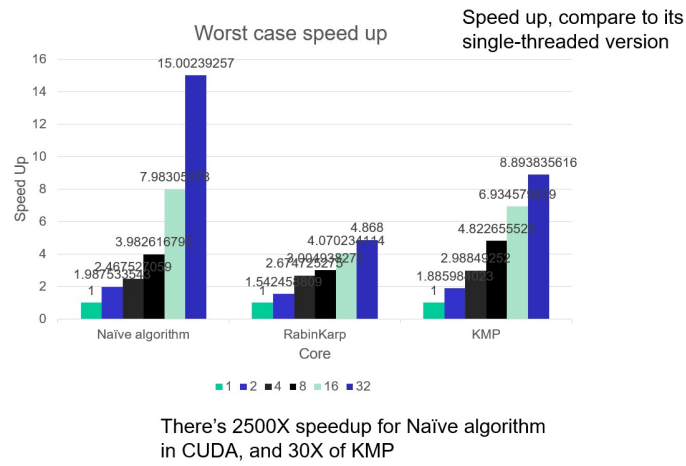


Figure 12: Speedup of seven algorithms for 1,2,4, 8, 16, and 32 threads. Genome data set, pattern length of 32.

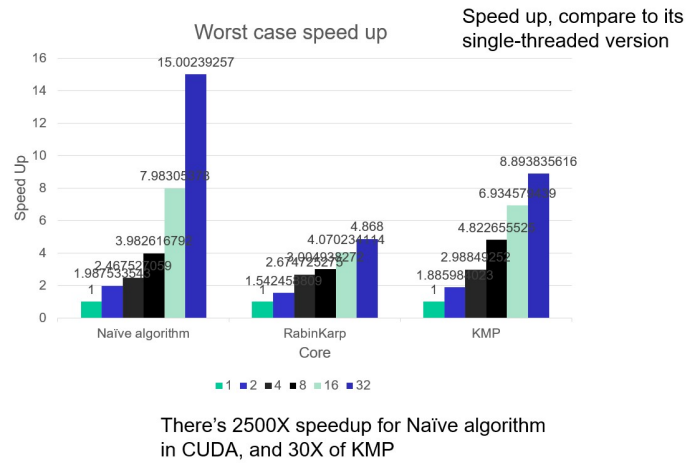


Figure 13: Speedup of seven algorithms for 1,2,4, 8, 16, and 32 threads. Genome data set, pattern length of 32.