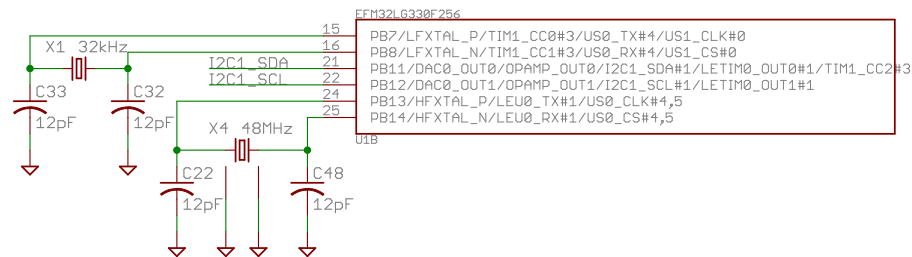
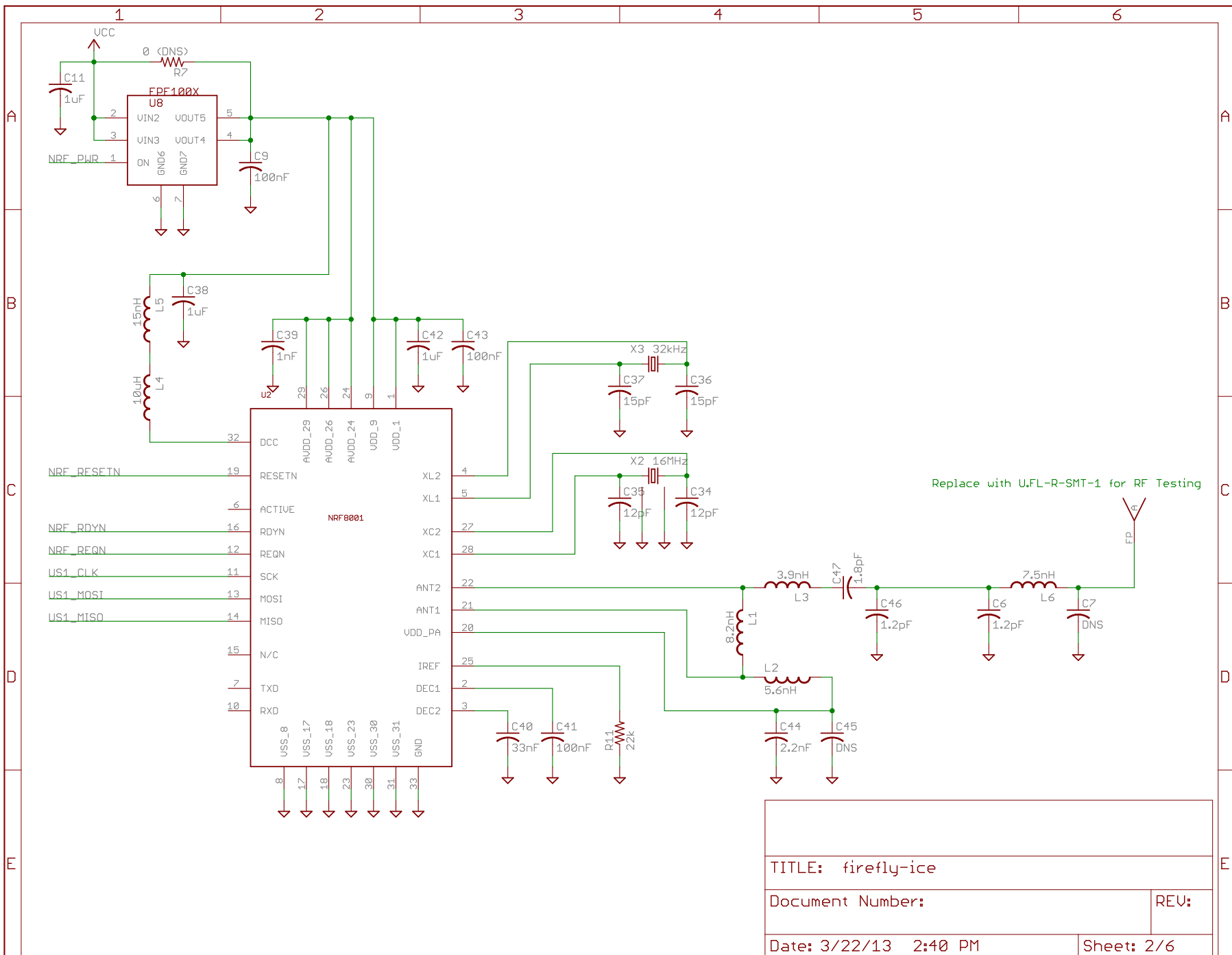
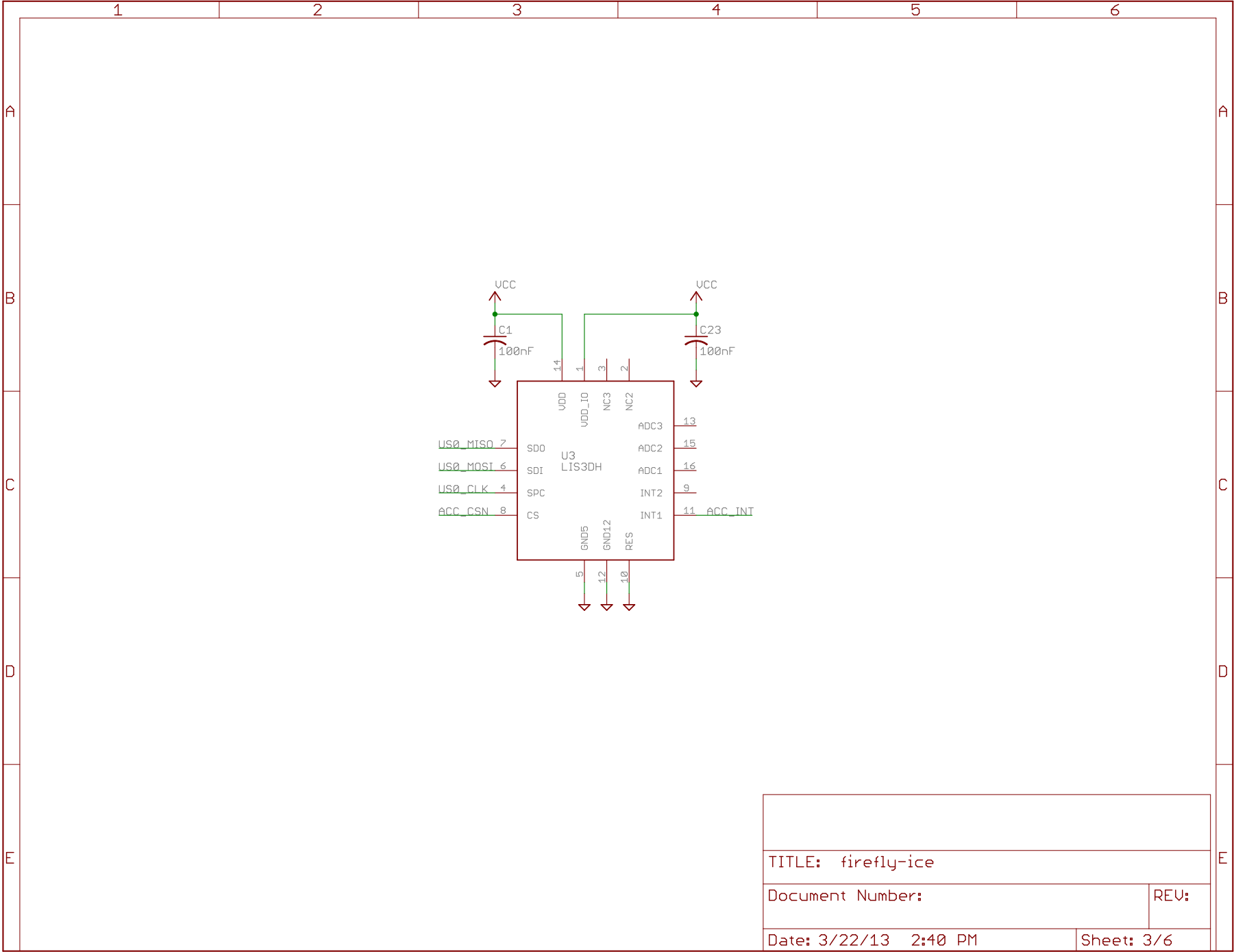


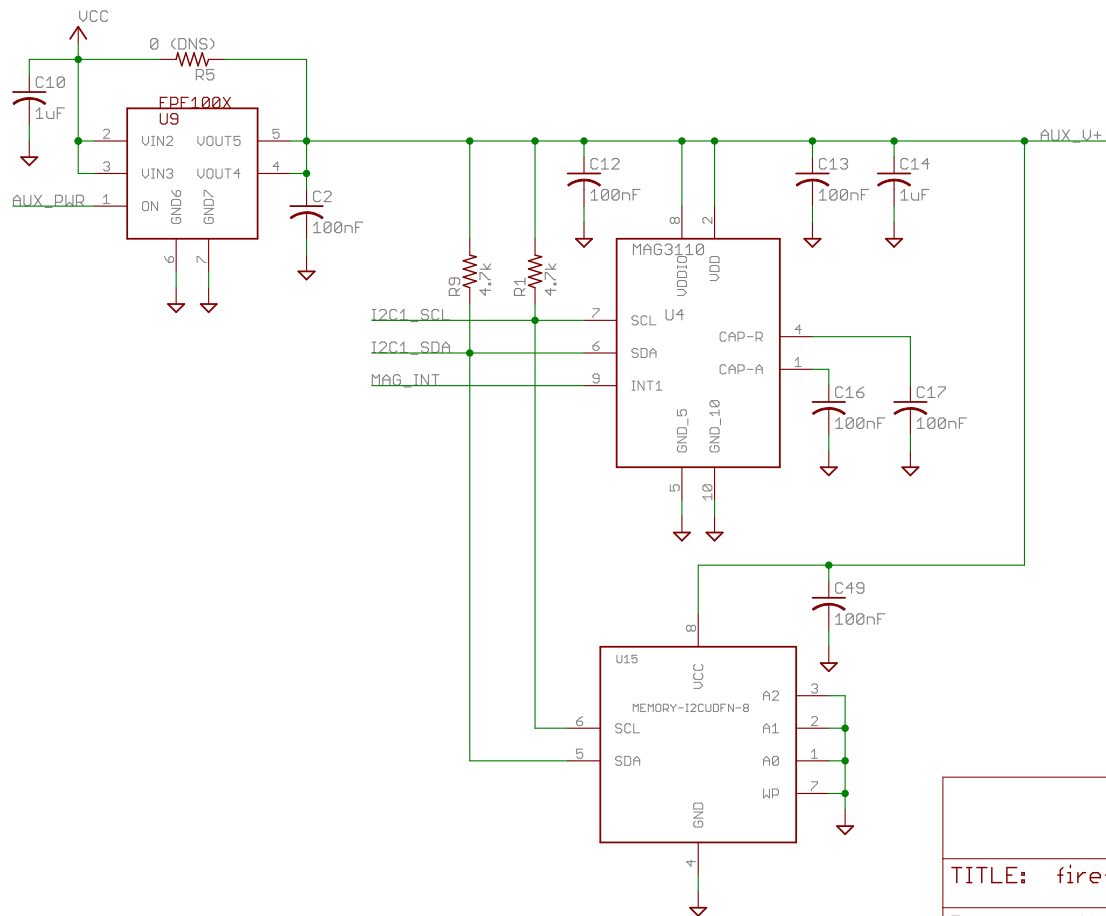
US1_MQSI	28	PD0/ADC0_CH0/DAC0_OUT0ALT/OPAMP_OUT0ALT#4/DAC0_OUT2/OPAMP_OUT2#1/PCNT2_S0IN#0/US1_TX#1
US1_MISO	29	PD1/ADC0_CH1/DAC0_OUT1ALT/OPAMP_OUT1ALT#4/DBG_SW0#2/PCNT2_S1IN#0/TIM0_CC0#3/US1_RX#1
US1_CLK	30	PD2/ADC0_CH2/DBG_SW0#3/TIM0_CC1#3/US1_CLK#1/USB_DMPU
NRF_P0EN	31	PD3/ADC0_CH3/DAC0_N2/OPAMP_N2/ETH_TD1#0,2/TIM0_CC2#3/US1_CS#1
NRF_P0YN	32	PD4/ADC0_CH4/DAC0_P2/OPAMP_P2/ETH_TD2#0,2/LEU0_TX#0
NRF_RESETN	33	PD5/ADC0_CH5/DAC0_OUT2/OPAMP_OUT2#0/ETH_TD3#0,2/LEU0_RX#0
BAT_U/2	34	PD6/ACMP0_O#2/ADC0_CH6/DAC0_P1/OPAMP_P1/ETH_TD0#0/I2C0_SDA#1/LES_ALTEX0/LETIM0_OUT0#0/PCNT0_S0IN#3/TIM1_CC0#4/US1_RX#2
CHG_RATF	35	PD7/ACMP1_O#2/ADC0_CH7/CMU_CLK0#2/DAC0_N1/OPAMP_N1/ETH_TCLK#0/I2C0_SCL#1/LES_ALTEX1/LETIM0_OUT1#0/PCNT0_S1IN#3/TIM1_CC1#4/US1_TX#2
ACC_INT	36	PD8/BU_VIN/CMU_CLK1#1



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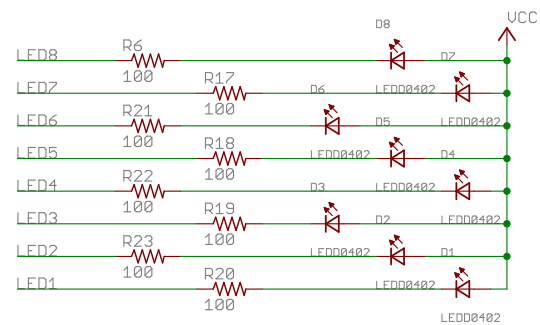
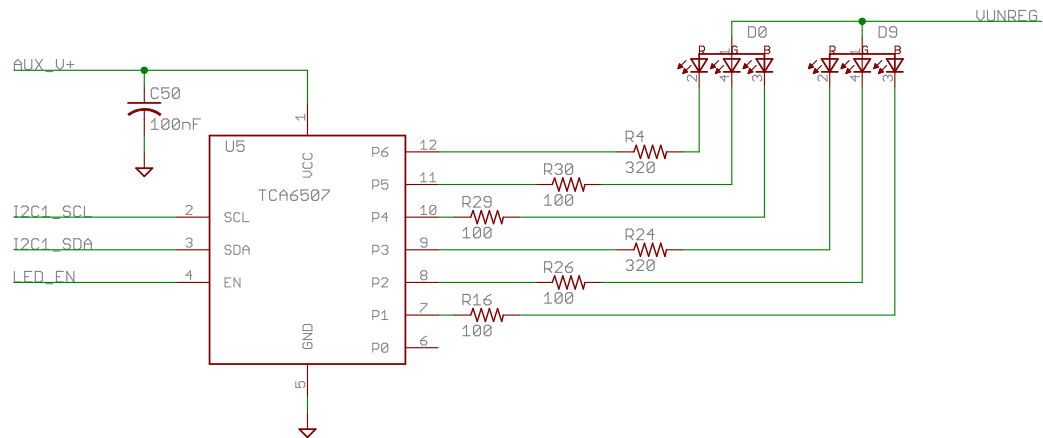
TITLE: firefly-ice

Document Number:

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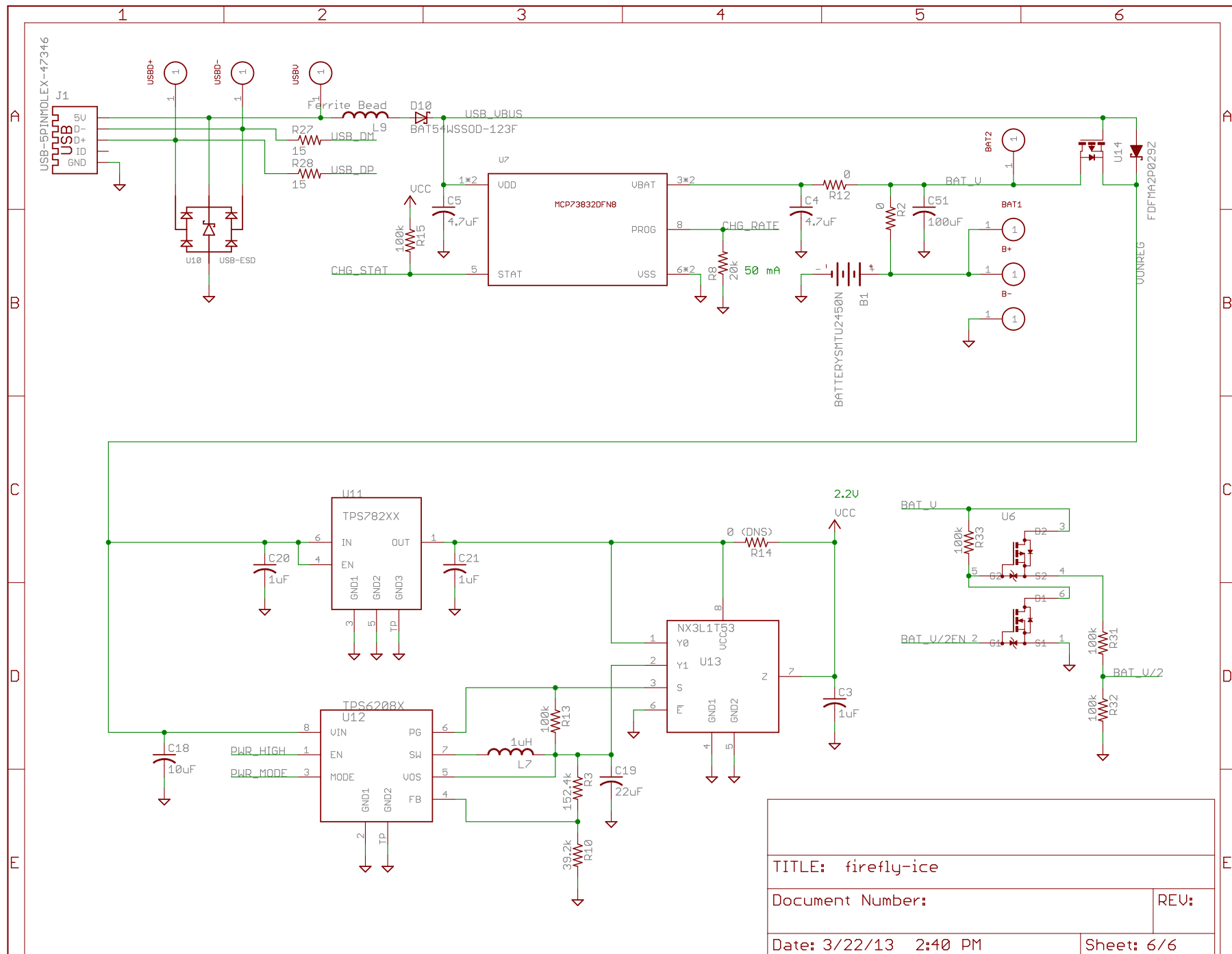
TITLE: firefly-ice

Document Number:

REV:

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