

Q1) Optimize the following using K-map

a) (10)  $f(A,B,C,D) = A'B' + B'C + AC' + AD + ACD$

b) (10)  $f(X,Y,Z,T) = \sum(0, 1, 2, 3, 4, 6, 8, 10, 12, 14)$

c) (10)  $f(X,Y,Z,T) = \prod(0, 1, 2, 3, 4, 6, 8, 10, 12, 14)$

a)

$\overline{A}\overline{B}$	00	01	11	10
00	1	1	1	1
01	0	0	0	0
11	1	1	1	0
10	1	1	1	1

$A'B' \rightarrow$ 

00	00
00	01
00	10
00	11

 $A\overline{C}' \rightarrow$ 

10	00
10	01
11	00
11	01

 $ACD \rightarrow$ 

10	11
11	11

$B'C \rightarrow$ 

00	10
00	11
10	10
10	11

 $AD \rightarrow$ 

10	01
10	11
11	01
11	11

$$= B' + AC' + AD$$

$$= B' + A(C' + D) \quad \text{ad-hoc}$$

b)

$\overline{X}\overline{Y}$	00	01	11	10
00	1	1	1	1
01	1			1
11	1			1
10	1			1

$$= T' + X'Y'$$

c)  $f(x,y,z,t) = \prod(0, 1, 2, 3, 4, 6, 8, 10, 12, 14)$

$$= \sum(5, 7, 9, 11, 13, 15)$$

$\overline{X}\overline{Y}$	00	01	11	10
00	0	0	0	0
01	0	1	1	0
11	0	1	1	0
10	0	1	1	0

$$= YT + XT$$

$$= T(X+Y)$$

Q2) (20) Design a circuit with the following definition, using K-map approach:

- Input A: 3-bit unsigned number ( $0 \leq A \leq 5$ )
- Input B: 1-bit value
- Output C: 3-bit unsigned number ( $0 \leq C \leq 5$ )

If  $B = 1$ ,  $C = (A + 2) \% 6$

else,  $C = (A - 2) \% 6$

	$A_2$	$A_1$	$A_0$	B	$C_2$	$C_1$	$C_0$
0	0	0	0	0	1	0	0
	0	0	0	1	0	1	0
1	0	0	1	0	1	0	1
	0	0	1	1	0	1	1
2	0	1	0	0	0	0	0
	0	1	0	1	1	0	0
3	0	1	1	0	0	0	1
	0	1	1	1	1	0	1
4	1	0	0	0	0	1	0
	1	0	0	1	0	0	0
5	1	0	1	0	0	1	1
	1	0	1	1	0	0	1
6	1	1	0	0	X	X	X
	1	1	0	1	X	X	X
7	1	1	1	0	X	X	X
	1	1	1	1	X	X	X

K-map for  $C_2$ :

$A_2 \backslash A_1 B$	00	01	11	10
00	1			1
01		1	1	
11	X	X	X	X
10	0	0	0	0

$$= A_2'A_1'B' + A_1B$$

K-map for  $C_1$ :

$A_2 \backslash A_1 B$	00	01	11	10
00	0	1	1	0
01	0	0	0	0
11	X	X	X	X
10	1	0	0	1

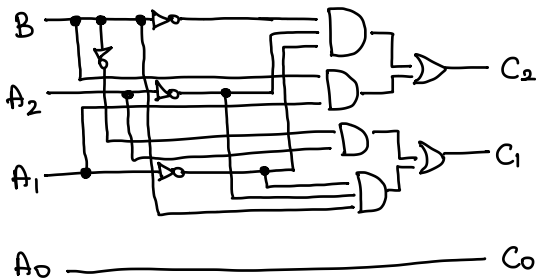
$$= A_2B' + A_2'A_1'B$$

K-map for  $C_0$ :

$A_2 \backslash A_1 B$	00	01	11	10
00	0	0	1	1
01	0	0	1	1
11	X	X	X	X
10	0	0	1	1

$$= A_0$$

Circuit:



Q3) (50)

Design a 2-bit signed/unsigned adder/subtractor circuit. Circuit will have a signed\_unsigned input pin to determine the signed/unsigned operation and an adder\_subtractor input to determine adder/subtractor operation. Draw the circuit diagram. Use the K-map approach, using only 4-value K-maps.

I assumed

$S/U = 0 \rightarrow$  signed

$S/U = 1 \rightarrow$  unsigned

$A/S = 0 \rightarrow$  adder

$A/S = 1 \rightarrow$  subtractor

$A_1$	$A_0$	$B_1$	$B_0$	$S/U=0$	$A/S=0$	$S/U=0$	$A/S=1$	$S/U=1$	$A/S=0$	$S/U=1$	$A/S=1$
0	0	0	0	0	0	0	0	0	0	0	0
0	0	0	1	0	1	1	1	0	1	1	1
0	0	1	0	1	0	1	0	1	0	1	0
0	0	1	1	1	1	0	1	1	1	0	1

signed subtractor

$$\begin{array}{r}
 00 \\
 -01 \\
 \hline
 00 \\
 +11 \\
 \hline
 11
 \end{array}
 \quad
 \begin{array}{r}
 00 \\
 -10 \\
 \hline
 00 \\
 +10 \\
 \hline
 10
 \end{array}
 \quad
 \begin{array}{r}
 00 \\
 -11 \\
 \hline
 00 \\
 +01 \\
 \hline
 01
 \end{array}
 \quad
 \begin{array}{r}
 00 \\
 -00 \\
 \hline
 00 \\
 +00 \\
 \hline
 00
 \end{array}$$

unsigned subtractor

$$\begin{array}{r}
 00 \\
 -01 \\
 \hline
 11
 \end{array}
 \quad
 \begin{array}{r}
 00 \\
 -10 \\
 \hline
 10
 \end{array}
 \quad
 \begin{array}{r}
 00 \\
 -11 \\
 \hline
 01
 \end{array}$$

$A_1$	$A_0$	$B_1$	$B_0$	$S/U=0$	$A/S=0$	$S/U=0$	$A/S=1$	$S/U=1$	$A/S=0$	$S/U=1$	$A/S=1$
0	1	0	0	0	1	0	1	0	1	0	1
0	1	0	1	1	0	0	0	1	0	0	0
0	1	1	0	1	1	1	1	1	1	1	1
0	1	1	1	0	0	1	0	0	0	2 0 1	1

$$\begin{array}{r}
 01 \\
 +00 \\
 \hline
 01
 \end{array}
 \quad
 \begin{array}{r}
 01 \\
 +01 \\
 \hline
 10
 \end{array}
 \quad
 \begin{array}{r}
 01 \\
 +10 \\
 \hline
 11
 \end{array}
 \quad
 \begin{array}{r}
 01 \\
 +11 \\
 \hline
 00
 \end{array}$$
  

$$\begin{array}{r}
 01 \\
 -00 \\
 \hline
 01
 \end{array}
 \quad
 \begin{array}{r}
 01 \\
 -01 \\
 \hline
 00
 \end{array}$$
  

$$\begin{array}{r}
 01 \\
 -10 \\
 \hline
 01 \\
 +10 \\
 \hline
 11
 \end{array}
 \quad
 \begin{array}{r}
 01 \\
 -11 \\
 \hline
 01 \\
 +01 \\
 \hline
 10
 \end{array}$$
  

$$\begin{array}{r}
 01 \\
 +01 \\
 \hline
 10
 \end{array}
 \quad
 \begin{array}{r}
 01 \\
 +10 \\
 \hline
 11
 \end{array}$$
  

$$\begin{array}{r}
 01 \\
 -10 \\
 \hline
 2 0 1 \\
 -11 \\
 \hline
 10
 \end{array}$$

A <sub>1</sub>	A <sub>0</sub>	B <sub>1</sub>	B <sub>0</sub>	S/U=0	A/S=0
1	0	0	0	1	0
1	0	0	1	1	1
1	0	1	0	0	0
1	0	1	1	0	1

S/U=0	A/S=0
1	0
1	1
0	0
0	1

S/U=0	A/S=1
1	0
0	1
0	0
1	1

S/U=1	A/S=0
1	0
1	1
0	0
0	1

S/U=1	A/S=1
1	0
0	1
0	0
1	1

$$\begin{array}{r} 10 \\ + 10 \\ \hline *00 \end{array}$$

$$\begin{array}{r} 10 \\ + 11 \\ \hline *01 \end{array}$$

$$\begin{array}{r} 10 \\ - 01 \\ \hline 10 \\ + 11 \\ \hline *01 \end{array}$$

$$\begin{array}{r} 10 \\ - 10 \\ \hline 01 \\ + 10 \\ \hline *00 \end{array}$$

$$\begin{array}{r} 10 \\ - 11 \\ \hline 10 \\ + 01 \\ \hline 11 \end{array}$$

$$\begin{array}{r} 10 \\ - 00 \\ \hline 10 \end{array}$$

$$\begin{array}{r} 10 \\ - 01 \\ \hline 01 \end{array}$$

$$\begin{array}{r} 10 \\ - 10 \\ \hline 00 \end{array}$$

$$\begin{array}{r} 10 \\ - 11 \\ \hline 11 \end{array}$$

A <sub>1</sub>	A <sub>0</sub>	B <sub>1</sub>	B <sub>0</sub>	S/U=0	A/S=0
1	1	0	0	1	1
1	1	0	1	0	0
1	1	1	0	0	1
1	1	1	1	1	0

S/U=0	A/S=0
1	1
0	0
0	1
1	0

S/U=0	A/S=1
1	1
1	0
0	1
0	0

S/U=1	A/S=0
1	1
0	0
0	1
1	0

S/U=1	A/S=1
1	1
1	0
0	1
0	0

$$\begin{array}{r} 11 \\ + 01 \\ \hline *00 \end{array}$$

$$\begin{array}{r} 11 \\ + 10 \\ \hline *01 \end{array}$$

$$\begin{array}{r} 11 \\ + 11 \\ \hline *10 \end{array}$$

$$\begin{array}{r} 11 \\ - 01 \\ \hline 11 \\ + 11 \\ \hline 010 \end{array}$$

$$\begin{array}{r} 11 \\ - 10 \\ \hline 01 \\ + 11 \\ \hline *01 \end{array}$$

$$\begin{array}{r} 11 \\ - 11 \\ \hline 00 \\ + 01 \\ \hline *00 \end{array}$$

$$\begin{array}{r} 11 \\ - 11 \\ \hline 00 \end{array}$$

$$\begin{array}{r} 11 \\ - 01 \\ \hline 10 \end{array}$$

$$\begin{array}{r} 11 \\ - 10 \\ \hline 01 \end{array}$$

Kmaps for signed addition & unsigned addition (same results)

$C_1$ :

$A_1 A_0 \backslash B_1 B_0$	00	01	11	10
00	0	0	1	1
01	0	1	0	1
11	1	0	1	0
10	1	1	0	0

$$= \underline{A_1' A_0' B_1} + \underline{A_1' B_1 B_0'} + \underline{A_1' A_0 B_1' B_0'} + \underline{A_1 B_1' B_0'} + \underline{A_1 A_0' B_1'} + A_1 A_0 B_1 B_0$$

$C_0$ :

$A_1 A_0 \backslash B_1 B_0$	00	01	11	10
00	0	1	1	0
01	1	0	0	1
11	1	0	0	1
10	0	1	1	0

$$= A_0' B_0 + A_0 B_0' = A_0 \oplus B_0$$

Kmaps for signed & unsigned subtraction:

$C_1$ :

$A_1 A_0 \backslash B_1 B_0$	00	01	11	10
00	0	1	0	1
01	0	0	1	1
11	1	1	0	0
10	1	0	1	0

$$= A_1' A_0' B_1' B_0 + A_1' B_1 B_0' + A_1' A_0 B_1 + A_1 A_0 B_1' + A_1 B_1' B_0' + A_1 A_0' B_1 B_0$$

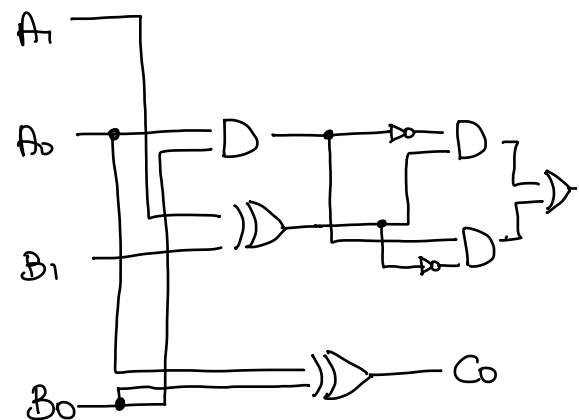
$C_0$ :

$A_1 A_0 \backslash B_1 B_0$	00	01	11	10
00	0	1	1	0
01	1	0	0	1
11	1	0	0	1
10	0	1	1	0

$$= A_0' B_0 + A_0 B_0' = A_0 \oplus B_0$$

Circuit for addition:

$$\begin{aligned} C_1 &= A_1' B_1 (A_0' + B_0') + A_1 B_1' (B_0' + A_0') \\ &\quad + A_0 B_0 (A_1' B_1' + A_1 B_1) \\ &= (A_0' + B_0') (A_1' B_1 + A_1 B_1') + \\ &\quad (A_0 B_0) (A_1' B_1' + A_1 B_1) \\ &= (A_0' + B_0') (A_1 \oplus B_1) + \\ &\quad (A_0 B_0) (A_1 \oplus B_1)' \\ &= (A_0 B_0)' (A_1 \oplus B_1) + \\ &\quad (A_0 B_0) (A_1 \oplus B_1)' \end{aligned}$$



# Circuit for subtraction

$$C_1 = A_1' A_0' B_1' B_0 + A_1 A_0' B_1 B_0 + A_1' B_1 B_0' + A_1' A_0 B_1 + A_1 A_0 B_1' + A_1 B_1' B_0'$$

$$= A_1' B_1 (B_0' + A_0) + A_1 B_1' (A_0 + B_0') + A_0' B_0 (A_1' B_1' + A_1 B_1)$$

$$= (A_0 + B_0') (A_1' B_1 + A_1 B_1') + (A_0' B_0) (A_1 \oplus B_1)'$$

$$= (A_0' B_0)' (A_1 \oplus B_1) + (A_0' B_0) (A_1 \oplus B_1)'$$

$$C_0 = A_0 \oplus B_0$$

Together:

