CS 575 Software Testing and Analysis

Control Flow Data Analysis



- (c) Slides patially adopted from the book/slides of
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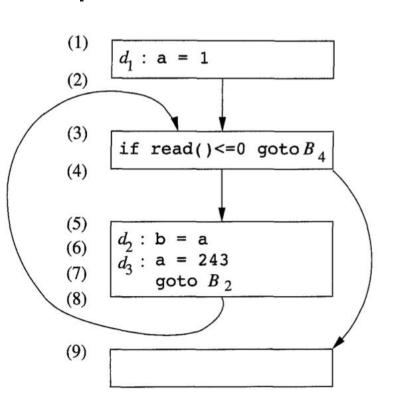


Software Testing and **Analysis**

- Revealing bugs
 - Null pointers, memory leaks, uninitialized variables, race conditions, buffer overflows...
- Code Optimization
- Defining Coverage Criteria
 - Based on data dependence:
 - Where does this value of x come from?
 - What would be affected by changing this?

...

Data Flow Analysis: Example



 \boldsymbol{B}_1

 B_{2}

 B_3

 B_{Δ}

- The first time program point (5) is executed, the value of a is 1 due to definition d1.
- In subsequent iterations, d3 reaches point (5) and the value of a is 243.
- At point (5), the value of *a* is one of {1,243}.
- It may be defined by one of {d1,d3}.



Def-Use Pairs

- A def-use (du) pair associates
 - a point in a program where a value is produced with
 - a point where it is used
- Extensively used for dataflow analysis in compilers
 - i.e., reaching definitions



Def (Definition)

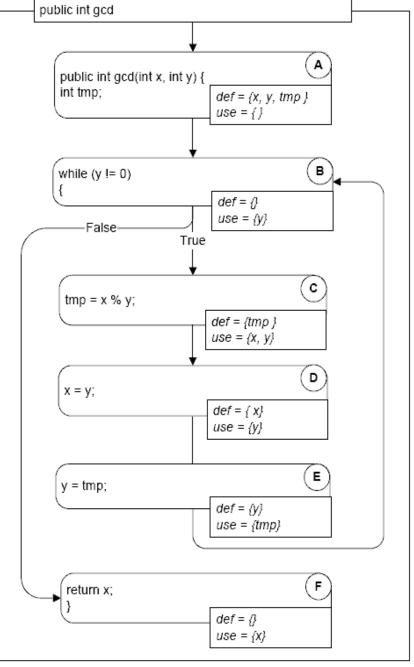
- Where a variable gets a value
 - Variable declaration (often the special value "uninitialized")
 - Variable initialization
 - Assignment
 - Values received by a parameter

Use

- Extraction of a value from a variable
 - Expressions
 - Conditional statements
 - Parameter passing
 - Returns



Def-Use Sets: Example

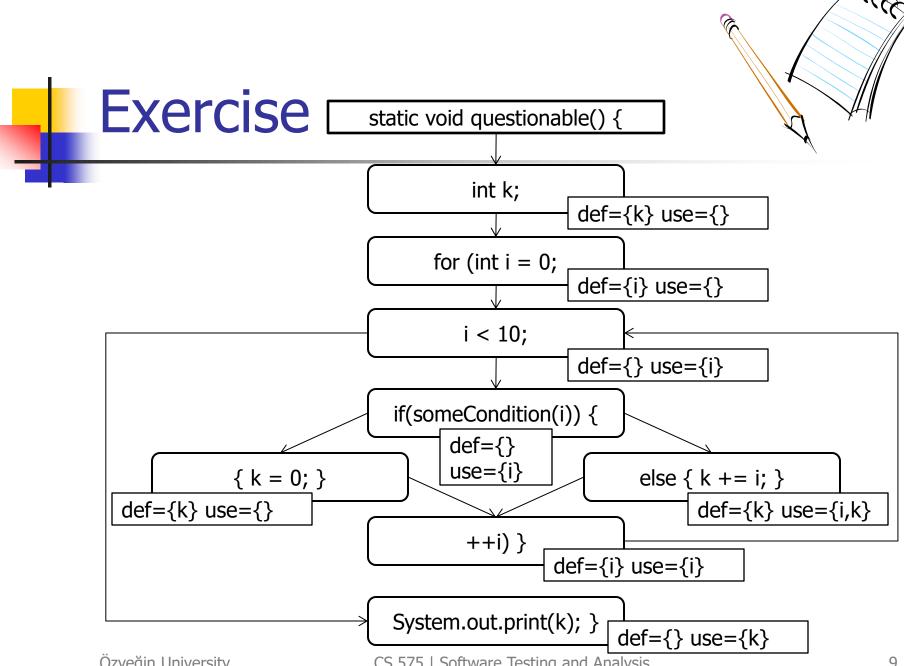




Exercise

- Draw the control flow graph for the method
- Annotate the nodes with *def* and *use* sets

```
static void questionable() {
   int k;
   for (int i = 0; i < 10; ++i) {
      if (someCondition(i)) {
        k = 0;
      } else {
        k += i;
      }
   }
   System.out.println(k);
}</pre>
```





Def-Use Pairs: Example

```
if (...) {
                                       if (...) {
                                                                  Definition:
     \mathbf{x} = \dots;
                                                                   x gets a
                                                                    value
                                             \mathbf{x} =
y = ... + x + ...;
                                                                  Use: the value
                                                                       of x is
                 Def-Use
                                                                     extracted
                Path for x
                                    = ... + x +
```



Killing (Overwriting) Definitions

- A definition-clear path is a path along the CFG from a definition to a use of the same variable without* another definition of the variable in-between
 - in case of any overwriting, the latter definition kills the former
- A def-use pair is formed if and only if there is a definition-clear path between the definition and the use

^{*} In fact, sometimes it is impossible to know for sure whether two definitions affect the same variable or storage location.

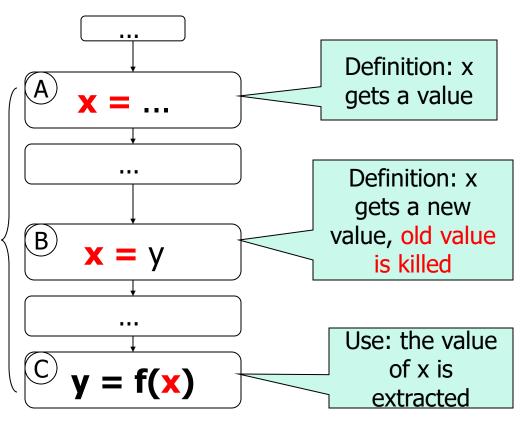


Killing (Overwriting) Definitions: Example

```
x = ... // A: def x
q = ...
x = y; // B: kill x, def x
z = ...
y = f(x); // C: use x
```

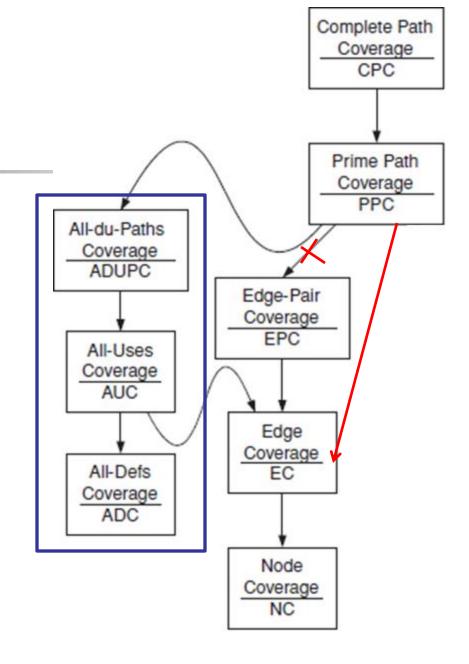
Path A..C is **not** definition-clear

Path B..C is definition-clear

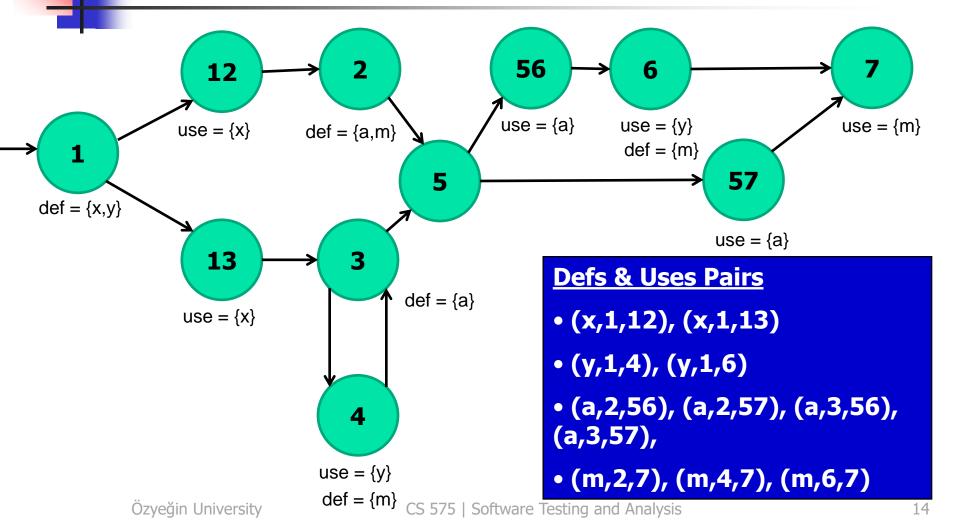


Recall:

Coverage Criteria Based on Data Flow



Data Flow Based Coverage Criteria: Example





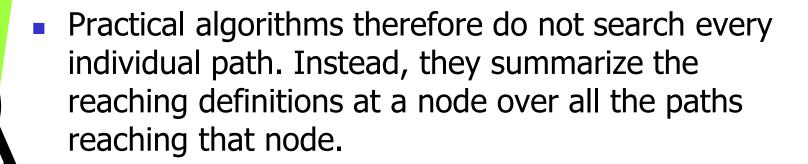
Calculating def-use pairs

- Defined in terms of paths in the CFG:
 - There is an association (d,u) between a definition of variable v at d and a use of variable v at u if and only if
 - there is at least one control flow path from d to u
 - with no intervening definition of v
 - v_d reaches $u(v_d)$ is a reaching definition at u).
 - If a control flow path passes through another definition e of the same variable v_i , v_e **kills** v_d at that point.



Calculating def-use pairs

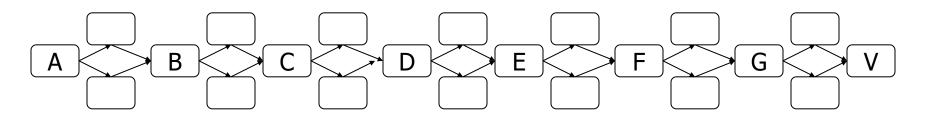
 Even if we consider only loop-free paths, the number of paths in a graph can be exponentially larger than the number of nodes and edges.







Example: Exponential number of paths (even without loops)



2 paths from A to B

4 from A to C

8 from A to D

16 from A to E

. . .

128 paths from A to V

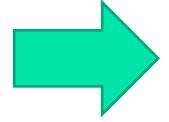
Tracing each path is not efficient, and we can do much better.



An Iterative Algorithm for Computing Reaching Definitions

 Based on the way reaching definitions at one node are related to the reaching definitions at an adjacent node







Propogation of Information Among Nodes of a CFG

- Suppose we are calculating the reaching definitions of node n, and there is an edge (p,n) from an immediate predecessor node p
 - If the predecessor node p can assign a value to variable v_r , then the definition v_p reaches n. We say the definition v_p is generated at p.
 - If a definition v_p of variable ν reaches a predecessor node p, and if ν is not redefined at that node, then the definition is propagated on from p to n.





Equations of node E(y = tmp)

Calculate reaching definitions at E in terms of its immediate predecessor D

- Reach(E) = ReachOut(D)
- ReachOut(E) = (Reach(E) \ $\{y_A\}$) $\cup \{y_F\}$



Equations of node B (while (y != 0))

This line has two predecessors:
Before the loop, end of the loop

- Reach(B) = ReachOut(A) ∪ ReachOut(E)
- ReachOut(A) = gen(A) = $\{x_A, y_A, tmp_A\}$
- ReachOut(E) = (Reach(E) \ $\{y_A\}$) $\cup \{y_E\}$



General equations for Reach analysis

```
Reach(n) = \cup ReachOut(m)
m \in pred(n)
```

ReachOut(n) = (Reach(n) \ kill (n)) \cup gen(n)

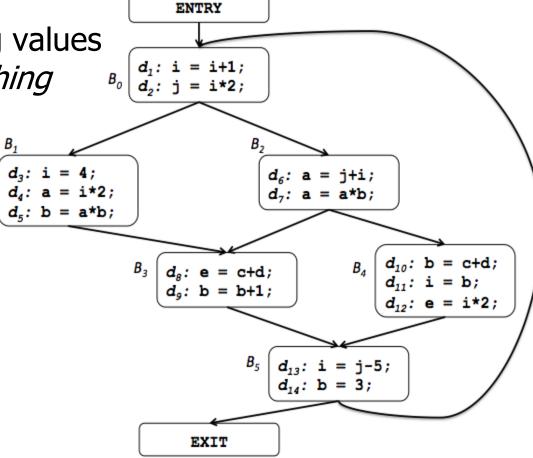
gen(n) = { $v_n | v$ is defined or modified at n } kill(n) = { $v_x | v$ is (re)defined or modified at x, $x \ne n$ }



Exercise

What are the following values according to the *Reaching Definitions* analysis?

- $gen(B_0)$
- kill(B₀)
- ReachOut(B₀)





Exercise

What are the following values according to the *Reaching Definitions* analysis?

• $gen(B_0)=$

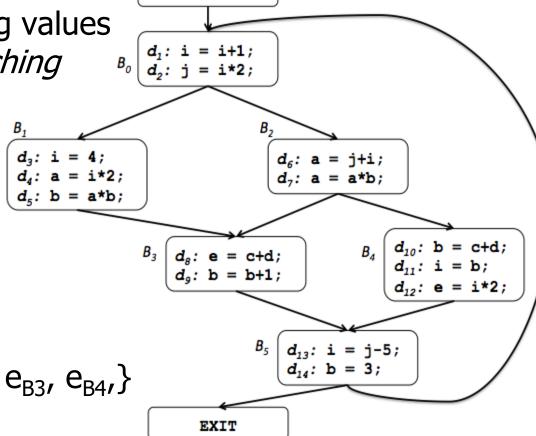
 $\{i_{B0}, j_{B0}\}$

• $kill(B_0)=$

 $\{i_{B1}, i_{B4}, i_{B5}\}$

• ReachOut(B_0)=

 $\{i_{B0}, j_{B0}, a_{B1}, a_{B2}, b_{B5}, e_{B3}, e_{B4}, \}$



ENTRY

Avail equations (available expressions)

Avail (n) =
$$\bigcap$$
 AvailOut(m) "all paths" rather than "any path"

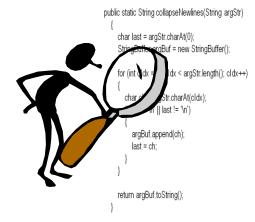
```
AvailOut(n) = (Avail (n) \ kill (n)) \cup gen(n)
```

```
gen(n) = { exp | exp is computed at n }
kill(n) = { exp | exp has variables assigned at n }
```



Possible Application of Avail analysis

- Compiler Optimization
 - If an expression is available, do not recompute it
- Enforcing Variable Initialization
 - Java requires a variable to be initialized before use on all execution paths
 - kill sets are empty since there is no way to "unitialize" a variable in Java

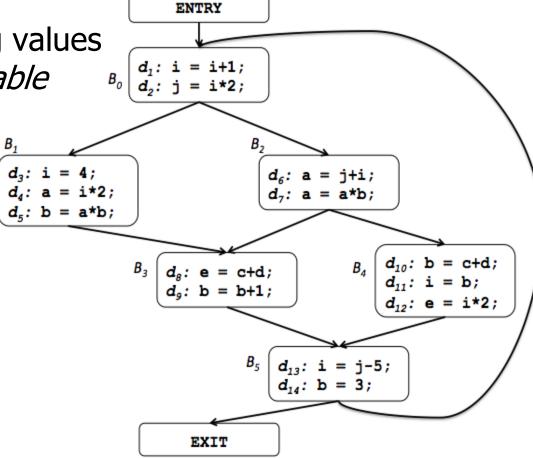




Exercise

What are the following values according to the Available Expressions analysis?

- gen(B₀)
- kill(B₀)
- Avail(B₅)

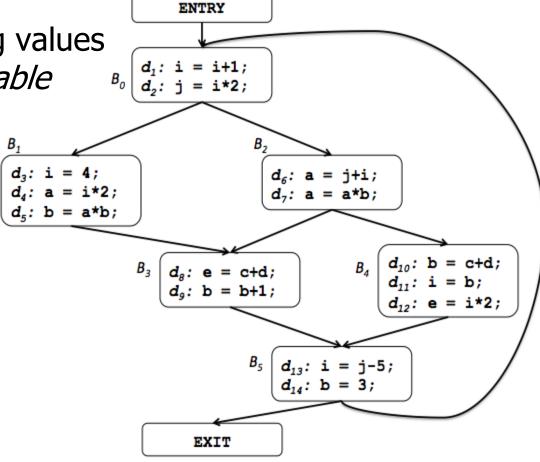




Exercise

What are the following values according to the Available Expressions analysis?

- $gen(B_0) = {i*2}$
- kill(B₀)= {j+i, j-5, i+1}
- Avail(B₅)=
 {i*2, c+d}



4

Live variable equations

```
Live(n) = \cup LiveOut(m)

m \in Succ(n)

if the variable might be used in "any following path"

LiveOut(n) = (Live(n) \ kill (n)) \cup gen(n)

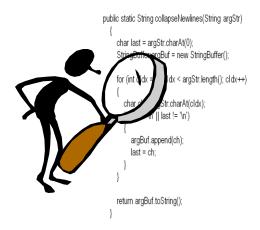
gen(n) = { v | v is used at n }
```

 $kill(n) = \{ v \mid v \text{ is modified at } n \}$



Possible Application of Live Variable Analysis

- Recognizing useless definitions
 - Often symptomatic for a fault, e.g., misplelling a variable name

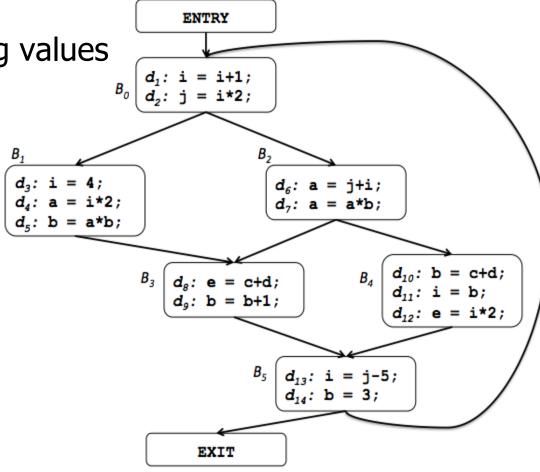




Exercise

What are the following values according to the *Live* Variables analysis?

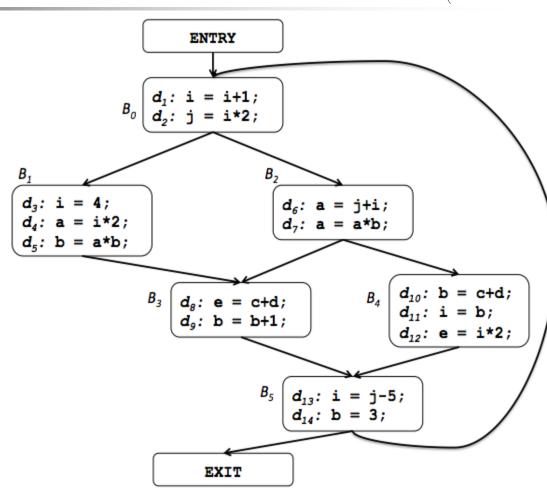
- gen(B₀)
- kill(B₀)
- Live(B₀)





Exercise

- What are the followir according to the Live Variables analysis?
 - gen(B₀)=
 {i}
 - kill(B₀)={i, j}
 - Live(B₀)={i, j, b, c, d}





Classification of analyses

- Forward/backward: a node's set depends on that of its predecessors/successors
- Any-path/all-path: a node's set contains a value iff it is coming from any/all of its inputs

	Any-path (∪)	All-paths (∩)
Forward (pred)	Reach	Avail
Backward (succ)	Live	"inevitable"



Inevitability Definition



$$Inev(n) = \bigcap InevOut(m)$$

$$m \in succ(n)$$
all paths
backward analysis

InevOut(n) = (Inev(n) \ kill (n))
$$\cup$$
 gen(n)

Here, we are interested in the accessibility of a node, not (re)definition or modification of variables



"Inevitability" Analysis

- Example usage scenarios:
 - Ensuring that interrupts are reenabled after executing an interrupt-handling routine
 - Ensuring that files are closed after opening them

. . . .





Iterative Solution of Dataflow Equations

- Initialize values (first estimate of answer)
 - For "any path" problems, first guess is "nothing" (empty set) at each node
 - For "all paths" problems, first guess is "everything" (set of all possible values = union of all "gen" sets)
- Repeat until nothing changes
 - Pick some node and recalculate (new estimate)

This will converge on a "fixed point" solution where every new calculation produces the same value as the previous guess.



Data flow analysis with arrays and pointers

- Arrays and pointers introduce uncertainty: Do different expressions access the same storage?
 - a[i] same as a[k] when i = k
 - a[i] same as b[i] when a = b (aliasing)
- The uncertainty is accommodated depending to the kind of analysis
 - Any-path: gen sets should include all potential aliases and kill set should include only what is definitely modified
 - All-path: vice versa