

Laboratory Assignment #0 – All Sections

Due: 27/10/2020, 23:59

Xilinx ISE Design Tool Installation

Requirements:

You are required to install Xilinx ISE Design tool on your computer. Then, you should follow and perform the first 57 pages of “Xilinx ISE Design Tool Guide (Schematic)” tutorial on SUCourse+. You just need to implement and simulate the circuit in the tutorial by following the steps described in the tutorial (you do not have to design a circuit or solve a problem on your own.).

The last step of your assignment is to save wave configuration as described in the page 57 of “Xilinx ISE Design Tool Guide (Schematic)”. Then, you should zip your project directory (if your project path is D:\Xilinx\lab1, then zip directory lab1) and submit the zip file until 27 October 2020, 23:59.