



DMP2004DWK

DUAL P-CHANNEL ENHANCEMENT MODE MOSFET

Product Summary

V _{(BR)DSS}	R _{DS(on) max}	I _D Τ _A = +25°C
20V	0.9Ω @ $V_{GS} = 4.5V$	-430mA

Description

This MOSFET is designed to minimize the on-state resistance (R_{DS(on)}) and yet maintain superior switching performance, making it ideal for high-efficiency power management applications.

Applications

Load Switch

Features

- Dual P-Channel MOSFET
- Low On-Resistance
- Low Gate Threshold Voltage V_{GS(TH)} <1V
- Low Input Capacitance
- Fast Switching Speed
- Low Input/Output Leakage
- ESD Protected
- Totally Lead-Free & Fully RoHS Compliant (Notes 1 & 2)
- Halogen and Antimony Free. "Green" Device (Note 3)
- For automotive applications requiring specific change control (i.e.: parts qualified to AEC-Q100/101/200, PPAP capable, and manufactured in IATF 16949 certified facilities), please refer to the related automotive grade (Q-suffix) part.
 A listing can be found at

https://www.diodes.com/products/automotive/automotive-products/.

 This part is qualified to JEDEC standards (as references in AEC-Q) for High Reliability.
 https://www.diodes.com/quality/product-definitions/

Mechanical Data

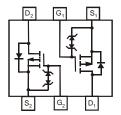
- Case: SOT363
- Case Material: Molded Plastic, "Green" Molding Compound.
 UL Flammability Classification Rating 94V-0
- Moisture Sensitivity: Level 1 per J-STD-020
- Terminals Connections: See Diagram
- Terminals: Finish Matte Tin Annealed over Alloy 42 Leadframe.
 Solderable per MIL-STD-202, Method 208 (3)
- Weight: 0.006 grams (Approximate)







Top View



Top View Internal Schematic

Ordering Information (Note 4)

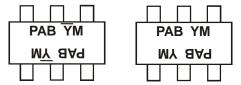
Part Number	Case	Packaging
DMP2004DWK-7	SOT363	3000/Tape & Reel

Notes: 1. No purposely added lead. Fully EU Directive 2002/95/EC (RoHS), 2011/65/EU (RoHS 2) & 2015/863/EU (RoHS 3) compliant.

- 2. See https://www.diodes.com/quality/lead-free/ for more information about Diodes Incorporated's definitions of Halogen- and Antimony-free, "Green" and Lead-free.
- 3. Halogen- and Antimony-free "Green" products are defined as those which contain <900ppm bromine, <900ppm chlorine (<1500ppm total Br + Cl) and <1000ppm antimony compounds.
- 4. For packaging details, go to our website at http://www.diodes.com/products/packages.html.



Marking Information



 $\begin{array}{l} PAB = Product\ Type\ Marking\ Code\\ \overline{Y}M\ or\ YM = Date\ Code\ Marking\\ Y\ or\ \overline{Y} = Year\ (ex:\ H = 2020)\\ M = Month\ (ex:\ 9 = September) \end{array}$

Date Code Key

Year	2007	~	20	20	2021	2022	2023	2024	. 20	25	2026	2027
Code	U	~	H	1		J	K	L	N	Л	N	0
Month	Jan	Feb	Mar	Apr	May	Jun	Jul	Aug	Sep	Oct	Nov	Dec
Code	1	2	3	4	5	6	7	8	9	0	N	D

Maximum Ratings (@T_A = +25°C, unless otherwise specified.)

Characteristic	Symbol	Value	Units	
Drain-Source Voltage	V_{DSS}	-20	V	
Gate-Source Voltage	V_{GSS}	±8	V	
Drain Current (Note 5) VGS = -4.5V	$T_A = +25$ °C $T_A = +85$ °C	Ι _D	-430 -310	mA

Thermal Characteristics (@T_A = +25°C, unless otherwise specified.)

Characteristic	Symbol	Value	Units
Total Power Dissipation (Note 5)	P_{D}	250	mW
Thermal Resistance, Junction to Ambient	R _{0JA}	500	°C/W
Operating and Storage Temperature Range	$T_{J_1}T_{STG}$	-65 to +150	°C

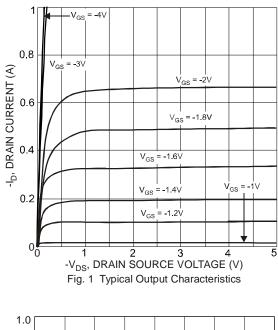
Electrical Characteristics (@T_A = +25°C, unless otherwise specified.)

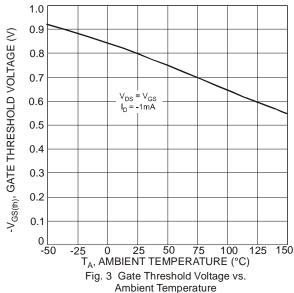
O 1			-			T 10 III
Characteristic	Symbol	Min	Тур	Max	Unit	Test Condition
OFF CHARACTERISTICS (Note 6)						
Drain-Source Breakdown Voltage	BV _{DSS}	-20	_	_	V	$V_{GS} = 0V, I_D = -250\mu A$
Zero Gate Voltage Drain Current	I _{DSS}	_		-1.0	μΑ	$V_{DS} = -20V, V_{GS} = 0V$
Gate-Source Leakage	I _{GSS}			±1.0	μΑ	$V_{GS} = \pm 4.5V, V_{DS} = 0V$
ON CHARACTERISTICS (Note 6)						
Gate Threshold Voltage	V _{GS(th)}	-0.5		-1.0	V	$V_{DS} = V_{GS}, I_{D} = -250 \mu A$
	R _{DS (ON)}		0.7	0.9		$V_{GS} = -4.5V$, $I_D = -430mA$
Static Drain-Source On-Resistance		_	1.1	1.4	Ω	$V_{GS} = -2.5V$, $I_D = -300mA$
			1.7	2.0		$V_{GS} = -1.8V, I_D = -150mA$
Forward Transfer Admittance	Y _{fs}	200	_	_	ms	$V_{DS} = 10V, I_D = 0.2A$
Diode Forward Voltage (Note 5)	V _{SD}	-0.5	_	-1.2	V	$V_{GS} = 0V, I_{S} = 115mA$
DYNAMIC CHARACTERISTICS						
Input Capacitance	C _{iss}	_		175	pF	101/11/
Output Capacitance	Coss	_	_	30	pF	$V_{DS} = -16V, V_{GS} = 0V$ - f = 1.0MHz
Reverse Transfer Capacitance	C _{rss}		_	20	pF	1 = 1.0IVIDZ

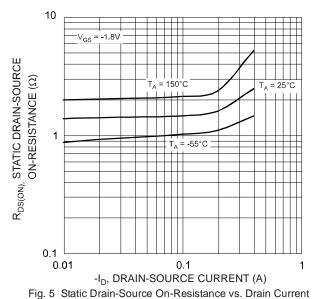
Notes: 5. Diodes Inc.'s "Green" policy can be found on our website at http://www.diodes.com.

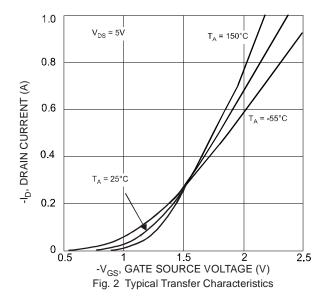
6. Short duration pulse test used to minimize self-heating effect.

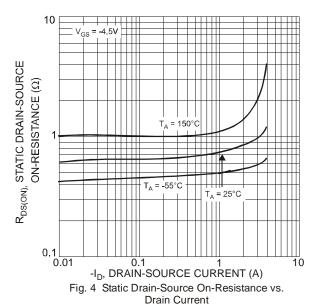












10 = 25°C R_{DS(ON)}, STATIC DRAIN-SOURCE ON-RESISTANCE (\O) 0.1 0.01 0.1 -I_D, DRAIN-SOURCE CURRENT (A)



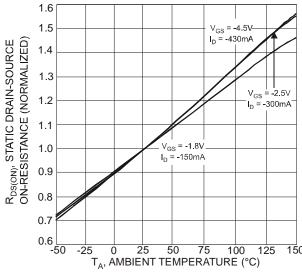


Fig. 7 Static Drain-Source On-State Resistance vs. Ambient Temperature

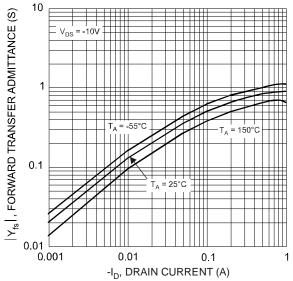


Fig. 9 Forward Transfer Admittance vs. Drain Current

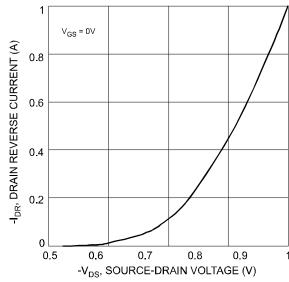


Fig. 8 Drain Reverse Current vs. Source-Drain Voltage

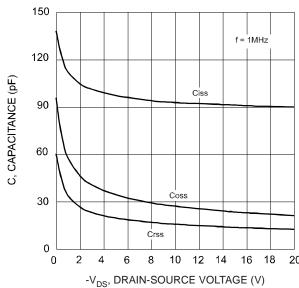
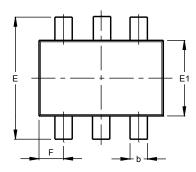


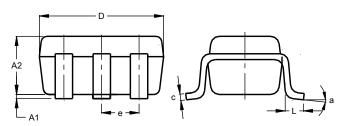
Fig. 10 Typical Capacitance



Package Outline Dimensions

Please see http://www.diodes.com/package-outlines.html for the latest version.

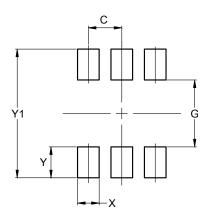




SOT363						
Dim	Min	Max	Тур			
A1	0.00	0.10	0.05			
A2	0.90	1.00	0.95			
b	0.10	0.30	0.25			
С	0.10	0.22	0.11			
D	1.80	2.20	2.15			
Е	2.00	2.20	2.10			
E1	1.15	1.35	1.30			
е	e 0.650 BSC					
F	0.40	0.45	0.425			
L	0.25	0.40	0.30			
а	0°	8°				
All Dimensions in mm						

Suggested Pad Layout

Please see http://www.diodes.com/package-outlines.html for the latest version.



Dimensions	Value (in mm)
С	0.650
G	1.300
Х	0.420
Y	0.600
Y1	2 500



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