
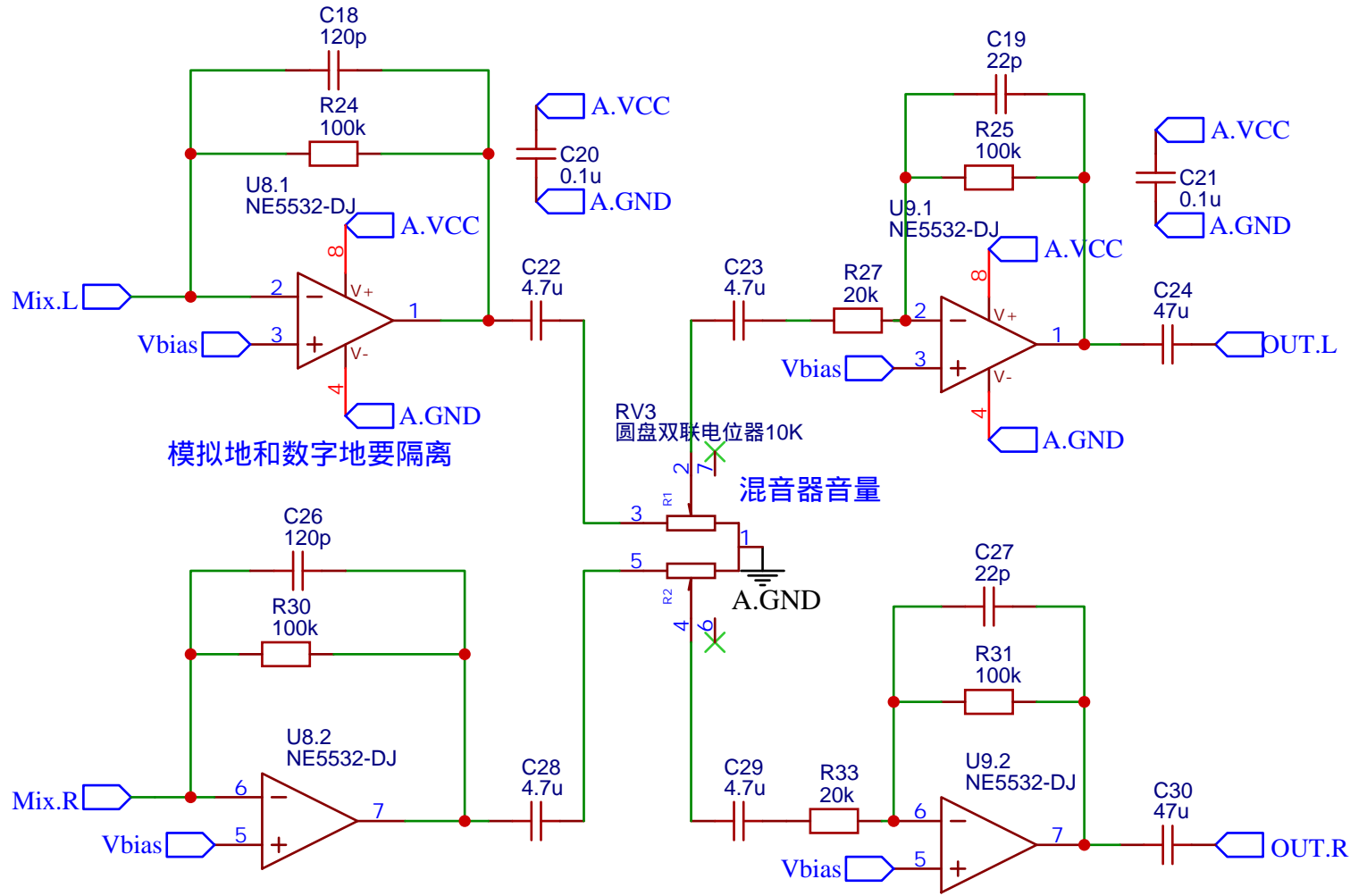


The diagram shows a stereo amplifier circuit. It has two input channels, OPLL-RO and OPLL-MO, each with a 2.7k resistor (R18, R21) and a 10uF capacitor (C14, C16) in series. The inputs are also connected to a 100k resistor (R20, R23) to ground. A 1.5nF capacitor (C15) is added in parallel with the feedback path from the output to the input of the OPLL-RO channel. The output of the OPLL-RO channel is connected to the Mix.L output through a 20k resistor (R19). The output of the OPLL-MO channel is connected to the Mix.R output through a 20k resistor (R22). The ground connections are labeled A.GND.

加1.5nf有重低音鼓

TITLE: Sheet_1		REV: 1.0
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120p用以滤除高频总线噪音  
以牺牲高频为代价，可以使用更大的电容



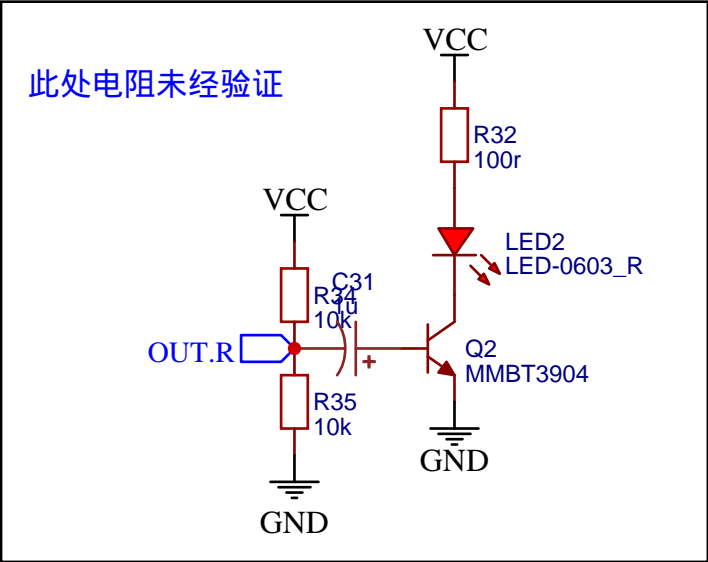
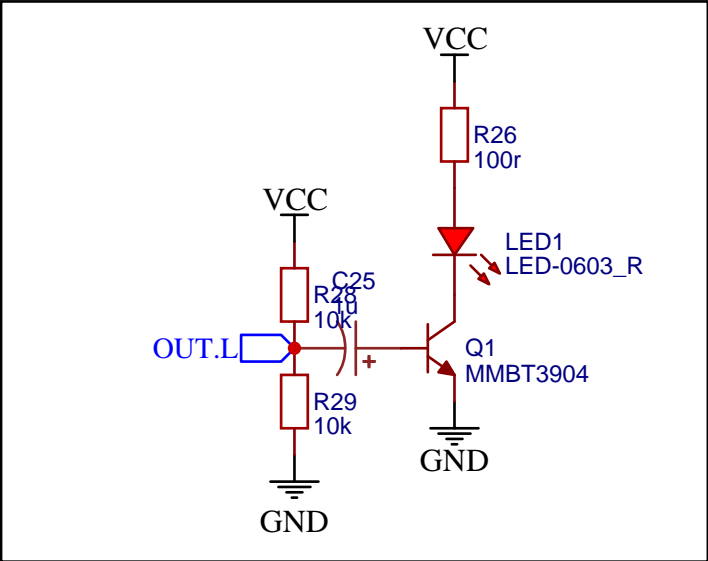
模拟地和数字地要隔离

混音器音量

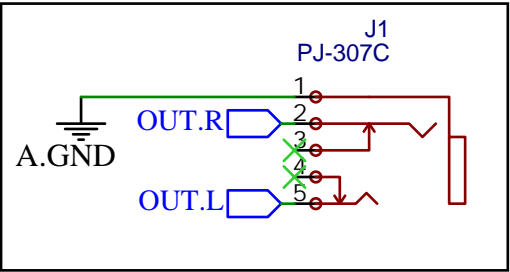
以后布局制版之前，音频放大电路要完整验证。

总线噪音不是该模块带来的，MegaGRRL主板在使用MegaMod时会放大总线噪音，无法避免  
解决方法是使用独立耳机孔输出  
此处使用VCC带来的噪音要小一些

OPM，YMF288模块使用独立耳机孔输出则没有任何噪音，但是本模块却无法避免噪音。



此处电阻未经验证



独立耳机孔音质很好，音量很大，但是OPLL还是有总线噪音

TITLE: Sheet_2		REV: 1.0
嘉立创EDA	Company: Your Company	Sheet: 1/1
	Date: 2023-10-05	Drawn By: Denjhang

