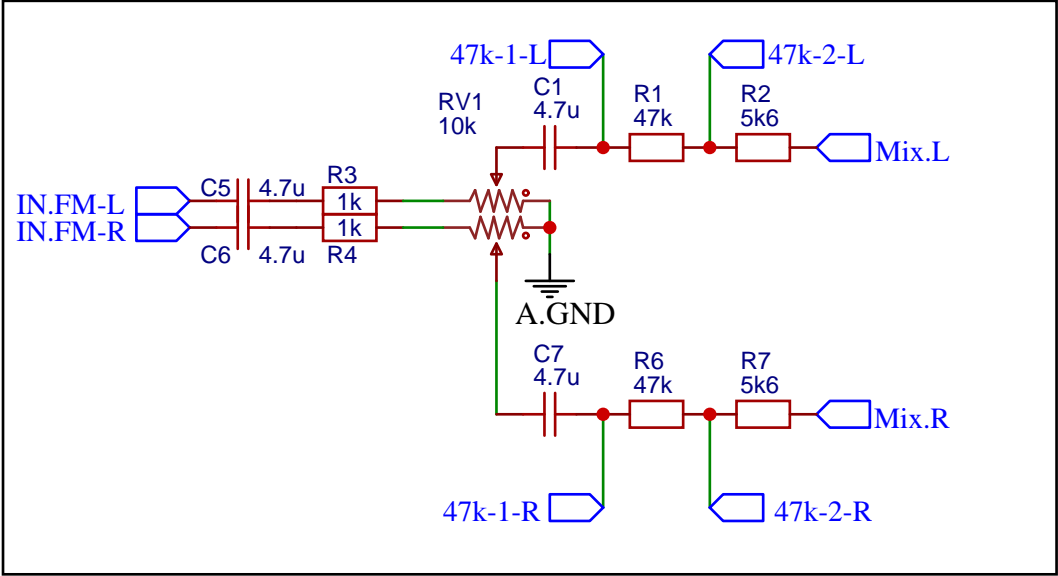
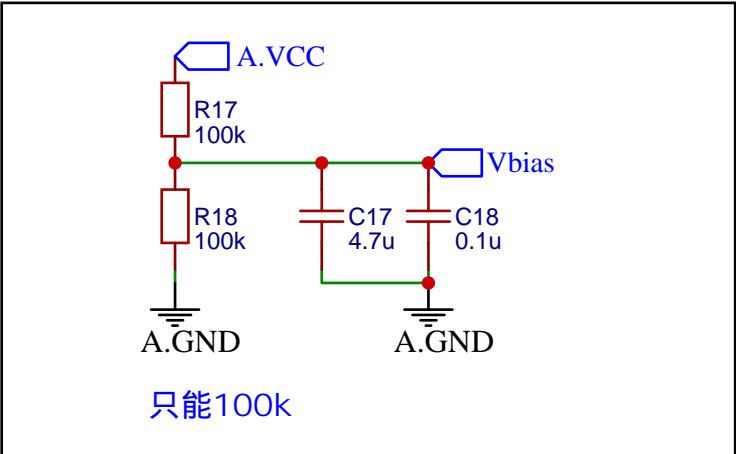
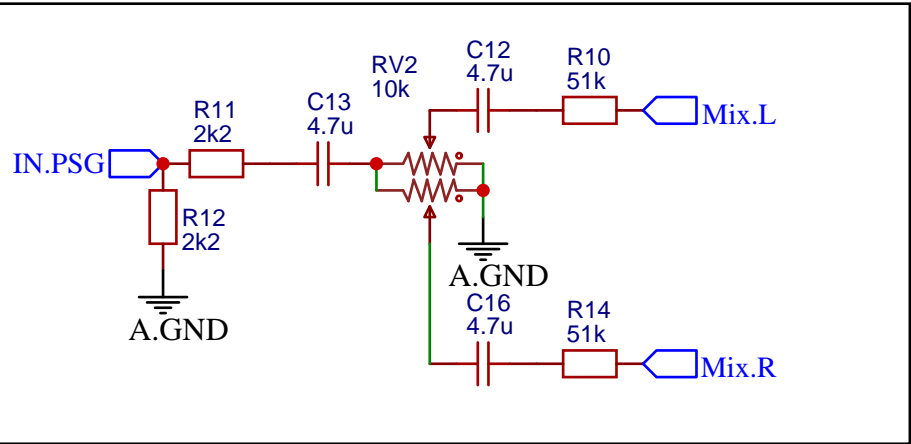
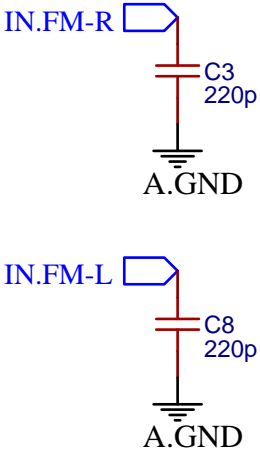


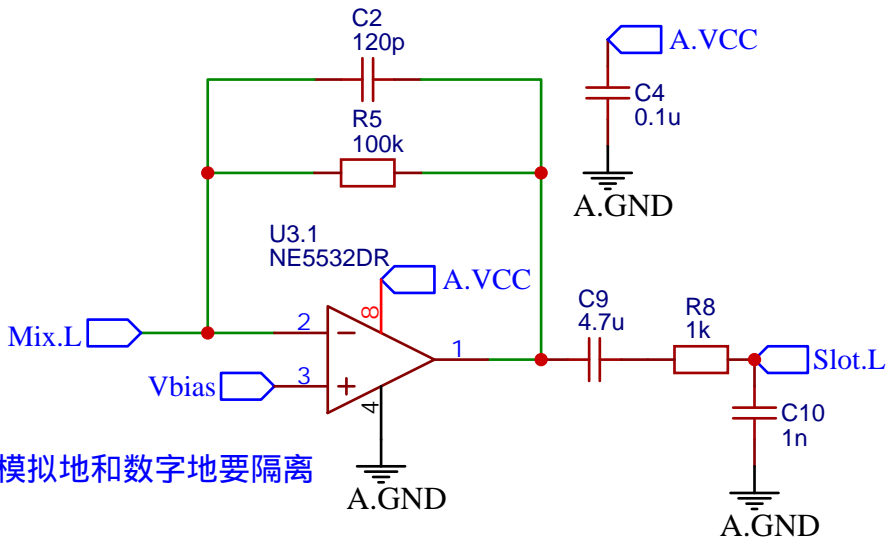
- 2024.8.13
- 1.使用旧版本混音电路，已成功
 - 2.单晶振拔插座，因为DCSG时钟不引入Mod
 - 3.取消一切累赘电容
- 2024.8.18
- 1.删除BSEL切换开关，默认BSEL0是FM
 - 2.增加贴片晶振
 - 3.优化布局



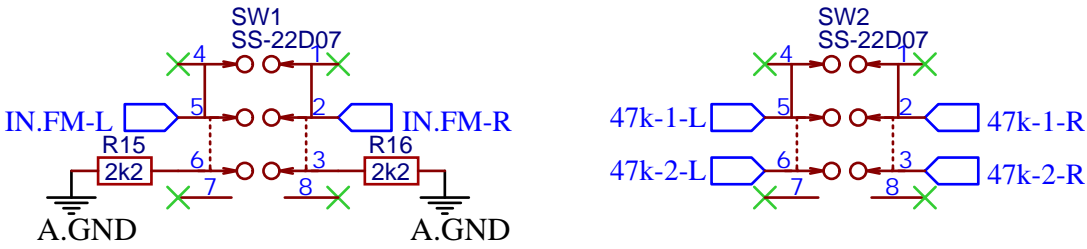
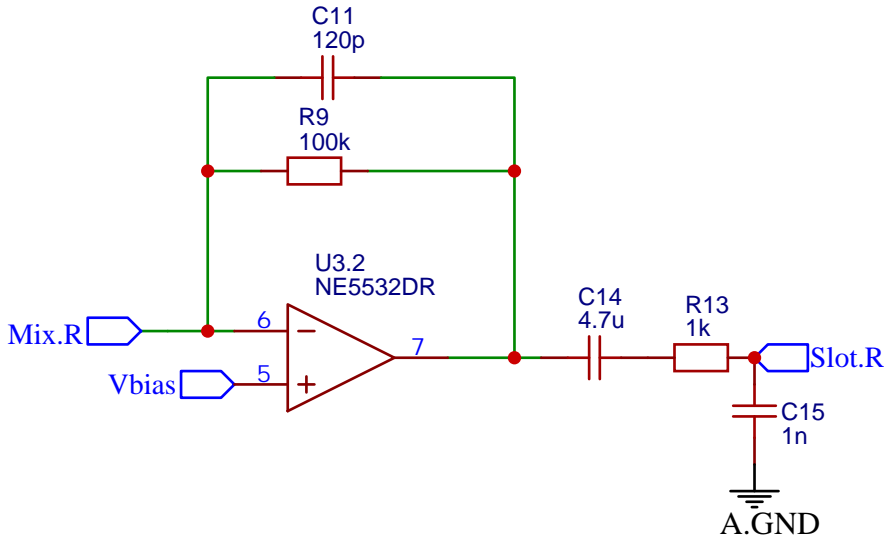
输入模式可能要改小电阻



只能100k



模拟地和数字地要隔离



YM3438/YM2612混音电路切换

