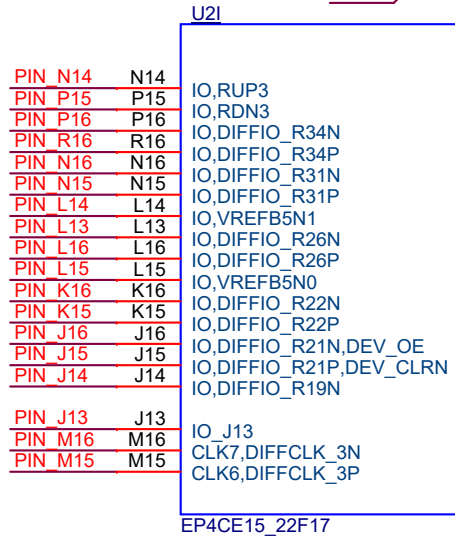
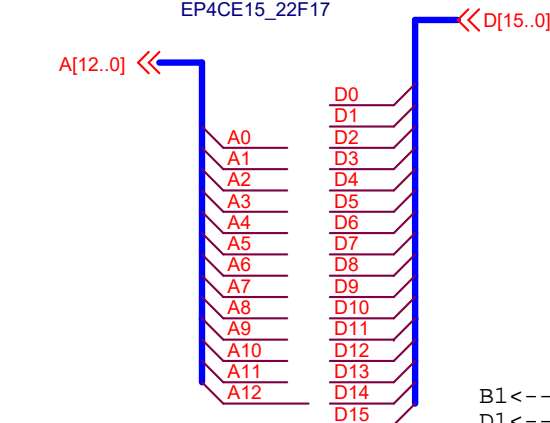
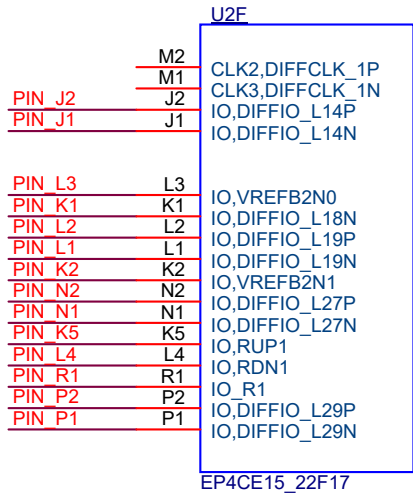
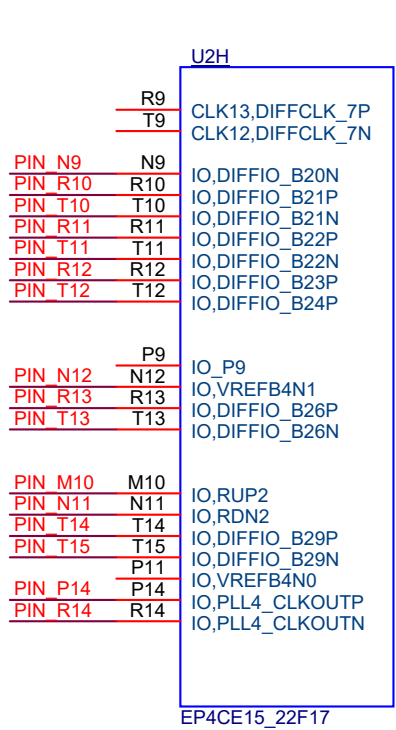
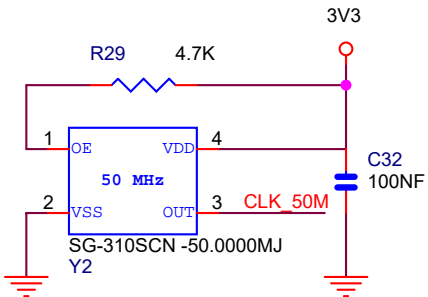
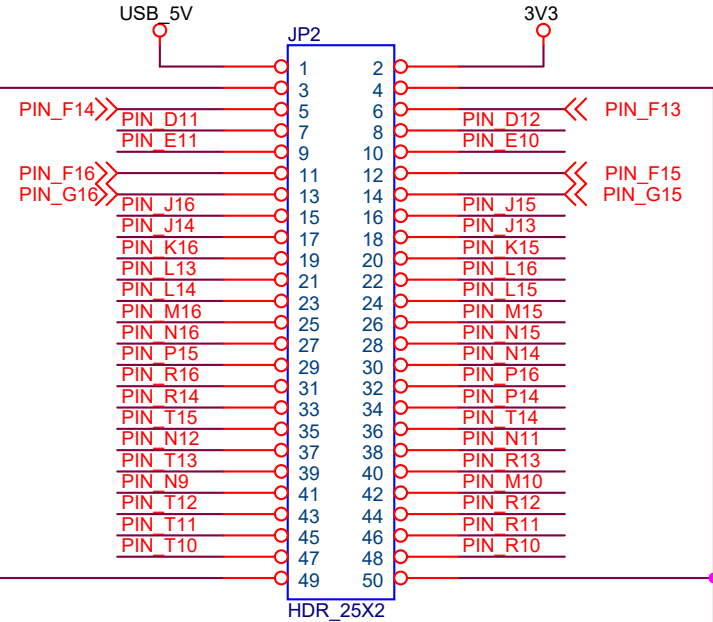
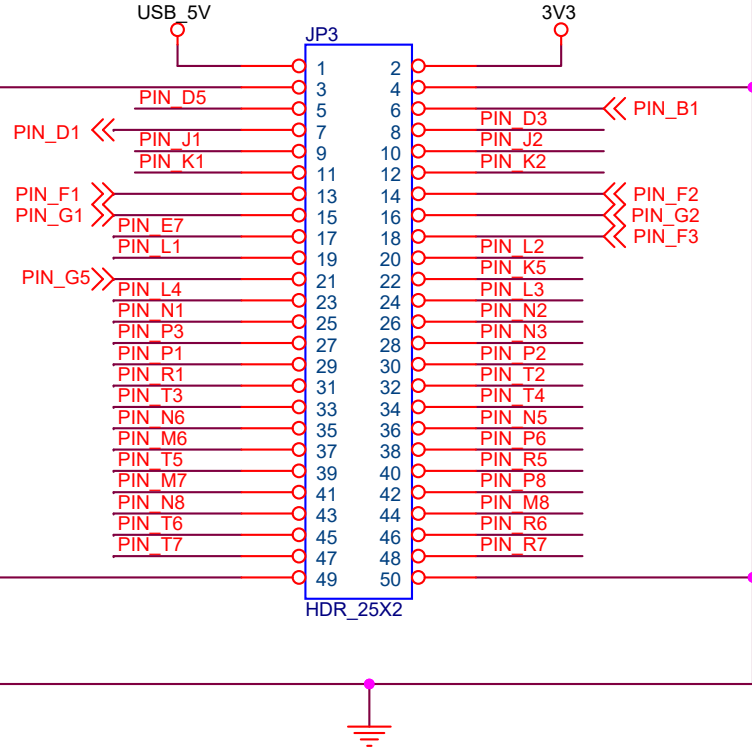


T10<----->VGA_VS
T11<----->VGA_HS
T14<----->VGA_R0
T15<----->VGA_R1
R14<----->VGA_R2
R16<----->VGA_R3
P15<----->VGA_R4
P16<----->VGA_R5
N16<----->VGA_G0
N15<----->VGA_G1
L15<----->VGA_G2
L16<----->VGA_G3
F15<----->VGA_G4
F16<----->VGA_G5
J13<----->VGA_B0
K15<----->VGA_B1
K16<----->VGA_B2
J14<----->VGA_B3
J15<----->VGA_B4
J16<----->VGA_B5

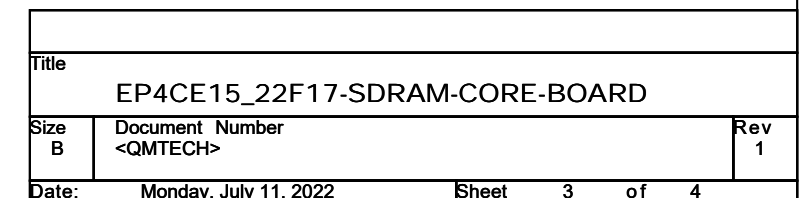
T12<----->Audio_Left
T13<----->Audio_Right

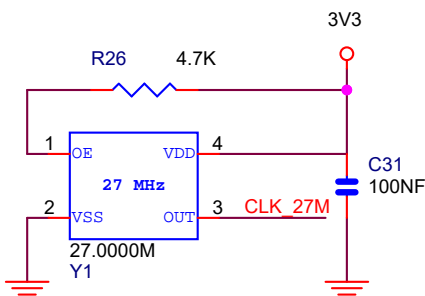
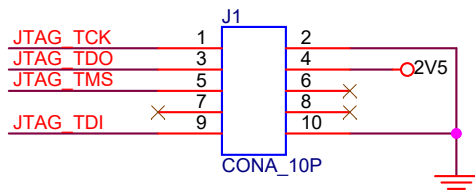
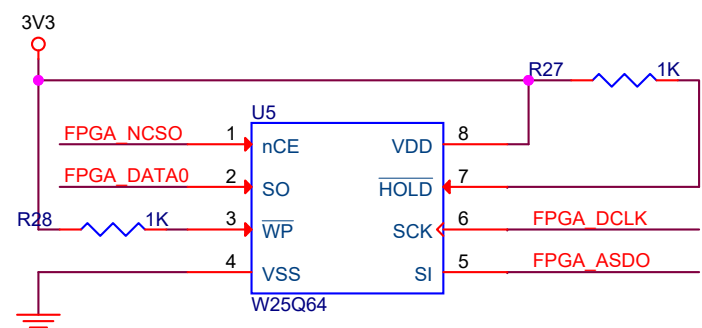
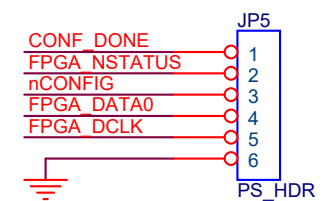
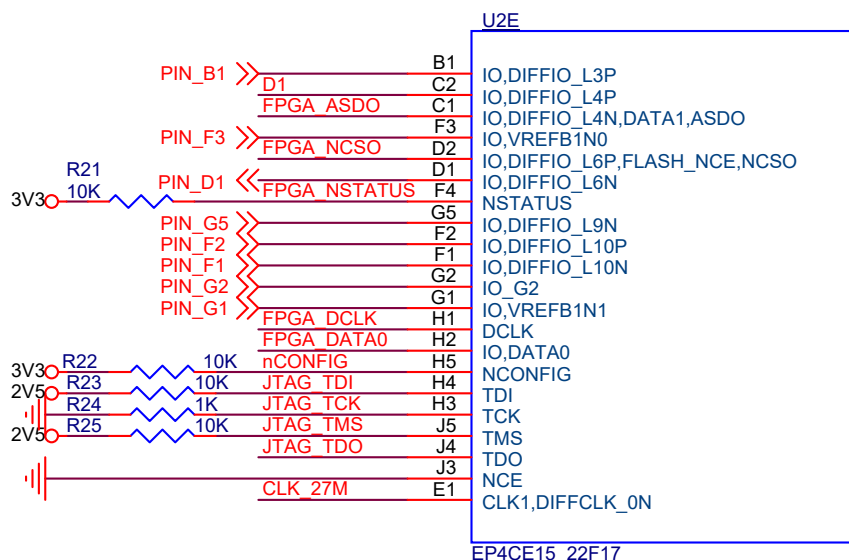
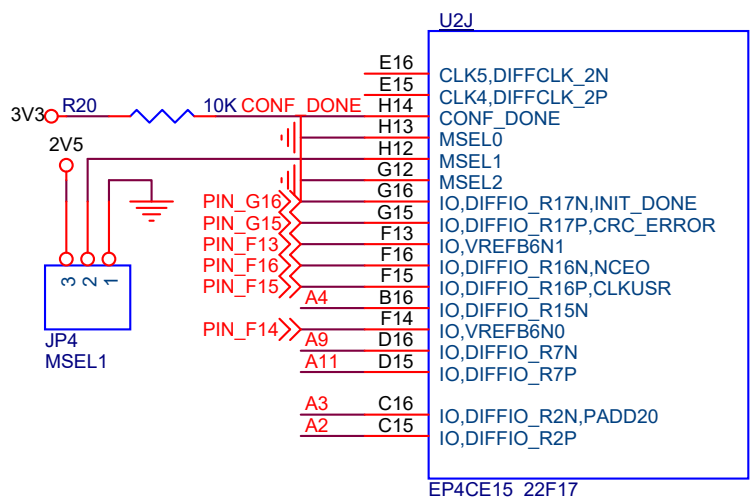
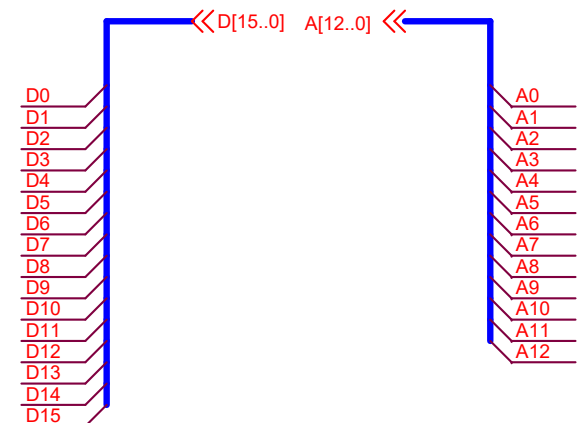
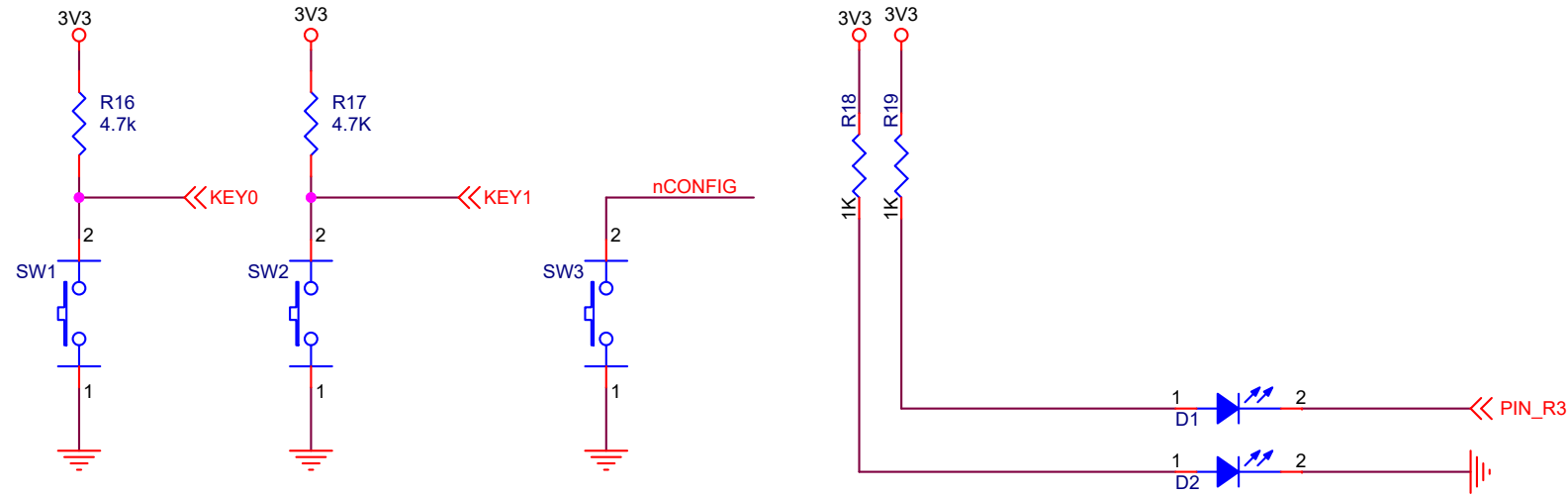


B1<----->UART_RX
D1<----->UART_TX
R1<----->MOSI
P1<----->Audio_IN
T3<----->SCK
T2<----->MISO
T4<----->SS



Title		
EP4CE15_22F17-SDRAM-CORE-BOARD		
Size	Document Number	Rev
B	<QMTECH>	1
Date:	Monday, July 11, 2022	Sheet 2 of 4





Title		
EP4CE15_22F17-SDRAM-CORE-BOARD		
Size	Document Number	Rev
B	<QMTECH>	1
Date:	Monday, July 11, 2022	Sheet 4 of 4