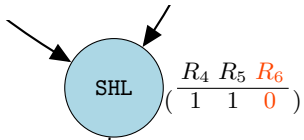


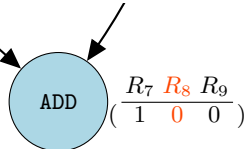
	reg	reg	reg
reg	0	0	0

	reg	imm	imm
imm	1	0	0

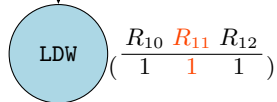


	reg	reg	reg
reg	0	0	0

	reg	t1	reg
reg	0	$\infty$	0
reg	0	$\infty$	0
t1	$\infty$	0	$\infty$



	reg	t2	t3
reg	0	$\infty$	$\infty$
t2	$\infty$	0	$\infty$
t3	$\infty$	$\infty$	0



$R_1$	imm	<-	IMM
$R_2$	reg	<-	VAR
$R_3$	reg	<-	imm
$R_4$	reg	<-	SHL(reg, reg)
$R_5$	reg	<-	SHL(reg, imm)
$R_6$	t1	<-	SHL(reg, imm)
$R_7$	reg	<-	ADD(reg, reg)
$R_8$	t2	<-	ADD(reg, t1)
$R_9$	t3	<-	ADD(reg, reg)
$R_{10}$	reg	<-	LDW(reg)
$R_{11}$	reg	<-	LDW(t2)
$R_{12}$	reg	<-	LDW(t3)