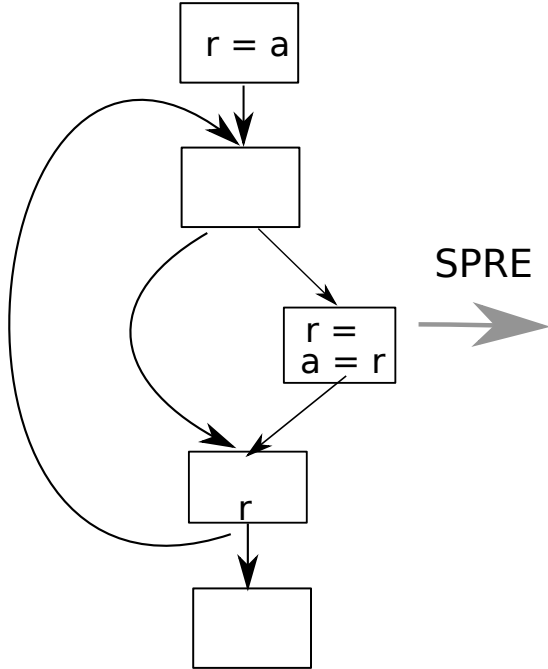
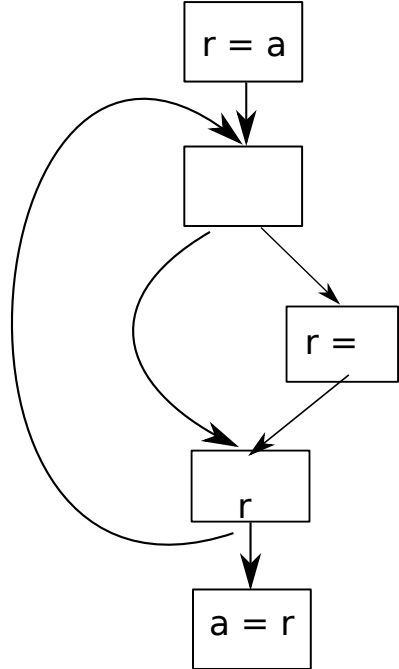


LPRE



SPRE



a is a memory location

r is a register