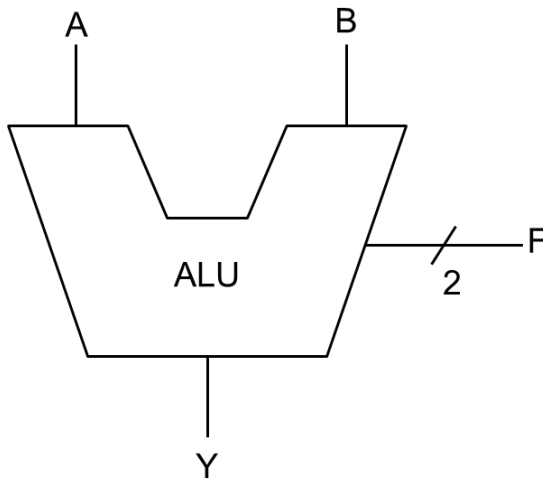# Homework

Release Date

21/02/2024

Due Date

29/02/2024 @ 5:30 PM

Overview

This homework is to help you revise digital logic design which is essential for this course.

1 Combinational Logic Design

Design the logic circuit of a 2-input ALU to perform the functions in the table below. Make use of the logic gates together with multiplexers and decoders.

F[1:0]	Function
00	A AND B
01	A OR B
10	A XOR B
11	A = ~B