Z80 Retro! - ESP32 Interface and Programmer Board with WiFi

PAGE 1: INTRODUCTION

Z80 Retro Interface

File: Z80Interface_sch.kicad_sch

SPI<->Z80 Bus

File: SPItoZ80Bus.kicad_sch

RS232

File: RS232.kicad_sch

ESP32

File: ESP32.kicad_sch

PAGE 2: Z80 RETRO INTERFACE

PAGE 3: SPI <-> Z80 ADDRESS, DATA, CONTROL EXPANSION

PAGE 4: RS232 EXTERNAL INTERFACES

PAGE 5: ESP32 PROGRAMMER & WIFI ACCESS

MOUNTING HOLES

- 4 required for mechanical
- 1 extra if board connects directly above Z80-Retro main





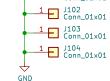




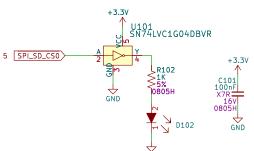




PROBE GND PINS 1 J101 Conn_01x01 1 J102 Conn_01x01



MICRO SD CARD ACTIVITY



PCB STACKUP NOTE

Use JLC04161H-3313 stackup to give : * Lower trace impedance (~50ohms). * GND plane closer to signal layer routing for improved signal integrity.

L0G011



L0G010

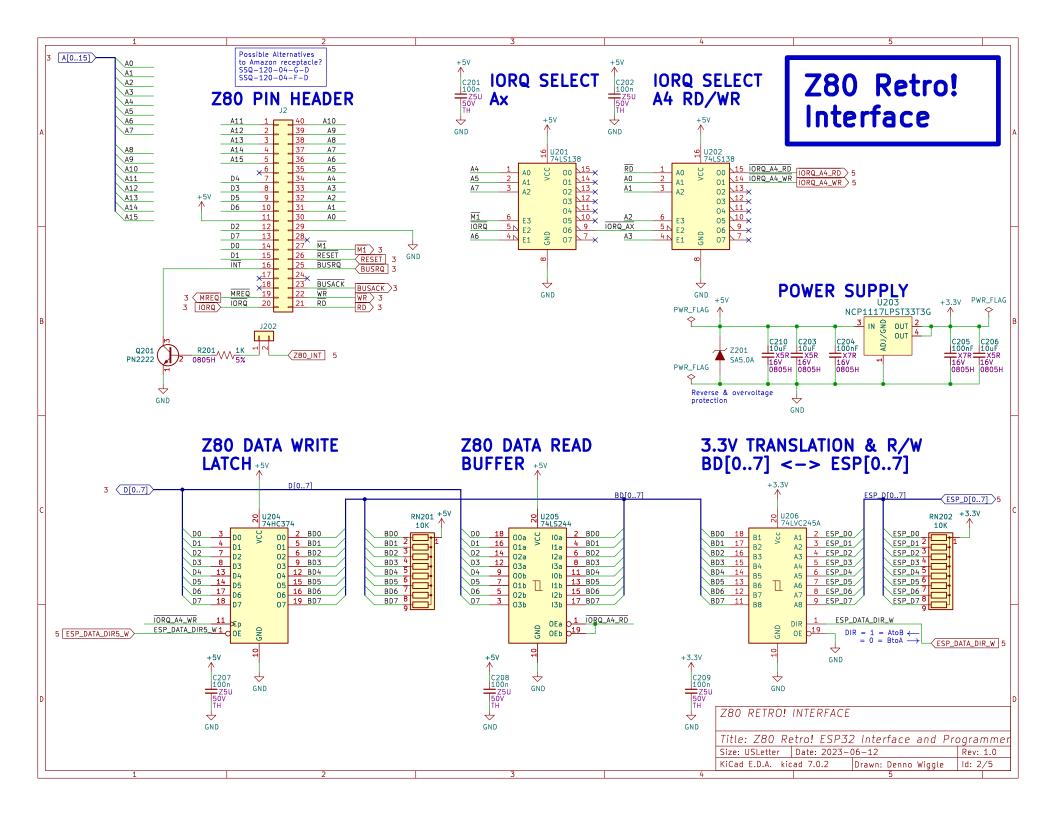
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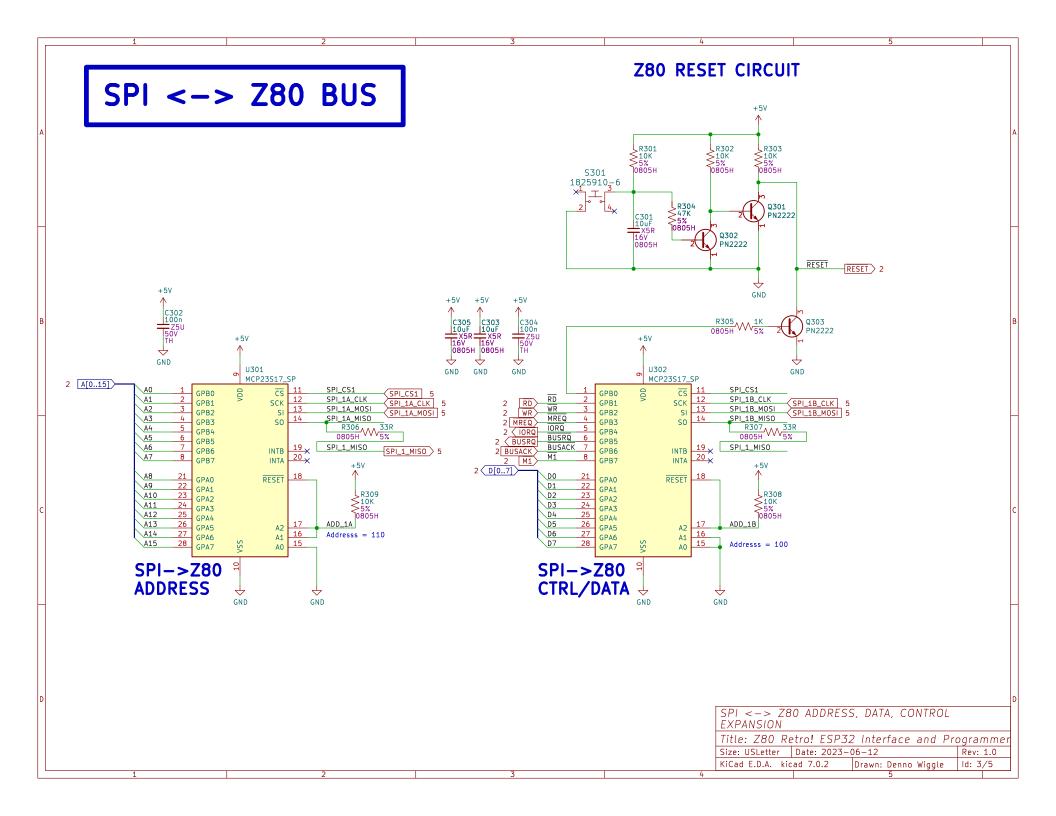
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Z80 Retro! - ESP32-S3 Interface and Programmer With WiFi

Title: Z80 Retro! ESP32 Interface and Programmer Size: USLetter Date: 2023-06-12 Rev: 1.0

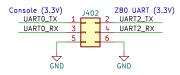
Drawn: Denno Wiggle KiCad E.D.A. kicad 7.0.2 ld: 1/5





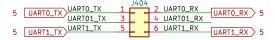
RS232

TTL RS232 HEADER



A place to add connection to external TTL level RS232 modules

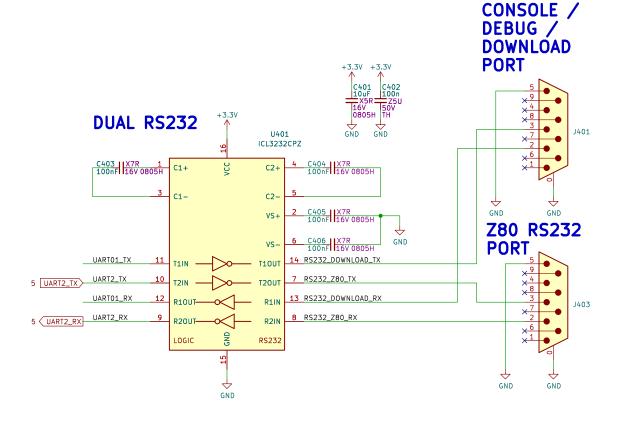
J401 TTL RS232 DB9 SELECTOR



J401 RS232 Output Options
To select ESP32 Console add jumpers
1-3, 2-4
To select ESP32 Debug messages add jumpers
3-5, 4-6
Default jumper settings
1-3, 2-4

If default jumpers are selected, external TTL level RS232 can be connected to pins 5 & 6 + GND for ESP32 debug/download port connection.

ESP32 Debug / Download option on uart 1 set in SW.



RS232 EXTERNAL INTERFACES

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