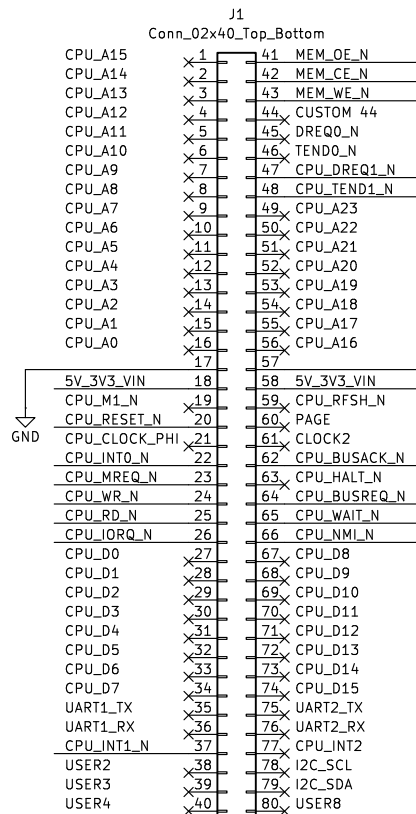
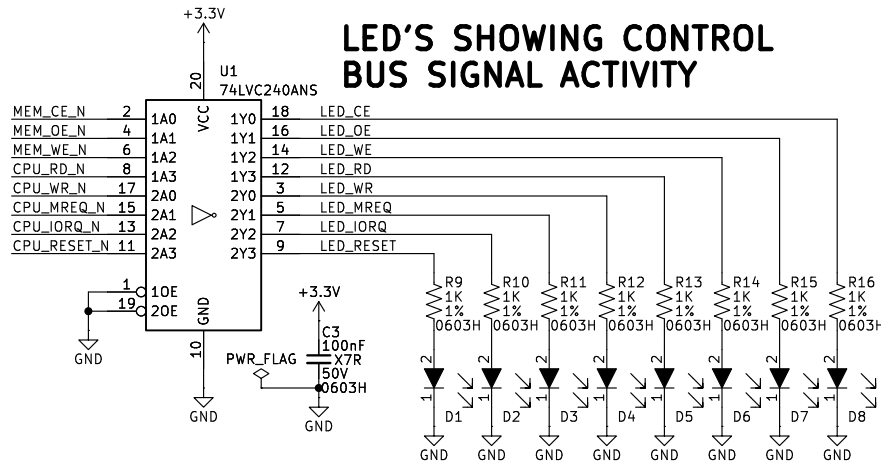


BUS ACTIVITY LED DISPLAY

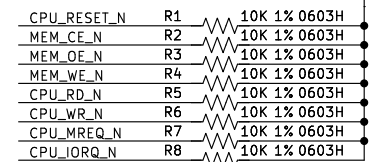
RCBUS CONNECTOR



LED'S SHOWING CONTROL BUS SIGNAL ACTIVITY

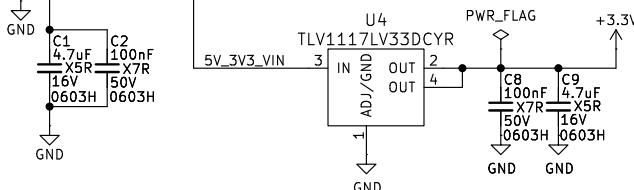


INPUT SIGNAL PULL-UPS

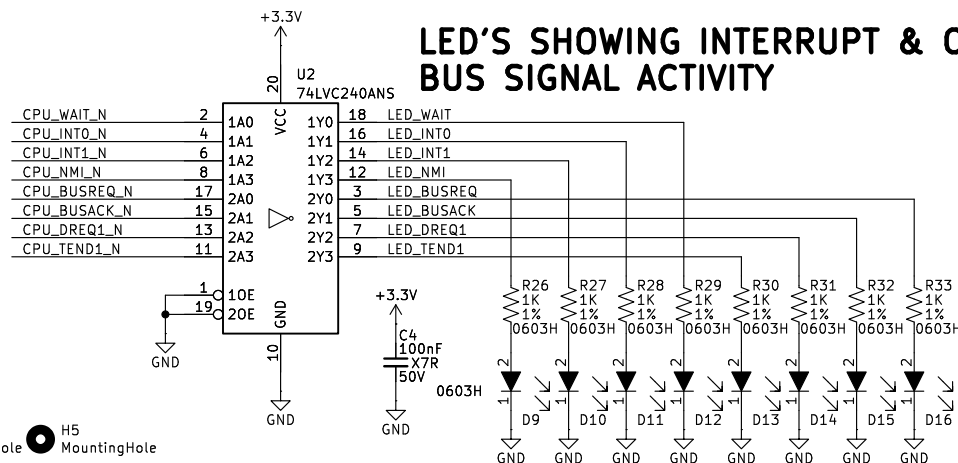


3.3V LDO POWER SUPPLY

$V_{in} = 5V$ or $3.3V$

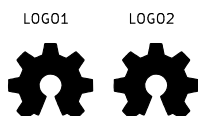
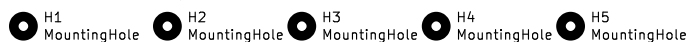


LED'S SHOWING INTERRUPT & OTHER BUS SIGNAL ACTIVITY



CPU_BUSREQ_N	R17	10K 1% 0603H
CPU_BUSACK_N	R18	10K 1% 0603H
CPU_DREQ1_N	R19	10K 1% 0603H
CPU_TEND1_N	R20	10K 1% 0603H
CPU_WAIT_N	R21	10K 1% 0603H
CPU_NMI_N	R22	10K 1% 0603H
CPU_INT0_N	R23	10K 1% 0603H
CPU_INT1_N	R24	10K 1% 0603H

MOUNTING/TOOLING HOLES



This documentation describes Open Hardware and is licensed under the CERN OHL v. 1.2.

You may redistribute and modify this documentation under the terms of the CERN OHL v.1.2. (<http://ohwr.org/cernohl>). This documentation is distributed WITHOUT ANY EXPRESS OR IMPLIED WARRANTY, INCLUDING OF MERCHANTABILITY, SATISFACTORY QUALITY AND FITNESS FOR A PARTICULAR PURPOSE. Please see the CERN OHL v.1.2 for applicable conditions

PCB STACKUP NOTE

JLC04161H-7628 (STD) stackup gives :
* 50ohm trace impedance for 13mil trace.

Bus Activity LED Display

Title: BALD

Size: USLetter

Date: 2024-07-24

Drawn: Denno Wiggle	Id: 1/1
---------------------	---------

Rev: 0