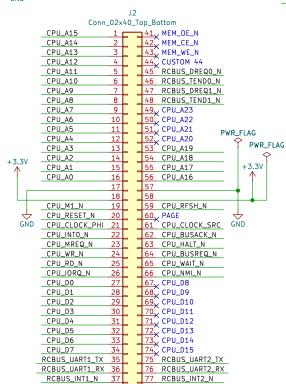
### **SERIES TERMINATORS**

_CPU_IORQ_N_S	R1	_^ ^ ^		1% 0603H	CPU_IORQ_N
CPU_MREQ_N_S	R2	_^		1% 0603H	CPU_MREQ_N
CPU_M1_N_S	R4	_^	<sup>7</sup> 33R	1% 0603H	CPU_M1_N
CPU_WR_N_S	R6	_^	<sup>7</sup> 33R	1% 0603H	CPU_WR_N
CPU_RD_N_S	R8	_^ ^ ^ ^		1% 0603H	CPU_RD_N
CPU_PHI_S	R10	_^ ^ ^ ^		1% 0603H	CPU_CLOCK_PHI
CPU_BUSACK_N_S	R12	_^		1% 0603H	CPU_BUSACK_N
CPU_TEND1_N_S	R14	_^	<sup>7</sup> 33R	1% 0603H	CPU_TEND1_N
CPU_RFSH_N_S	R16	_^	33R	1% 0603H	CPU_RFSH_N



#### RCBUS CONNECTOR



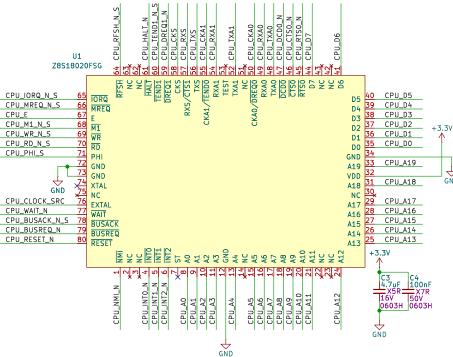
78<sub>×</sub> 12C\_SCL

79 12C\_SDA

USER2 USER3

USER4

#### Z8S180 20 MHZ CPU



## ZORO Z8S180 ON RCBUS ONLY

## LOGO1 LOGO2 PCB STACKUP NOTE



JLC04161H-7628 (STD) stackup gives : \* 50ohm trace impedance for 13mil trace.

## MOUNTING/TOOLING HOLES



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# INPUT SIGNAL PULL-UPS

+3.37

		4	
CPU_CLOCK_SRC	R3	ΛΛΛ 10K 1% 0603H	
CPU_RESET_N	R5	VVV10K 1% 0603H I	
CPU_BUSREQ_N	R7	VVV10K 1% 0603H	
CPU_WAIT_N	R9	VVV10K 1% 0603H	
CPU_NMI_N	R11	VVV10K 1% 0603H I	
CPU_INTO_N	R13	VVV10K 1% 0603H I	
CPU_INT1_N	R15	10K 1% 0603H	
CPU_INT2_N	R17	VVV 10K 1% 0603H	
CPU_DREQ1_N	R18	10K 1% 0603H	

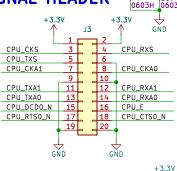
## **RCBUS JUMPERS**

RCBUS_UART1_TX	1	74	1 2	CPU_TXA0
RCBUS_UART2_TX	3		4	CPU_TXA1
RCBUS_UART1_RX	5		6	CPU_RXA0
RCBUS_UART2_RX	7		8	CPU_RXA1
RCBUS_INT2_N	9		10	CPU_INT2_N
RCBUS_INT1_N	11		12	CPU_INT1_N
		15	J	
_RCBUS_DREQO_N	1		2	CPU_CKA0
RCBUS_TENDO_N	3		4	CPU_CKA1
RCBUS_DREQ1_N	5		6	CPU_DREQ1_N
RCBUS_TEND1_N	7		8	CPU_TEND1_N
			] _	

+3.3\

C5 C6 4.7uF 100nF





	1
R31	10K 1% 0603H
R32	10K 1% 0603H
R33	VVV10K 1% 0603H
R34	10K 1% 0603H
R35	10K 1% 0603H
	R32 R33 R34

Z8S180	On	RCBUS	Only	
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Title: ZORO			
Size: USLetter	Date: 2024-0	Rev: 0	
KiCad E.D.A. 8.0	.4	Drawn: Denno Wiggle	ld: 1/1