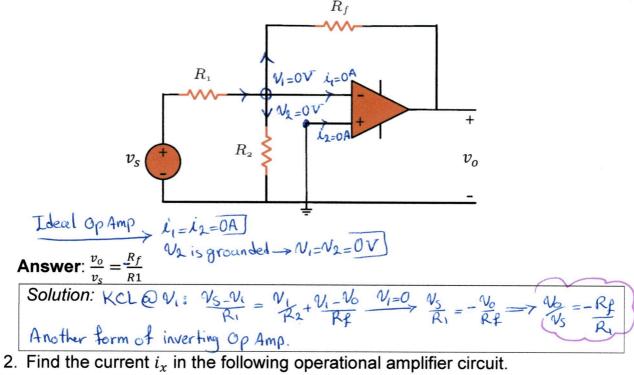


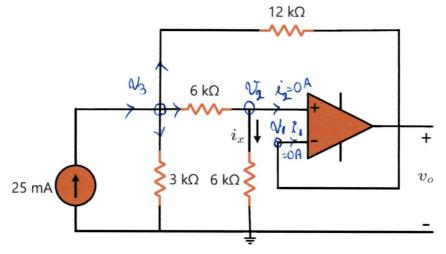
School of Electrical Engineering & **Telecommunications**

ELEC1111

Topic 6: Operational Amplifiers

1. Find the voltage gain $\frac{v_o}{v_c}$ in the following operational amplifier circuit.



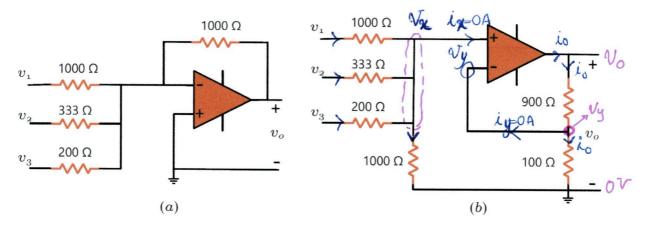


Answer: $i_x = 4.545 \text{ mA}$,

Solution: Ideal opamp,
$$L_1 = L_2 = 0A$$
, $V_1 = V_0 = 7V_2 = V_1 = V_0$

KCL@ V_2 : $\frac{V_2}{6K} = \frac{\sqrt{3} - \sqrt{2}}{6K} = \frac{\sqrt{2} + \sqrt{2}}{6K} = \frac{\sqrt{3} - \sqrt{2}}{2} = \frac{\sqrt{2} + \sqrt{2} + \sqrt{2}}{2} = \frac{\sqrt{2} + \sqrt{2} + \sqrt{2} + \sqrt{2}}{2} = \frac{\sqrt{2} + \sqrt{2} + \sqrt{2} + \sqrt{2} + \sqrt{2}}{2} = \frac{\sqrt{2} + \sqrt{2} + \sqrt{2} + \sqrt{2} + \sqrt{2}}{2} = \frac{\sqrt{2} + \sqrt{2} + \sqrt{2} + \sqrt{2} + \sqrt{2}}{2} = \frac{\sqrt{2} + \sqrt{2} + \sqrt{2} + \sqrt{2} + \sqrt{2}}{2} = \frac{\sqrt{2} + \sqrt{2} + \sqrt{2}$

3. Calculate v_o in the following two Op Amp circuits in terms of three input voltages v_1 , v_2 , and v_3 .

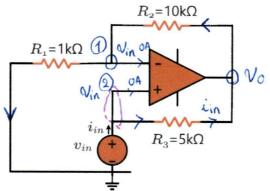


Answer:

a)
$$v_{0} = -(v_{1} + 3v_{2} + 5v_{3})$$
b) $v_{0} = v_{1} + 3v_{2} + 5v_{3}$

Solution: (a) Typical Summing Opamp $\Rightarrow V_{0} = -\left(\frac{1 \times v_{1}}{1 \times v_{2}} + \frac{1 \times v_{2}}{200} + \frac{1 \times v_{2}$

- 4. In the following circuit,
 - a) Calculate the ratio $\frac{v_{in}}{i_{in}}$ in terms of R_1 , R_2 , and R_3 , and then use the numerical values of resistances given in the circuit to determine the ratio. What does this ratio represent?
 - b) If $R_1 = R_2 = R$, determine the value of R_3 for the Op Amp circuit such that $\frac{v_{in}}{i_{in}} = -33 \text{ k}\Omega$



Answer:

a)
$$\frac{v_{in}}{i_{in}} = -\frac{R_1 R_3}{R2}$$

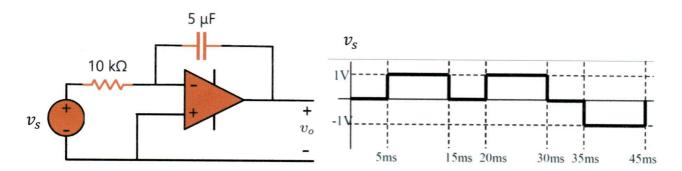
b)
$$R_3 = 33 \text{ k}\Omega$$

b)
$$R_3 = 33 \text{ k}\Omega$$

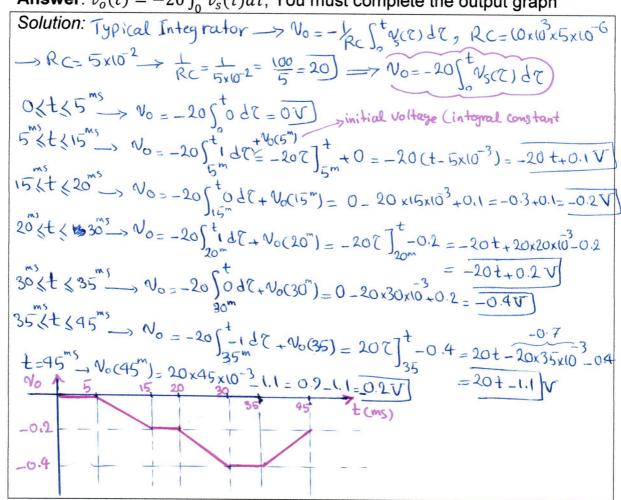
Solution: Ideal of Amp $V^- + V^+ = V$ in $i^- = i^+ = 0$ A

KCL @ $V_0 = V$ in $V_0 =$

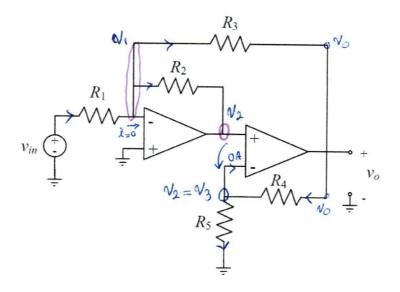
5. Calculate and draw the output voltage v_o in the following circuit for input voltage signal v_i given in the graph from $0 \le t \le 45~\mathrm{ms}$.



Answer: $v_o(t) = -20 \int_0^t v_s(\tau) d\tau$, You must complete the output graph



- 6. (Final Exam S1, 2014) Consider the Op Amp circuit below with input v_{in} and output v_o .
 - a) Derive an expression for the voltage gain $\frac{v_o}{v_{in}}$ in terms of the resistor values R_1 , R_2 , R_3 , R_4 , and R_5 .
 - b) If $R_1=1~\mathrm{k}\Omega$, $R_2=2~\mathrm{k}\Omega$, $R_3=3~\mathrm{k}\Omega$, and $R_5=4~\mathrm{k}\Omega$, determine the value of R_4 such that the voltage gain $\frac{v_o}{v_{in}}=-1.8$.



Answer:

a)
$$\frac{v_o}{v_{in}} = -\frac{R_2 R_3 (R_4 + R_5)}{R_1 R_3 R_5 + R_1 R_2 (R_4 + R_5)}$$

b)
$$R_4 = 5 \text{ k}\Omega$$

Solution: KCL@V₁:
$$N_{in} - V_{i} = \frac{N_{i} - V_{2}}{R_{2}} + \frac{V_{i} - V_{0}}{R_{3}}$$
 Ideal op Amp, $\frac{V_{in}}{R_{1}} = \frac{V_{2} - V_{0}}{R_{2}}$ KCL@ V_{3} : $\frac{V_{0} - V_{3}}{R_{4}} = \frac{V_{3}}{R_{5}} = \frac{V_{3} - V_{2}}{XR_{4}} + \frac{V_{0} - V_{0}}{R_{5}} = \frac{R_{4}}{R_{5}} = \frac{V_{2}}{R_{5}} - \frac{V_{0}}{R_{5}} = \frac{V_{2}}{R_{5}} + \frac{V_{0}}{R_{5}} = \frac{V$

Non-inverting Op Amp From V2 to Vo

$$\frac{RR_{1}}{R_{2}(R_{4}+R_{5})} = -\left(\frac{R_{1}R_{5}}{R_{2}(R_{4}+R_{5})} + \frac{R_{1}}{R_{3}}\right)N_{0} = -\frac{R_{1}R_{3}R_{5} + R_{1}R_{2}(R_{4}+R_{5})}{R_{2}R_{3}(R_{4}+R_{5})}N_{0}$$

All resistors in KR KR factorout Kas

b)
$$\frac{V_0}{V_1 h} = -1.8 = -\frac{2 k_3 k (R_4 + 4 k)}{1 k_x 3 k_x 4 k_+ 1 k_x 2 k (R_4 + 4 k)} = \frac{6 (R_4 + 4) x 10^8}{[12 + 2(R_4 + 4)] x 10^8}$$

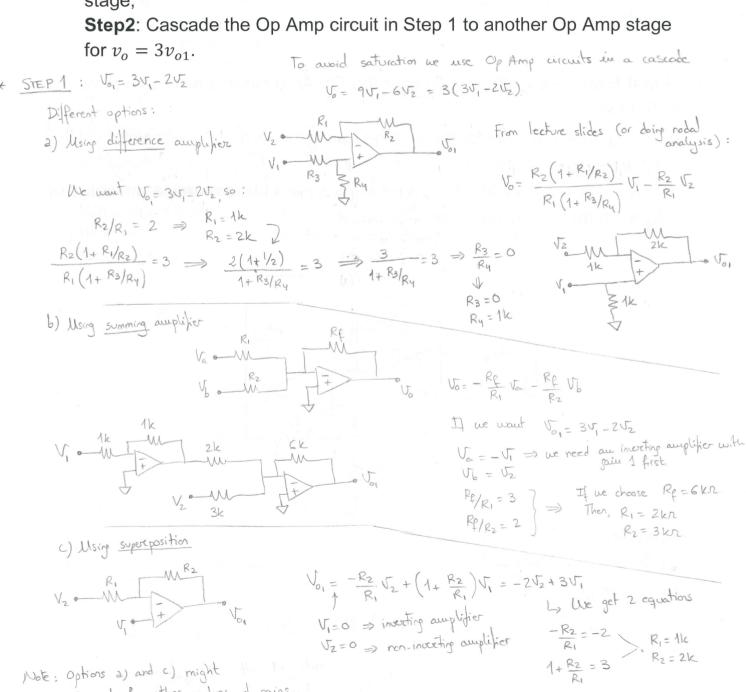
=>
$$1.08 = \frac{6R4 + 24}{12 + 2R4 + 8} \longrightarrow 36 + 3.6R4 = 6R4 + 24 \longrightarrow 12 = 2.4R4$$

7. Design a circuit with operational amplifiers that can generate the following output

$$v_o = 9v_1 - 6v_2$$

considering that the range of inputs are $1 \text{ V} \leq v_1 \leq 2 \text{ V}$ and $2 \text{ V} \leq v_2 \leq 3 \text{ V}$. The operational amplifiers have a supply voltage of $\pm 12 \text{ V}$.

Hint: Consider the impact that the supply voltage of the operational amplifier has on the output of the device and how you can design around this limitation. This means that you have to break the design in two steps: **Step1**: Design an Op Amp circuit for $v_{01} = 3v_1 - 2v_2$ as first Op amp stage,



not work for other values of gains

(i.e. the simultaneous equations might not have a solution)

Non-inverting amplifier with jain 3

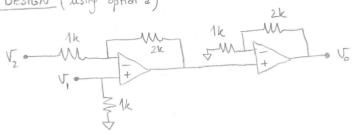


$$V_{0} = (1 + Rf/R) V_{0} = 3V_{0}$$

$$1 + Rf/R = 3 \implies Rf = 2K$$

$$R = 1k$$

* FULL DESIGN (using option 2)



Note: Using Op Amp circuits in a cascade help to make the circuit more robust, since the range of input voltages can be bigger and we usual avoid saturation. In any case a design without cascading would also work for the given rauge of

input voltages;

$$\begin{cases} 1 \vee \bot \vee V_1 \perp 2 \vee & \text{worst case scenario} \\ 2 \vee \bot \vee V_2 \perp 3 \vee & \text{(that would lead to} & \vee V_2 = 3 \vee \\ \vee V_{cc} = 12 \vee & \text{highest } \vee V_0 \end{cases}$$

$$\begin{cases} V_{cc} = 12 \vee & \text{worst case scenario} \\ \vee V_{cc} = -12 \vee & \text{highest } \vee V_0 \end{cases}$$

$$V_{cc} = -12 \vee & \text{worst case scenario} \\ V_{cc}$$